SGM48755X
High Isolation and Low Leakage 4:1 CMOS Analog Signal Multiplexer

GENERAL DESCRIPTION

The SGM48755X is a 4:1 CMOS analog signal multiplexer. It operates from 2.5V to 5.5V single power supply and all digital inputs support 1.8V logic control.

Other features include low voltage, high off-isolation and low off-leakage current. The high performances make it very suitable for multiple applications,

such as cellular phones, audio and video signal routing, etc.

The SGM48755X is available in a Green MSOP-10 package. It operates over an ambient temperature range of -40℃ to +125℃.

FEATURES

- **Single Supply Voltage Range: 2.5V to 5.5V**
- **On-Resistance: 27Ω (TYP) with 5V Supply**
- **"T" Type Switch**
- **1.8V Logic Compatible**
- **Low On-Resistance Flatness**
- **High Off-Isolation: -85dB (R_L = 50Ω, f = 1MHz)**
- **Low Off-Leakage Current: ±1μA (MAX)**
- **Low On-Leakage Current: ±1μA (MAX)**
- **Low Distortion: 0.33% (R_L = 600Ω, f = 20Hz to 20kHz)**
- **-40℃ to +125℃ Operating Temperature Range**
- **Available in a Green MSOP-10 Package**

APPLICATIONS

Automotive

Portable Equipment

Sample-and-Hold Circuits

Data-Acquisition Systems

Battery-Powered Systems

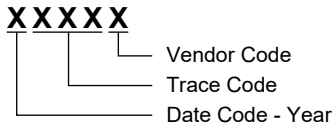
Audio and Video Signal Routing

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM48755X	MSOP-10	-40℃ to +125℃	SGM48755XMS10G/TR	SGM0G7 XMS10 XXXXX	Tape and Reel, 4000

MARKING INFORMATION

NOTE: XXXXX = Date Code, Trace Code and Vendor Code.



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

V_{CC} to GND ..... -0.3V to 6V
Voltage into Any Terminal (1) ..... -0.3V to (V_{CC} + 0.3V)
Continuous Current into Any Terminal ..... ±20mA
Peak Current (Pulsed at 1ms, 10% duty cycle) ..... ±40mA
Junction Temperature ..... +150℃
Storage Temperature Range ..... -65℃ to +150℃
Lead Temperature (Soldering, 10s) ..... 260℃
ESD Susceptibility
HBM 6000V
CDM 1000V

NOTE:
1. Internal diodes will clamp the voltage on any signal that is lower than GND. Limit the current through the forward diode to the maximum ratings.

RECOMMENDED OPERATING CONDITIONS

Supply Voltage Range ..... 2.5V to 5.5V
Operating Temperature Range ..... -40℃ to +125℃

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

ESD SENSITIVITY CAUTION

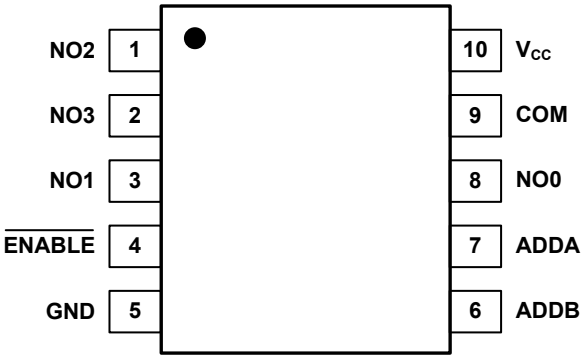
This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATION

(TOP VIEW)



MSOP-10

PIN DESCRIPTION

PIN	NAME	FUNCTION
1	NO2	Analog Switch Normally Open Input Pin 2.
2	NO3	Analog Switch Normally Open Input Pin 3.
3	NO1	Analog Switch Normally Open Input Pin 1.
4	$\overline{\text{ENABLE}}$	Digital Enable Input Pin. Drive $\overline{\text{ENABLE}}$ low or connect to GND for normal operation. Drive $\overline{\text{ENABLE}}$ high or connect to V_{CC} to turn all switches off.
5	GND	Ground.
6	ADDB	Logic-Level Address Input Pin.
7	ADDA	Logic-Level Address Input Pin.
8	NO0	Analog Switch Normally Open Input Pin 0.
9	COM	Analog Switch Common Pin.
10	V_{CC}	Positive Analog and Digital Supply Voltage Input Pin.

NOTE:
Any input pin can be used as an output pin, and any output pin can also be used as an input pin. Signal transmission in both directions is equally well.

ELECTRICAL CHARACTERISTICS

(V_{CC} = 5V, Full = -40℃ to +125℃, typical values are at T_A = +25℃, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Analog Switch							
Analog Signal Range	$V_{NO\_}$, V_{COM}		Full	GND		V_{CC}	V
On-Resistance	R_{ON}	V_{CC} = 5V, V_I = 0V to V_{CC}, I_{COM} = 1mA	+25℃		27	35	Ω
			Full			45	
On-Resistance Match between Channels	ΔR_{ON}	V_{CC} = 5V, V_I = 0V to V_{CC}, I_{COM} = 1mA	+25℃		0.6	3.5	Ω
			Full			4	
On-Resistance Flatness	$R_{FLAT(ON)}$	V_{CC} = 5V, V_I = 0V to V_{CC}, I_{COM} = 1mA	+25℃		7	12	Ω
			Full			17	
Channel Off Leakage Current	$I_{NO\_(OFF)}$	V_{CC} = 5V, $V_{NO\_}$ = 0V, V_{COM} = 5V, or $V_{NO\_}$ = 5V, V_{COM} = 0V	Full		±0.01	±1	μA
	$I_{COM(OFF)}$	V_{CC} = 5V, $V_{NO\_}$ = 0V, V_{COM} = 5V, or $V_{NO\_}$ = 5V, V_{COM} = 0V	Full		±0.01	±1	
Channel On Leakage Current	$I_{NO\_(ON)}$, $I_{COM(ON)}$	V_{CC} = 5V, V_{COM} = 5V, 0V	Full		±0.01	±1	μA
Digital I/O							
Logic Input Logic Threshold High	V_{ADDAH}, V_{ADDBH}, $V_{\overline{ENABLE}H}$		Full	1.8			V
Logic Input Logic Threshold Low	V_{ADDAL}, V_{ADDBL}, $V_{\overline{ENABLE}L}$		Full			0.5	V
Input-Current High	I_{ADDAH}, I_{ADDBH}, $I_{\overline{ENABLE}H}$	V_{ADDA}, V_{ADDB}, $V_{\overline{ENABLE}}$ = V_{CC}	Full		±0.01	±1	μA
Input-Current Low	I_{ADDAL}, I_{ADDBL}, $I_{\overline{ENABLE}L}$	V_{ADDA}, V_{ADDB}, $V_{\overline{ENABLE}}$ = 0V	Full		±0.01	±1	μA
Dynamic Characteristics							
Propagation Delay Time	t_{PD}	R_L = 300Ω, C_L = 35pF	Full	0.1	1.3	3	ns
Address Transition Time	t_{TRANS}	$V_{NO\_}$ = 3V/0V, R_L = 300Ω, C_L = 35pF, Test Circuit 1	Full	15	65	135	ns
$\overline{ENABLE}$ Turn-On Time	t_{ON}	$V_{NO\_}$ = 3V, R_L = 300Ω, C_L = 35pF, Test Circuit 2	Full	10	40	90	ns
$\overline{ENABLE}$ Turn-Off Time	t_{OFF}	$V_{NO\_}$ = 3V, R_L = 300Ω, C_L = 35pF, Test Circuit 2	Full	30	50	75	ns
Break-Before-Make Delay Time	t_D	$V_{NO\_}$ = 3V, R_L = 300Ω, C_L = 35pF, Test Circuit 3	Full	5	40	85	ns
Charge Injection	Q	R_S = 0Ω, C_L = 1nF, Test Circuit 4	+25℃		1		pC
Off-Isolation	O_{ISO}	R_L = 50Ω, C_L = 30pF, f = 1MHz, Test Circuit 5	+25℃		-85		dB
-3dB Bandwidth	BW	R_L = 50Ω, C_L = 30pF, Test Circuit 6	+25℃		210		MHz
Channel-to-Channel Crosstalk	X_{TALK}	R_L = 50Ω, C_L = 30pF, f = 1MHz, Test Circuit 7	+25℃		-75		dB
Input Off-Capacitance	$C_{NO\_(OFF)}$	$V_{NO\_}$ = 0V, f = 1MHz, Test Circuit 8	+25℃		10		pF
Output Off-Capacitance	$C_{COM(OFF)}$	$V_{COM\_}$ = 0V, f = 1MHz, Test Circuit 8	+25℃		12		pF
Output On-Capacitance	$C_{COM(ON)}$	V_I = 0V, f = 1MHz, Test Circuit 8	+25℃		20		pF
Total Harmonic Distortion	THD	R_L = 600Ω, V_I = $5V_{P-P}$, f = 20Hz to 20kHz	+25℃		0.33		%
Power Supply							
Power Supply Range	V_{CC}		Full	2.5		5.5	V
Power Supply Current	I_{CC}	V_{ADDA}, V_{ADDB}, $V_{\overline{ENABLE}}$ = V_{CC} or 0V	Full		±0.01	±1	μA

ELECTRICAL CHARACTERISTICS (continued)

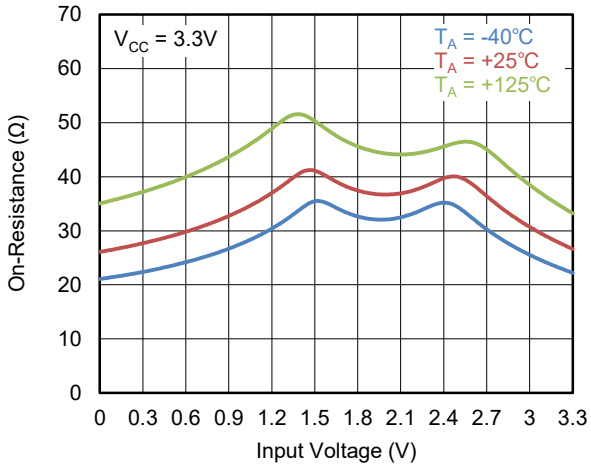
(V_{CC} = 3.3V, Full = -40℃ to +125℃, typical values are at T_A = +25℃, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Analog Switch							
Analog Signal Range	$V_{NO\_}$, V_{COM}		Full	GND		V_{CC}	V
On-Resistance	R_{ON}	V_{CC} = 3.3V, V_I = 0V to V_{CC}, I_{COM} = 1mA	+25℃		43	60	Ω
			Full			72	
On-Resistance Match between Channels	ΔR_{ON}	V_{CC} = 3.3V, V_I = 0V to V_{CC}, I_{COM} = 1mA	+25℃		0.7	3.5	Ω
			Full			4	
On-Resistance Flatness	$R_{FLAT(ON)}$	V_{CC} = 3.3V, V_I = 0V to V_{CC}, I_{COM} = 1mA	+25℃		16	26	Ω
			Full			29	
Channel Off Leakage Current	$I_{NO\_(OFF)}$, $I_{COM(OFF)}$	V_{CC} = 3.3V, $V_{NO\_}$ = 0V, V_{COM} = 3.3V, or $V_{NO\_}$ = 3.3V, V_{COM} = 0V	Full		±0.01	±1	μA
Channel On Leakage Current	$I_{NO\_(ON)}$, $I_{COM(ON)}$	V_{CC} = 3.3V, V_{COM} = 3.3V, 0V	Full		±0.01	±1	μA
Digital I/O							
Logic Input Logic Threshold High	V_{ADDAH}, V_{ADDBH}, $V_{\overline{ENABLE}H}$		Full	1.7			V
Logic Input Logic Threshold Low	V_{ADDAL}, V_{ADDBL}, $V_{\overline{ENABLE}L}$		Full			0.4	V
Input-Current High	I_{ADDAH}, I_{ADDBH}, $I_{\overline{ENABLE}H}$	V_{ADDA}, V_{ADDB}, $V_{\overline{ENABLE}}$ = V_{CC}	Full		±0.01	±1	μA
Input-Current Low	I_{ADDAL}, I_{ADDBL}, $I_{\overline{ENABLE}L}$	V_{ADDA}, V_{ADDB}, $V_{\overline{ENABLE}}$ = 0V	Full		±0.01	±1	μA
Dynamic Characteristics							
Propagation Delay Time	t_{PD}	R_L = 300Ω, C_L = 35pF	Full	0.1	1.5	4	ns
Address Transition Time	t_{TRANS}	$V_{NO\_}$ = 3V/0V, R_L = 300Ω, C_L = 35pF, Test Circuit 1	Full	20	92	225	ns
$\overline{ENABLE}$ Turn-On Time	t_{ON}	$V_{NO\_}$ = 3V, R_L = 300Ω, C_L = 35pF, Test Circuit 2	Full	15	58	160	ns
$\overline{ENABLE}$ Turn-Off Time	t_{OFF}	$V_{NO\_}$ = 3V, R_L = 300Ω, C_L = 35pF, Test Circuit 2	Full	35	66	115	ns
Break-Before-Make Delay Time	t_D	$V_{NO\_}$ = 3V, R_L = 300Ω, C_L = 35pF, Test Circuit 3	Full	10	50	145	ns
Charge Injection	Q	R_S = 0Ω, C_L = 1nF, Test Circuit 4	+25℃		0.7		pC
-3dB Bandwidth	BW	R_L = 50Ω, C_L = 30pF, Test Circuit 6	+25℃		210		MHz
Power Supply							
Power Supply Current	I_{CC}	V_{ADDA}, V_{ADDB}, $V_{\overline{ENABLE}}$ = V_{CC} or 0V	Full		±0.01	±1	μA

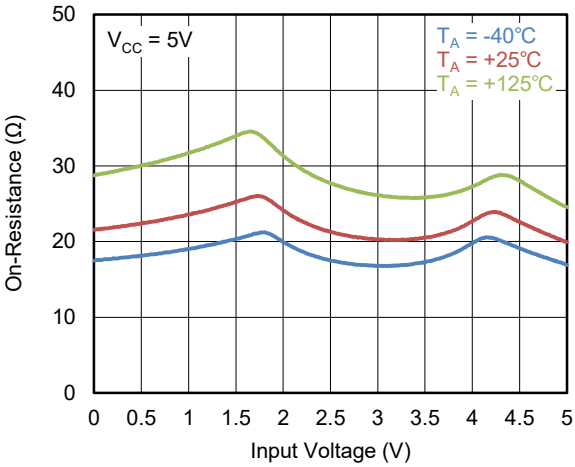
TYPICAL PERFORMANCE CHARACTERISTICS

T_A = +25℃, unless otherwise noted.

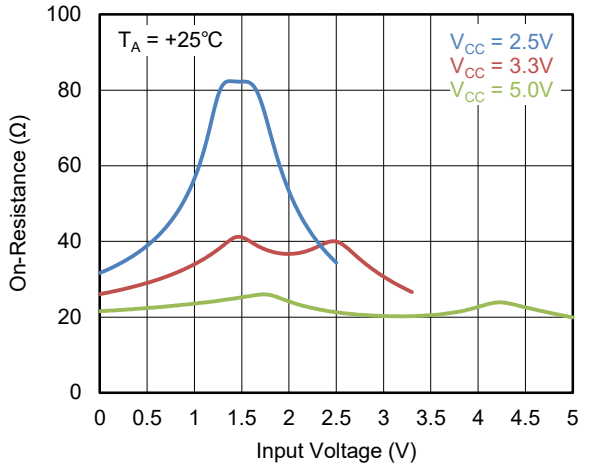
On-Resistance vs. Input Voltage



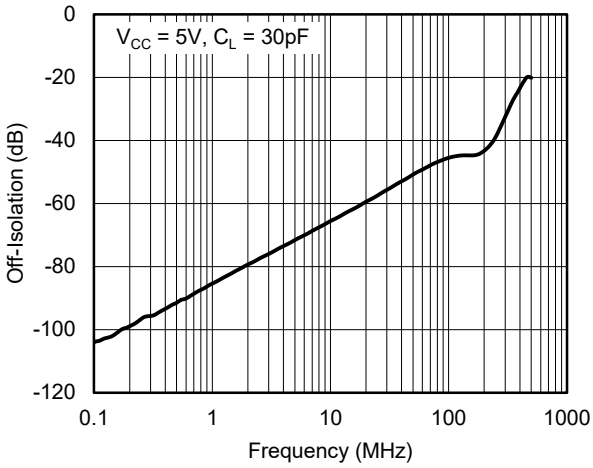
On-Resistance vs. Input Voltage



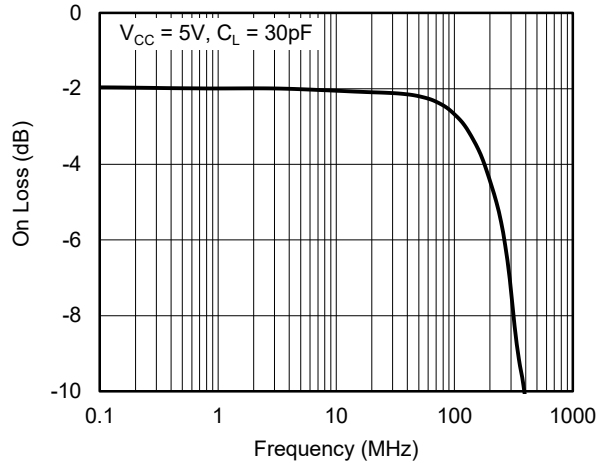
On-Resistance vs. Input Voltage



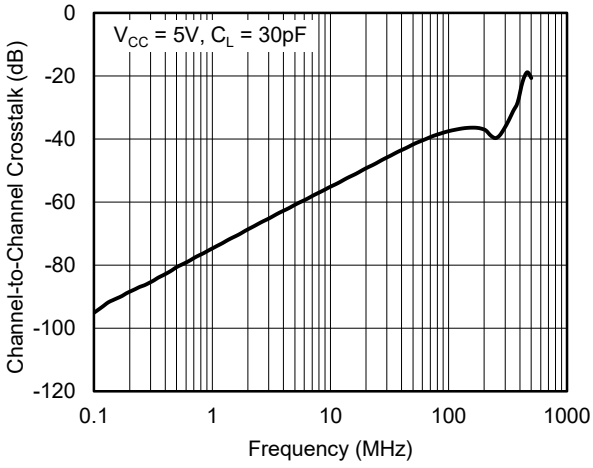
Off-Isolation vs. Frequency



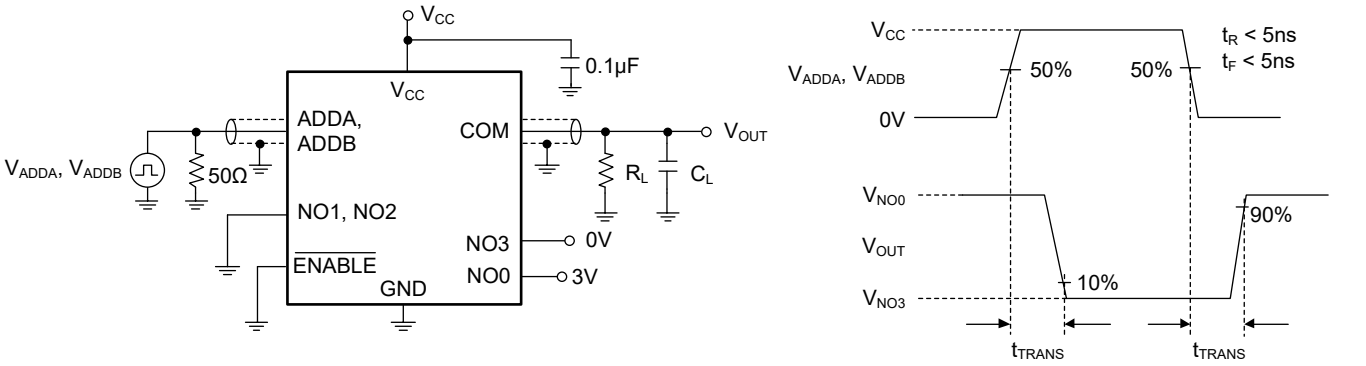
On Loss vs. Frequency



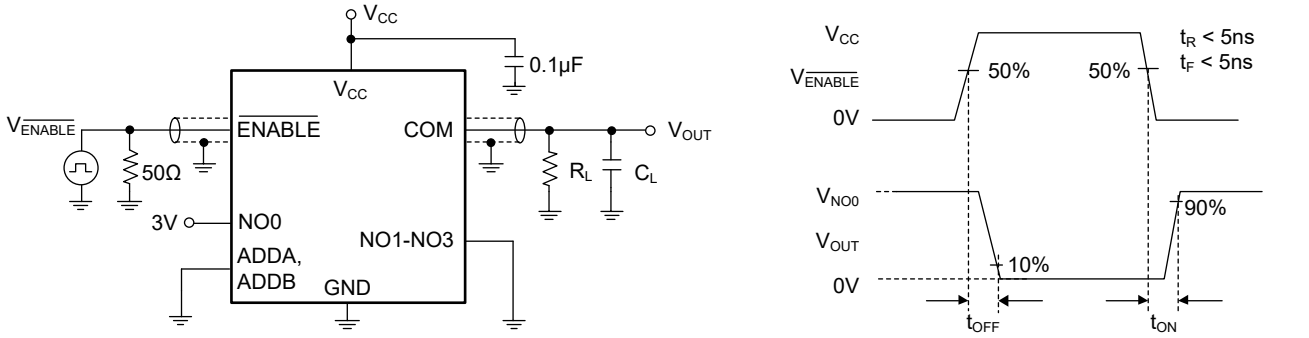
Channel-to-Channel Crosstalk vs. Frequency



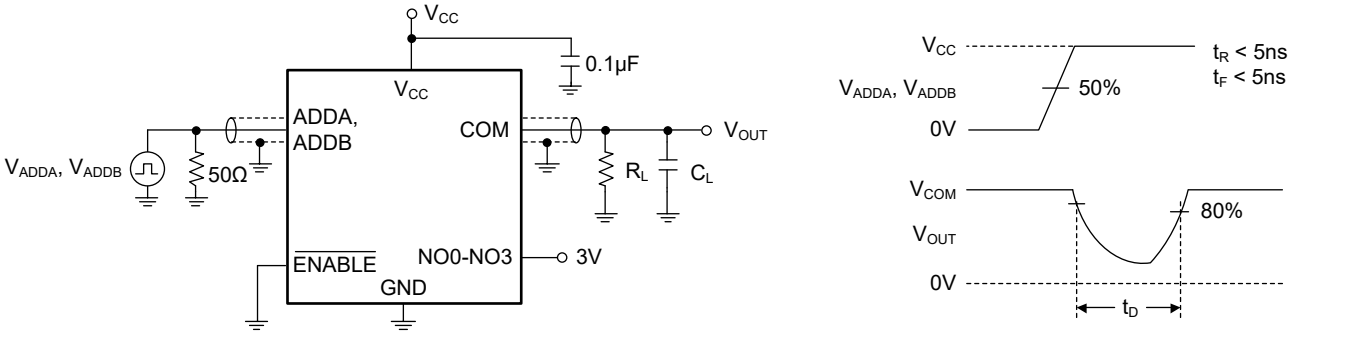
TEST CIRCUITS



Test Circuit 1. Address Transition Times (t_{TRANS})

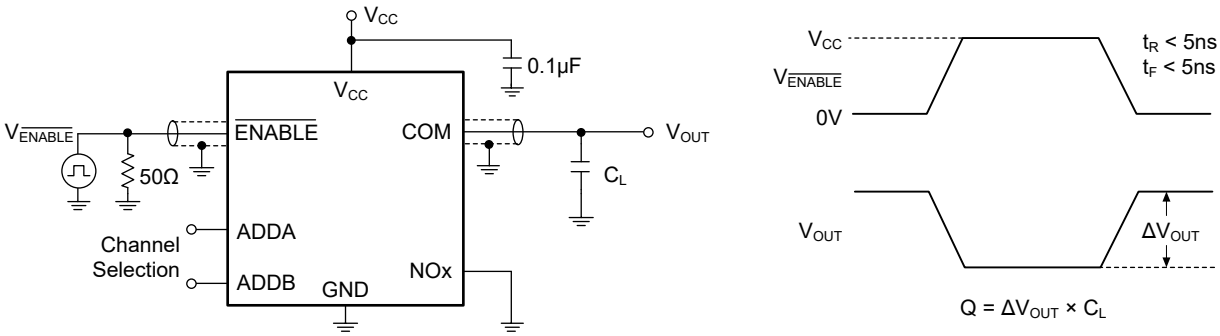


Test Circuit 2. Switching Times (t_{ON}, t_{OFF})

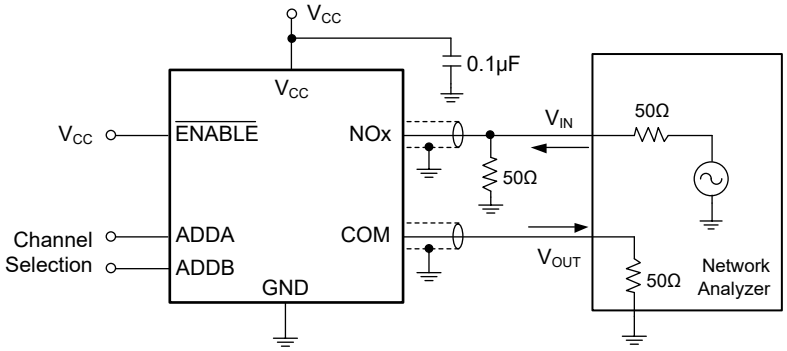


Test Circuit 3. Break-Before-Make Delay Time (t_D)

TEST CIRCUITS (continued)

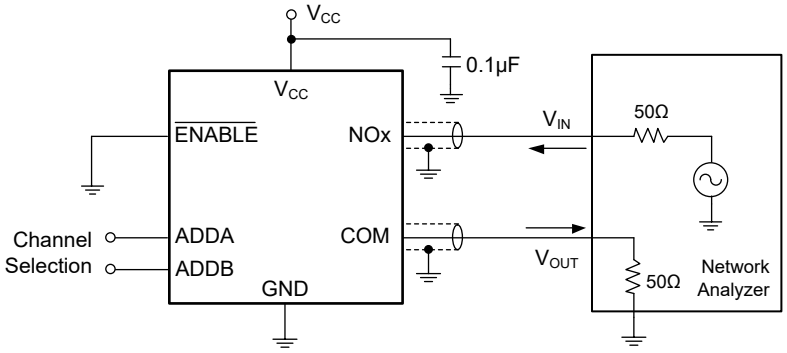


Test Circuit 4. Charge Injection (Q)



Off-Isolation = 20log (V_{OUT}/V_{IN})
Measured between COM and "OFF" NO Terminal on Each Switch.

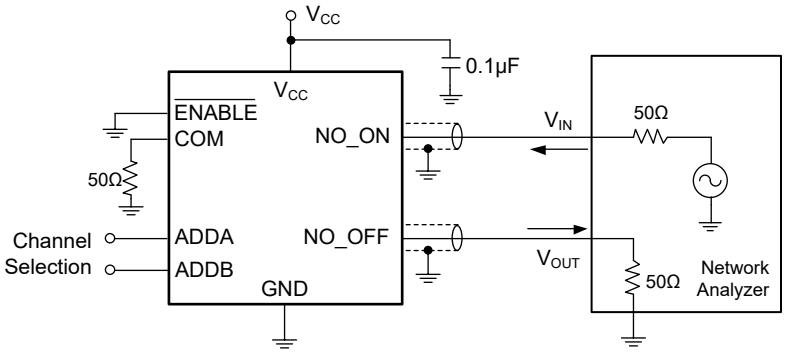
Test Circuit 5. Off-Isolation



On Loss = 20log (V_{OUT}/V_{IN})
Measured between COM and "ON" NO Terminal on Each Switch.

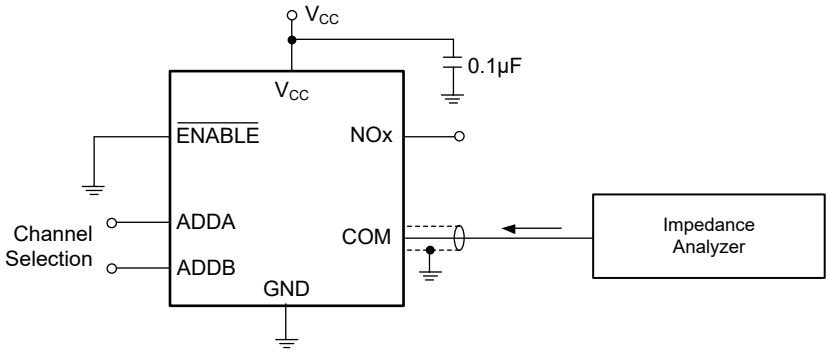
Test Circuit 6. On Loss

TEST CIRCUITS (continued)



Channel-to-Channel Crosstalk = 20log (V_{OUT}/V_{IN})
Measured from One Channel (NO_ON) to All other Channels (NO_OFF).

Test Circuit 7. Channel-to-Channel Crosstalk



Test Circuit 8. Capacitance

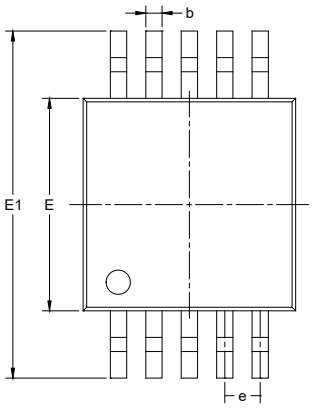
REVISION HISTORY

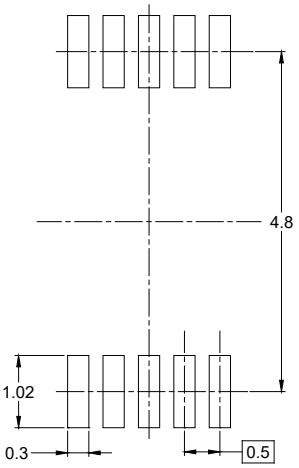
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Original (JANUARY 2024) to REV.A	Page
Changed from product preview to production data	All

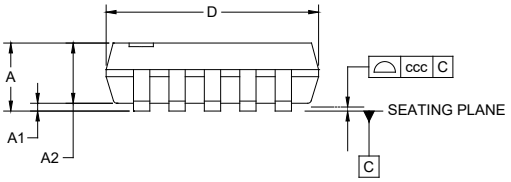
PACKAGE OUTLINE DIMENSIONS

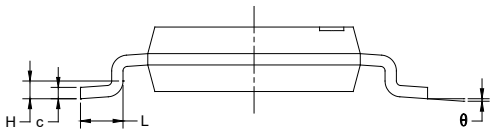
MSOP-10





RECOMMENDED LAND PATTERN (Unit: mm)



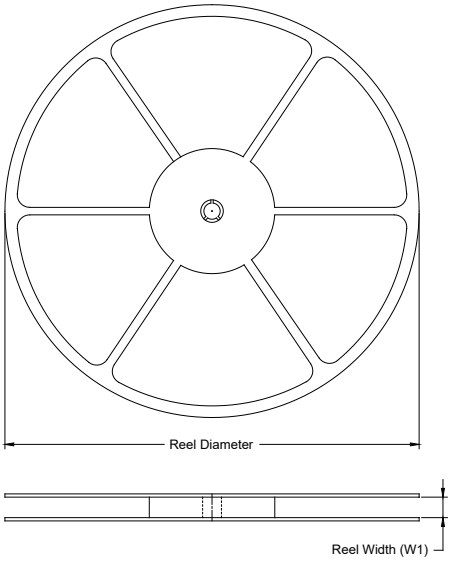


Symbol	Dimensions In Millimeters		
	MIN	MOD	MAX
A	-	-	1.100
A1	0.000	-	0.150
A2	0.750	-	0.950
b	0.170	-	0.330
c	0.080	-	0.230
D	2.900	-	3.100
E	2.900	-	3.100
E1	4.750	-	5.050
e	0.500 BSC		
H	0.250 TYP		
L	0.400	-	0.800
θ	0°	-	8°
ccc	0.100		

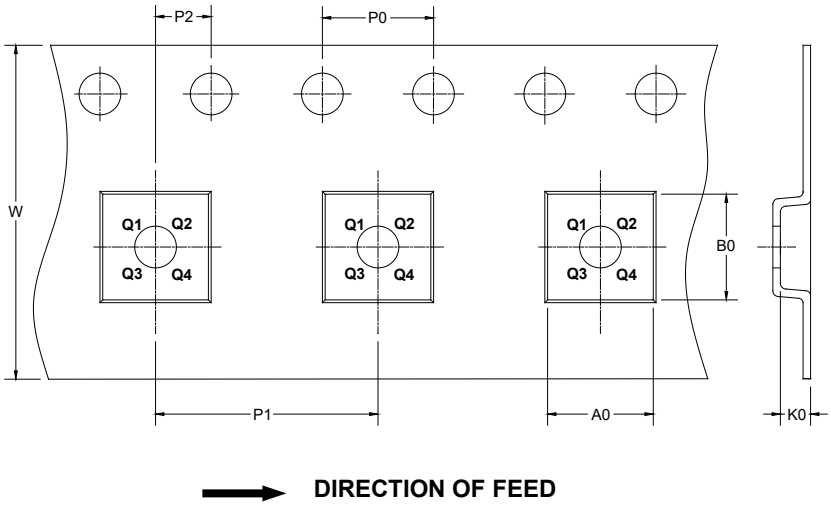
NOTES:
1. This drawing is subject to change without notice.
2. The dimensions do not include mold flashes, protrusions or gate burrs.
3. Reference JEDEC MO-187.

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



DIRECTION OF FEED

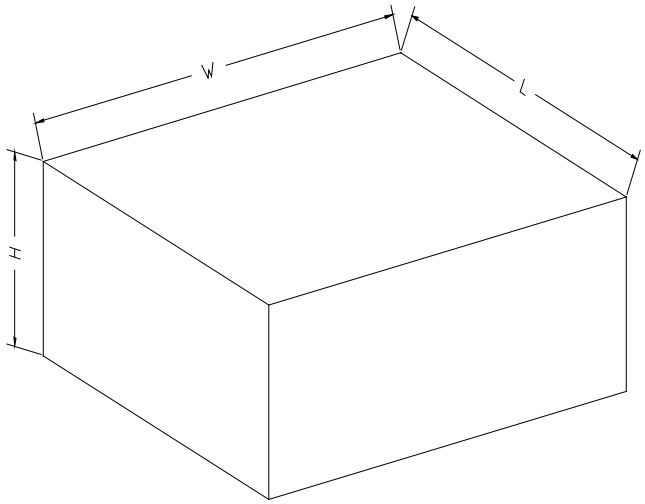
NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
MSOP-10	13"	12.4	5.20	3.30	1.50	4.0	8.0	2.0	12.0	Q1

DD0001

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13″	386	280	370	5

DD0002