SGM8199L Voltage Output, High-Side or Low-Side Measurement, Bidirectional Current Shunt Monitor

GENERAL DESCRIPTION

The SGM8199L series is a voltage output, bidirectional current shunt monitor, which is designed to sense voltage drops across the shunt at common mode voltage range without being affected by the supply voltage. The device features a wide common mode voltage range from -0.1V to 26V. It also offers a low offset, which allows 10mV full-scale maximum drops across the shunt when sensing current.

The SGM8199L series provides two fixed gains: 50V/V and 100V/V. It operates from 2.7V to 26V single supply, consuming only $85\mu A$ quiescent current.

The SGM8199L series is available in Green SC70-6 and UTQFN-1.8×1.4-10L packages. It is specified over the extended -40°C to +125°C temperature range.

APPLICATIONS

Laptops
Mobile Phones
Telecom Equipment
Power Managements
Charging Systems
Welding Equipment

FEATURES

- Wide Input Common Mode Range: -0.1V to 26V
- Low Input Offset Voltage:

SGM8199L1: $350\mu V$ (V_{CM} = 12V, MAX) SGM8199L2: $330\mu V$ (V_{CM} = 12V, MAX) (Enable 10mV Full-Scale Shunt Drops)

• Gain Options:

SGM8199L1 Gain: 50V/V SGM8199L2 Gain: 100V/V

• Gain Error:

SGM8199L1: ±0.4% (MAX) SGM8199L2: ±0.4% (MAX)

- Low Quiescent Current: 85µA (TYP)
- -40°C to +125°C Operating Temperature Range
- Available in Green SC70-6 and UTQFN-1.8×1.4-10L Packages

TYPICAL APPLICATION

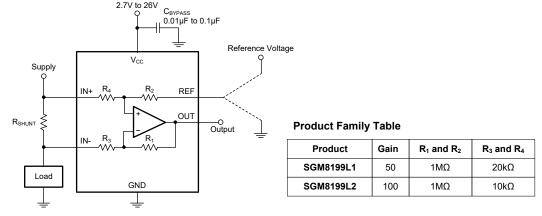


Figure 1. Recommended Application Circuit



PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	GAIN	SPECIFIED TEMPERATURE RANGE	ATURE ORDERING NUMBER		PACKING OPTION
SGM8199L1	SC70-6	50V/V	-40°C to +125°C	SGM8199L1XC6G/TR	MBCXX	Tape and Reel, 3000
3GIVIO 199L1	UTQFN-1.8×1.4-10L	50V/V	-40°C to +125°C	SGM8199L1XUWQ10G/TR	JCXX	Tape and Reel, 3000
SCM9400L2	SC70-6	100V/V	-40°C to +125°C	SGM8199L2XC6G/TR	MJDXX	Tape and Reel, 3000
SGM8199L2	UTQFN-1.8×1.4-10L	100V/V	-40°C to +125°C	SGM8199L2XUWQ10G/TR	JEXX	Tape and Reel, 3000

MARKING INFORMATION

NOTE: XX = Date Code.

SC70-6

VYY X X

Date Code - Month
Date Code - Year
Serial Number

UTQFN-1.8×1.4-10L

YY X X

Date Code - Month
Date Code - Year
Serial Number

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage28V
V _{IN} Differential±28V
Input Common Mode Voltage RangeGND - 0.3V to 28V
Input Current into All Pins5mA
Junction Temperature+150°C
Storage Temperature Range65°C to +150°C
Lead Temperature (Soldering, 10s)+260°C
ESD Susceptibility
HBM3000V
MM200V
CDM2000V

RECOMMENDED OPERATING CONDITIONS

Operating Temperature Range-40°C to +125°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods

may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

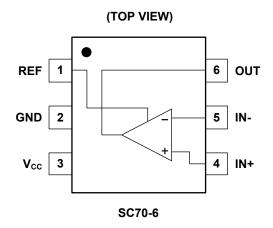
ESD SENSITIVITY CAUTION

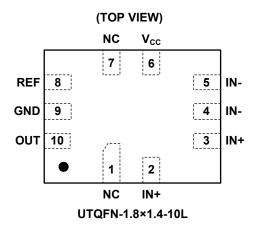
This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATIONS





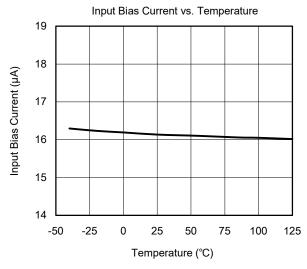
ELECTRICAL CHARACTERISTICS

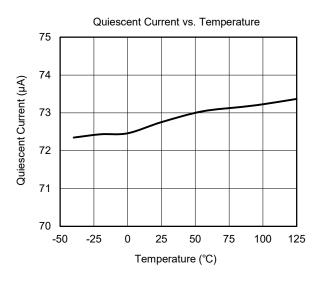
 $(V_{CC} = 2.7 \text{V to 26V}, V_{IN+} = 12 \text{V}, V_{SENSE} = V_{IN+} - V_{IN-}, \text{ and } V_{REF} = V_{CC}/2, \text{ Full} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}, \text{ typical values are at } T_A = +25 ^{\circ}\text{C}, \text{ unless otherwise noted.})$

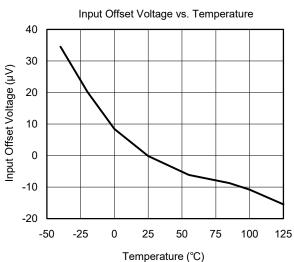
unless otherwise noted.) PARAMETER		CONDITIONS			MIN	TYP	MAX	UNITS
Input Characteristics								
,		$V_{CM} = -0.1V$ to 2V, $V_{SENSE} = 0$ mV				150	720	
		V _{CM} = 2V to 26V, V _{SENSE} = 0mV	SGM8199L1			150	350	
Input Offset Voltage, RTI (1) (V _{OS})		$V_{CM} = -0.1V$ to 2V, $V_{SENSE} = 0$ mV		+25°C		70	700	μV
		V_{CM} = 2V to 26V, V_{SENSE} = 0mV	SGM8199L2			80	330	
Input Offset Voltage Drift (ΔV _{OS} /Δ	T)			Full		1		μV/°C
Input Bias Current (I _B)	,	V _{SENSE} = 0mV				16		μA
Input Offset Current (I _{OS})		V _{SENSE} = 0mV		+25°C		35		nA
Input Common Mode Voltage Rar	nge (V _{CM})			Full	-0.1		26	V
-	0 (5,	V _{CM} = -0.1V to 26V, V _{SENSE} = 0mV			85	104		
		$V_{CM} = 2V \text{ to } 26V, V_{SENSE} = 0mV$	- SGM8199L1		87	100		
Common Mode Rejection Ratio (0	CMRR)	V_{CM} = -0.1V to 26V, V_{SENSE} = 0mV		Full	90	110		dB
		$V_{CM} = 2V \text{ to } 26V, V_{SENSE} = 0mV$	- SGM8199L2		87	104		
Output Characteristics		TOWN 2 1 12 20 1, 1 SENGE 2 1111				1		
Gain (G)			SGM8199L1			50		
			SGM8199L2	+25°C		100		V/V
Gain Error		V _{SENSE} = -5mV to 5mV	SGM8199L1	Full		0.02	0.4	
			SGM8199L2			0.05	0.4	%
Gain Temperature Coefficient						10	0.4	ppm/°C
·		V _{SENSE} = -5mV to 5mV				0.01		%
Nonlinearity Error Maximum Capacitive Load		No sustained oscillation				1		nF
IVIAAIITIUITI Capacitive Load	V _{OH}	NO Sustained Oscillation		+25°C		140	380	
Output Voltage Swing from Rail	V _{OL}	V_{CC} = 26V, R _L = 10k Ω to $V_{CC}/2$				85	300	mV
Dynamic Performance	VOL					00	300	
Dynamic renormance			SGM8199L1			74		
-3dB Bandwidth		$V_{CC} = 5V$, $R_L = 10k\Omega$, $C_L = 10pF$	SGM8199L2	+25℃		68		kHz - - V/μs
		$f = 1kHz$, $R_L = 10k\Omega$, $V_{OUT} = 1V_{P-P}$	36101019912			0.15		
		$f = 1kHz, R_L = 10k\Omega, V_{OUT} = 1V_{P-P}$	SGM8199L1			0.13		
Slew Rate (SR)		$f = 1kHz, R_L = 10k\Omega, V_{OUT} = 2V_{P-P}$ $f = 1kHz, R_L = 10k\Omega, V_{OUT} = 1V_{P-P}$		+25°C		0.33		
			SGM8199L2					
Noise, RTI (1)		$f = 1kHz$, $R_L = 10k\Omega$, $V_{OUT} = 2V_{P-P}$				0.3		
Noise, KTI			CCM040014					1
Voltage Noise Density (e _n)		f = 1kHz	SGM8199L1	+25°C		50		nV/√ Hz
Power Supply			SGM8199L2			35	<u> </u>	<u> </u>
				F	0.7	1	00	
Operating Voltage Range (V _{CC})				Full	2.7	0.5	26	V
Quiescent Current (I _Q)		V _{CC} = 26V, V _{SENSE} = 0mV		+25°C		85	120	μΑ
D 0)DD)	V 0.7V/4-00V/V 0.3V		Full +25°C		405	130	- 15
Power Supply Rejection Ratio (PSRR)		V_{CC} = 2.7V to 26V, V_{SENSE} = 0mV				105		dB

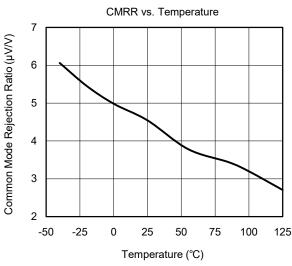
NOTE: 1. RTI = Referred-to-input.

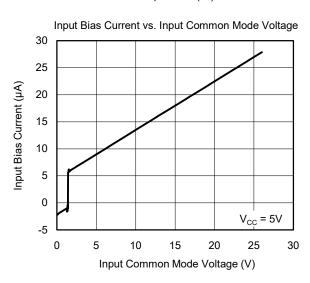
TYPICAL PERFORMANCE CHARACTERISTICS

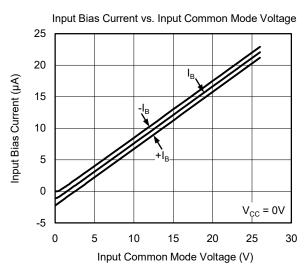




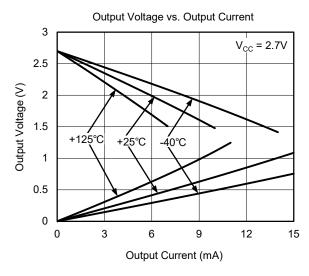


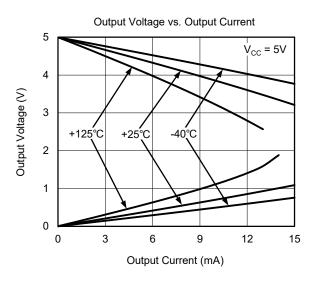


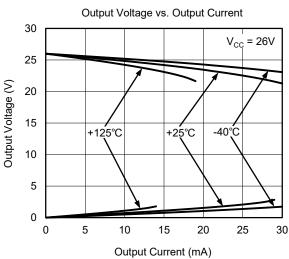


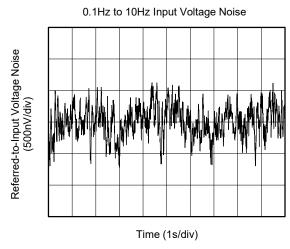


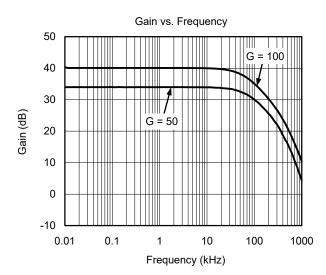
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

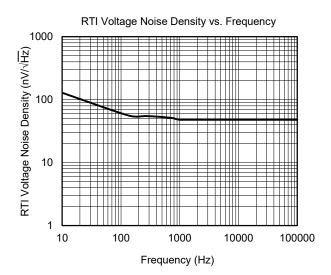




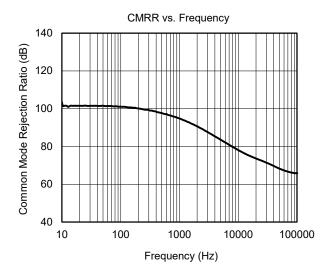


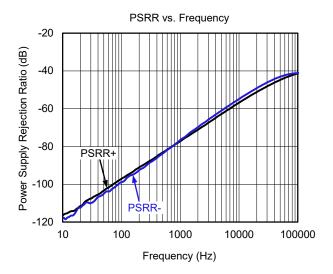


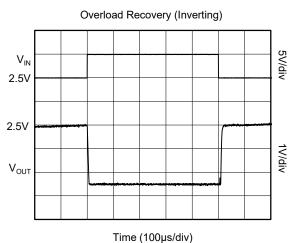


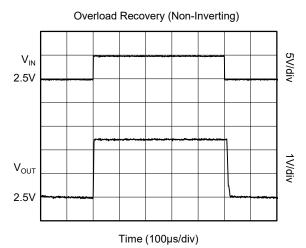


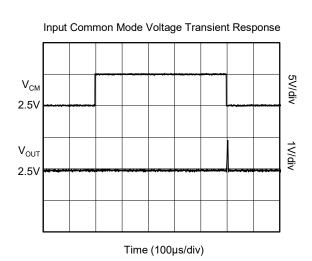
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

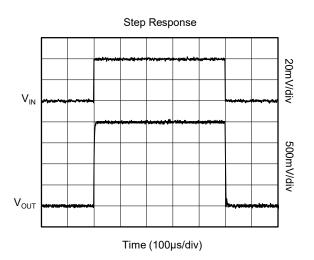




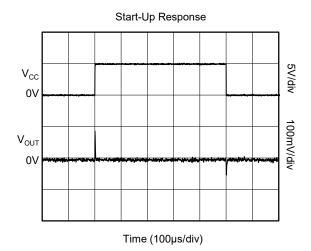


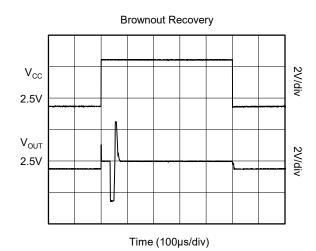






TYPICAL PERFORMANCE CHARACTERISTICS (continued)





APPLICATION INFORMATION

Recommended Connection

The recommended connection of SGM8199L is shown in Figure 1. Also, it is recommended that the shunt resistor should be placed as close as possible to the two input pins of the current shunt monitor, so the extra resistance which is series with the measured shunt resistor will be reduced.

The bypass capacitor is necessary for the current shunt monitor to improve its stability. In most applications, the power supply is noisy and it will affect the operation of SGM8199L, so the bypass capacitor is needed in this case.

On the UTQFN-1.8×1.4-10L package, two pins are provided for each input. These pins should be tied together (that is, tie IN+ to IN+ and tie IN- to IN-).

Power Supply

The SGM8199L can accurately measure the current when the common mode voltage exceeds the power supply voltage presented at the $V_{\rm CC}$ pin. For example, the $V_{\rm CC}$ power supply can be 5V and the load or common mode power supply voltage is allowed to reach up to 26V. The output voltage range is limited by the level of the power supply.

Selection of R_{SHUNT}

For the typical range of the differential input, the current shunt monitor of SGM8199L can work accurately with the order of 10mV. The application of SGM8199L series will determine the selection of the shunt resistor $R_{\text{SHUNT}}.$ Also, the users should consider the trade-off between voltage loss and the accuracy of small input signals. The effect of offset can be minimized by using high values of $R_{\text{SHUNT}},$ while the voltage loss can be minimized by using low values of $R_{\text{SHUNT}}.$ For most applications, a voltage drop of 60mV over R_{SHUNT} is the appropriate range for the selection of $R_{\text{SHUNT}},$ and the corresponding offset voltage is 350µV only.

Unidirectional Application

In unidirectional operation, the current measuring direction of SGM8199L is fixed. Usually, the REF pin is connected directly to GND pin to ensure that the output is biased at 0V. Also, if the users desire to measure low input voltage with high accuracy, biasing REF pin to 300mV is a good method to set the current shunt monitor to its linear region.

For the least frequent case, REF pin can connect to the

power supply pin directly to measure the negative current. In this case, the output voltage is equal to the voltage of power supply when input voltage is equal to 0mV.

Bidirectional Application

Bidirectional application indicates that the current shunt monitor of SGM8199L can measure the current for two different directions. For this special case, the REF pin can be set anywhere between 0V to V_{CC} for the bias of output voltage. For simplification, it is recommended that $V_{REF} = V_{CC}/2$ is a typical voltage point. However, if the absolute values of positive and negative current are not equal, the voltage of REF pin should be set other than $V_{CC}/2$.

Input Filtering

It is not recommended to add a filter at the output of SGM8199L, as doing so increases impedance seen at the output of the internal buffer. Filtering at the input pins will be a good choice as long as the change of the input impedance is taken in account. Figure 2 indicates the application of input filtering.

For minimizing the error and enhancing the accuracy of the result, the resistance of the external resistor $R_{\rm S}$ should be less than $10\Omega.$ At the internal input structure of SGM8199L, there is a bias network which causes mismatch in the bias current $I_{\rm B}$ of two input pins, and the external resistors result in the mismatch of voltage drops (because of the mismatch of $I_{\rm B}$) across $R_{\rm S}$ so that the differential error will be created. Also, this differential error will be reflected to the input pins of the current shunt monitor and result in the accuracy. However, the bias current difference can result in little effect on the operation of the current shunt monitor so the users do not need to care about that.

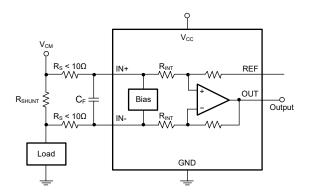


Figure 2. Filtering at Input

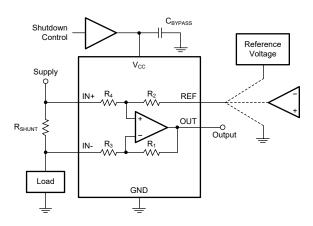


APPLICATION INFORMATION (continued)

Shutting Down SGM8199L

There is no shutdown control at the internal of SGM8199L, so the only way to shut down the quiescent current from the power supply is by using an external logic gate or transistor switch.

However, the users may concern about the amount of current through SGM8199L in shutdown mode. The schematic in Figure 3 can be used to evaluate the amount of current drain in shutdown mode.



Product Family Table

Product	Gain	R ₁ and R ₂	R ₃ and R ₄	
SGM8199L1	50	1ΜΩ	20kΩ	
SGM8199L2	100	1ΜΩ	10kΩ	

NOTE: $1M\Omega$ path from shunt inputs to reference and SGM8199L output.

Figure 3. Shutting Down the Current Shunt Monitor with $V_{REF} = 0$

Input Impedance of REF Input

The common mode rejection ratio (CMRR) can be influenced by the input impedance of REF Pin, but the users do not need to concern about it if REF pin is driven by a power supply. However, if the REF pin is driven by resistive dividers, it should be buffered by an operational amplifier to provide low input impedance.

If the output can be measured differentially, for example, a differential analog-to-digital converter, the users do not need to care about the external impedance which is added at the REF pin and the effect can be removed. Figure 4 indicates an example about how to remove the effect of extra impedance at REF pin.

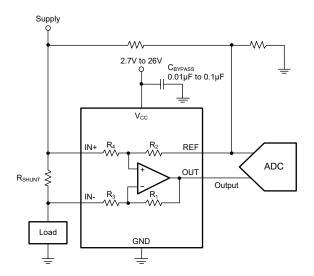


Figure 4. Cancelling the Input Impedance at REF Input

APPLICATION INFORMATION (continued)

The Performance of SGM8199L when Common Mode Transients above 26V

The SGM8199L can be powered by the power supply whose power-on transient is higher than 26V, especially for the application of automotive industry. At this case, a Zener diode or Zener-type transient absorber (Transzorbs) can be used to prevent the current shunt monitor from over-voltage during the transient of power-on. It does not recommend the users to use the transient absorber other than Transzorbs because of the significant time delay. However, any Zener diode needs extra operational resistors to provide the operating current, so the 10Ω resistors are selected (any large external resistor will affect the gain). Also, 10Ω resistors and a Zener diode with lowest rating of power are enough for handling the short-term transient in most applications.

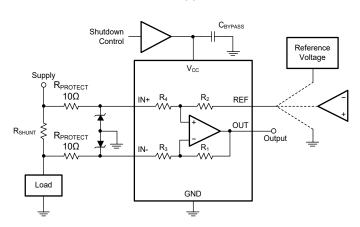


Figure 5. Using Dual Diodes for Absorbing Supply Transient

If the low power Zener diode cannot protect the current shunt monitor from supply transient, a Transzorb with high power must be taken into account in this case. In Figure 6, for saving the space of PCB board, a Transzorb and back-to-back diode can also be used to absorb the transient. For the applications of Figure 5 and Figure 6, the total area which is occupied by SGM8199L itself and its protective components is equivalent to the MSOP-8 package, and smaller than SOIC-8 package.

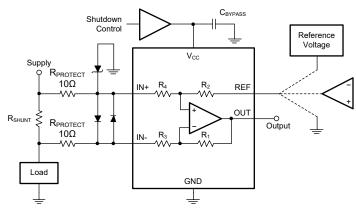


Figure 6. Using Transzorb and Input Clamps for Absorbing Supply Transient

SGM8199L

Voltage Output, High-Side or Low-Side Measurement, Bidirectional Current Shunt Monitor

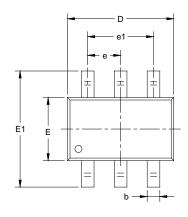
REVISION HISTORY

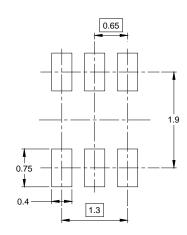
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

MAY 2023 - REV.A.1 to REV.A.2	Page
Updated Package Outline Dimensions section	13
OCTOBER 2020 – REV.A to REV.A.1	Page
Updated Typical Performance Characteristics section	
Changes from Original (SEPTEMBER 2018) to REV.A	Page
Changed from product preview to production data	All

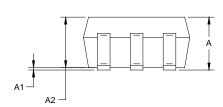


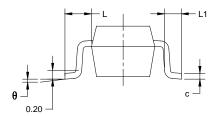
PACKAGE OUTLINE DIMENSIONS SC70-6





RECOMMENDED LAND PATTERN (Unit: mm)



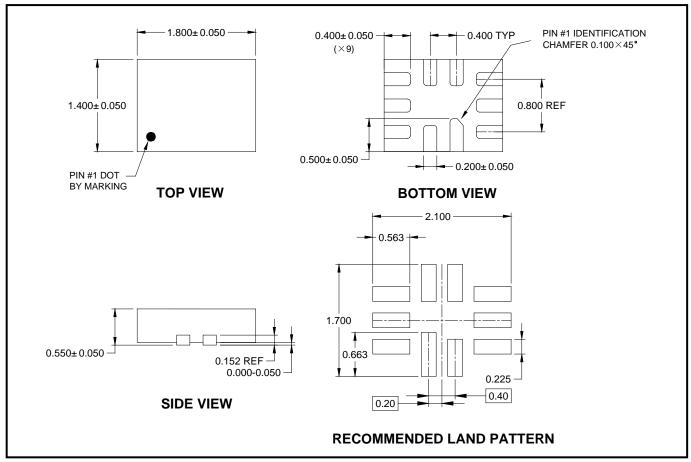


Symbol	_	nsions meters	Dimensions In Inches			
	MIN	MAX	MIN	MAX		
Α	0.800	1.100	0.031	0.043		
A1	0.000	0.100	0.000	0.004		
A2	0.800	1.000	0.031	0.039		
b	0.150	0.350	0.006	0.014		
С	0.080	0.220	0.003	0.009		
D	2.000	2.200	0.079	0.087		
E	1.150	1.350	0.045	0.053		
E1	2.150	2.450	0.085	0.096		
е	0.65 TYP		0.026 TYP			
e1	e1 1.300 BSC		0.051 BSC			
L	0.525	REF	0.021	REF		
L1	0.260	0.460	0.010	0.018		
θ	0°	8°	0°	8°		

NOTES:

- 1. Body dimensions do not include mode flash or protrusion.
- 2. This drawing is subject to change without notice.

PACKAGE OUTLINE DIMENSIONS UTQFN-1.8×1.4-10L

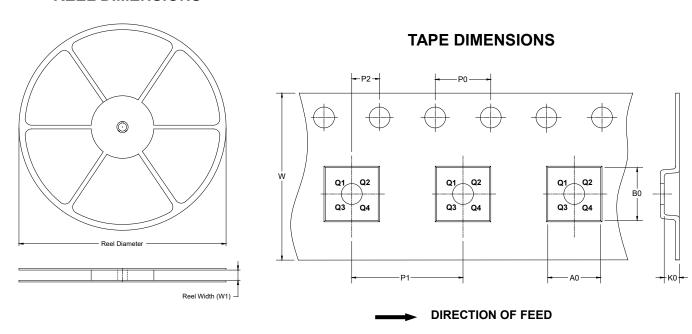


NOTES:

- 1. All linear dimensions are in millimeters.
- 2. This drawing is subject to change without notice.

TAPE AND REEL INFORMATION

REEL DIMENSIONS

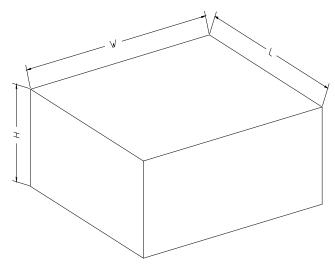


NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SC70-6	7"	9.5	2.40	2.50	1.20	4.0	4.0	2.0	8.0	Q3
UTQFN-1.8×1.4-10L	7"	9.0	1.75	2.10	0.70	4.0	4.0	2.0	8.0	Q1

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18