

SGM6031 400nA Ultra-Low Power, Buck Converter with 200mA Output Current

GENERAL DESCRIPTION

The SGM6031 family is a 200mA, 1.4MHz synchronous Buck DC/DC converter with 400nA ultra-low quiescent current, which is ideal for powering ultra-low power applications with special needs.

The SGM6031 operation voltage range is from 1.8V to 5.5V, allowing the use of a regulated 5V input. The SGM6031 is available in both adjustable and fixed output voltage versions. The output voltage of SGM6031 can be programmed by an external resistor divider in adjustable version.

The SGM6031 delivers an output current of 200mA with a peak inductor current of 430mA. Besides, the SGM6031 series has the under-voltage lockout (UVLO) function. It is disabled when the voltage on VIN reaches the UVLO condition. The SGM6031's under-voltage lockout level is continuously monitored. Only the inductor, C_{IN} and C_{OUT} capacitors are needed as external components to make a Buck DC/DC converter solution.

The SGM6031 is available in a Green UTDFN-1.5×2-6L package.

FEATURES

- Input Voltage Range: 1.8V to 5.5V
- Fixed Output Voltages: 1.0V, 1.2V, 1.5V, 1.8V, 2.5V, 2.8V, 3.0V and 3.3V
- Adjustable Output Voltages: 1.0V to 3.3V
- High Efficiency at Low Output Currents:
 Up to 90% with I_{OUT} = 0.1mA
- Ultra-Low Power Buck Converter
- 200mA (MAX) Output Current
- 400nA (TYP) Quiescent Current
- 100% Duty Cycle (Pass Mode)
- -40°C to +85°C Operating Temperature Range
- Available in a Green UTDFN-1.5×2-6L Package

APPLICATIONS

Energy Harvest Devices

Ultra-Low Power Applications

Low-Power Wireless Monitoring

Backup Power Supply Circuits

2-Cell and 3-Cell Alkaline-Powered Systems

Portable Game Consoles

Wearable Devices

Thermal Electric Generator Harvesting

Wireless Sensor Networks

Smart Building Controls

Environmental Monitoring

PACKAGE/ORDERING INFORMATION

MODEL	V _{OUT} (V)	PACKAGE DESCRIPTION	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM6031-1.0	1.0	UTDFN-1.5×2-6L	SGM6031-1.0YUDT6G/TR	GJC XXX	Tape and Reel, 3000
SGM6031-1.2	1.2	UTDFN-1.5×2-6L	SGM6031-1.2YUDT6G/TR	GJD XXX	Tape and Reel, 3000
SGM6031-1.5	1.5	UTDFN-1.5×2-6L	SGM6031-1.5YUDT6G/TR	GJE XXX	Tape and Reel, 3000
SGM6031-1.8	1.8	UTDFN-1.5×2-6L	SGM6031-1.8YUDT6G/TR	GJF XXX	Tape and Reel, 3000
SGM6031-2.5	2.5	UTDFN-1.5×2-6L	SGM6031-2.5YUDT6G/TR	GK0 XXX	Tape and Reel, 3000
SGM6031-2.8	2.8	UTDFN-1.5×2-6L	SGM6031-2.8YUDT6G/TR	GK1 XXX	Tape and Reel, 3000
SGM6031-3.0	3.0	UTDFN-1.5×2-6L	SGM6031-3.0YUDT6G/TR	GX0 XXX	Tape and Reel, 3000
SGM6031-3.3	3.3	UTDFN-1.5×2-6L	SGM6031-3.3YUDT6G/TR	GK2 XXX	Tape and Reel, 3000
SGM6031-ADJ	ADJ	UTDFN-1.5×2-6L	SGM6031-ADJYUDT6G/TR	GLF XXX	Tape and Reel, 3000

MARKING INFORMATION

NOTE: XXX = Date Code.

YYY— Serial Number

XXX

Date Code - Week

Date Code - Year

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

Input Voltage Range on VIN, EN, VOUT, FB, S	SW
	0.3V to 6V
Peak Currents VIN, VOUT	510mA
Typical Thermal Resistance	
UTDFN-1.5×2-6L, θ _{JA}	130°C/W
Junction Temperature	+150°C
Storage Temperature Range6	5°C to +150°C
Lead Temperature (Soldering, 10s)	+260°C
ESD Susceptibility	
HBM	7000V
MM	400V
CDM	1000V

RECOMMENDED OPERATING CONDITIONS

Input Voltage Range1.8V to 5.5V
Input Capacitance, C _{IN} 22µF (MIN)
Output Capacitance, C _{OUT} 10µF (MIN), 22µF (TYP)
Inductance, L
Operating Ambient Temperature Range40°C to +85°C
Operating Junction Temperature Range40°C to +125°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

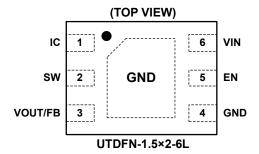
ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATION



PIN DESCRIPTION

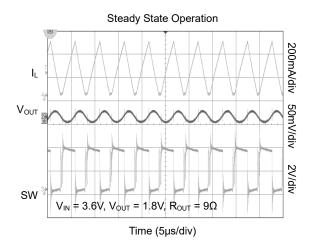
PIN	NAME	FUNCTION
1	IC	For Internal Connection.
2	SW	Switching Node. Connect to output inductor.
_	VOUT	Buck Regulator Output.
3 FB		Feedback Input (adjustable voltage version only). The voltage at this pin is regulated to 1.0V. Connect to the resistor divider between output and ground to set output voltage.
4	GND	Ground. Power and IC ground. All signals are referenced to this pin.
5	EN	Enable Input. Input logic high to enable this circuit and logic low to shut down. Do not leave this pin unconnected. EN is recommended to be effective 10ms later than VIN.
6	VIN	Input Voltage. Connect to input power source.
Exposed Pad	GND	Connect to GND.

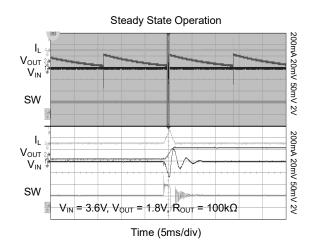
ELECTRICAL CHARACTERISTICS

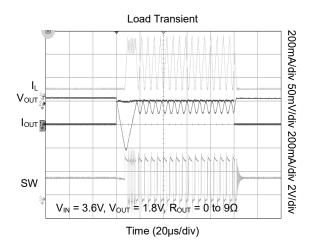
 $(V_{IN} = 3.6V, V_{OUT} = 1.2V. Full = -40^{\circ}C$ to +85°C, typical values are at $T_A = +25^{\circ}C$, unless otherwise noted.)

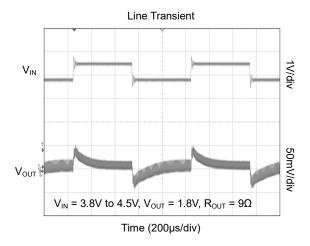
PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Quiescent Current	•						•
Buck Enabled State	IQ	V _{IN} = 1.8V, no load, no switching	+25°C		400	700	nA
Output							
Feedback Voltage	V _{FB}		+25°C	0.98	1.0	1.02	V
Output Accuracy			+25°C	-2		2	%
Output Accuracy			Full	-3		3	70
Output Line Regulation		V_{IN} = 1.8V to 5.5V, I_{OUT} = 100 μ A	+25°C		0.3		%/V
Output Load Regulation		I _{OUT} = 100μA to 200mA	+25°C		0.002		%/mA
Output Ripple		I _{OUT} = 1mA	+25°C		15		mV_{PP}
Power Switch							
High-side Switch On-Resistance	RDSON		+25°C		510	620	mΩ
Low-side Switch On-Resistance	RDSON		+25°C		530		11122
Cycle-by-Cycle Current Limit	I _{LIM}		+25°C	300	430	510	mA
Maximum Switching Frequency	f _{SW}		+25°C		1.4		MHz
Input							
Input Under Voltage Protection	$V_{\text{IN_UVLO}}$	V _{IN} falling	+25°C	1.19	1.3		V
EN							
Voltage for EN High Setting	V _{IH}		Full	1.1			V
Voltage for EN Low Setting	V _{IL}		Full			0.4]
Thermal Shutdown							
Thermal Shutdown	T _{TSD}				160		°C
Thermal Shutdown Hysteresis	T _{HYS}				20		°C

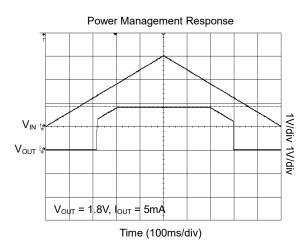
TYPICAL PERFORMANCE CHARACTERISTICS

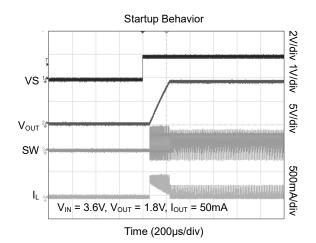




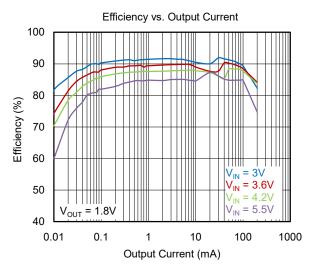


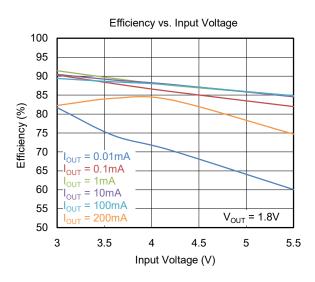


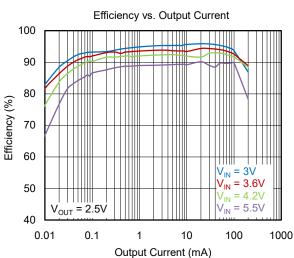


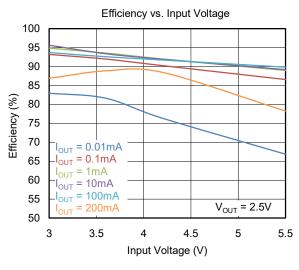


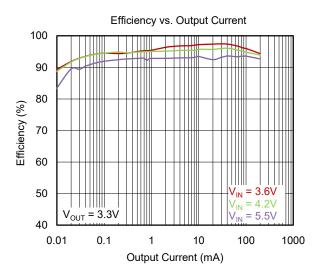
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

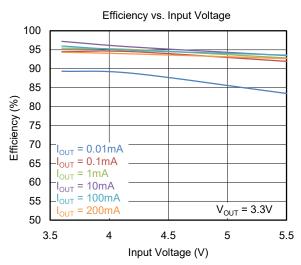




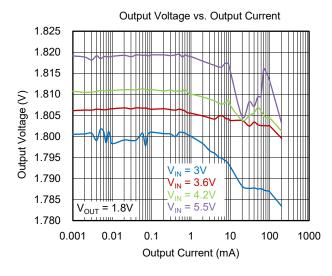


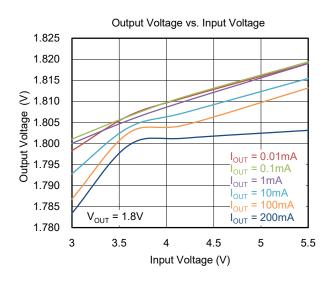


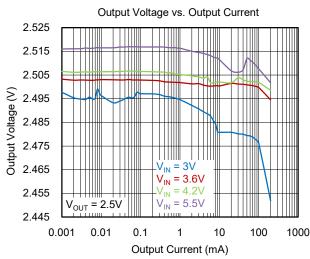


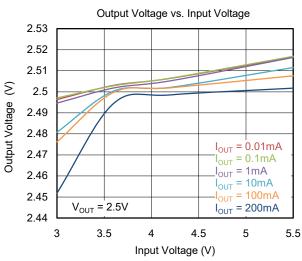


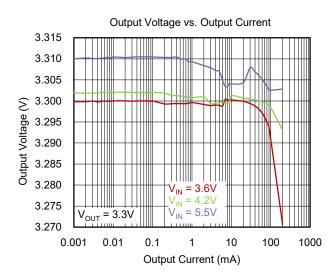
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

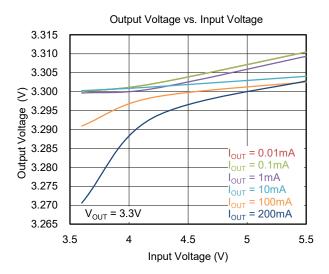




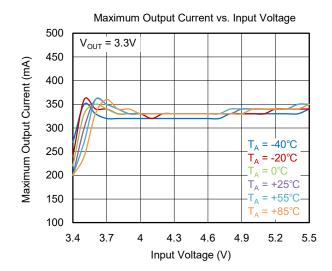


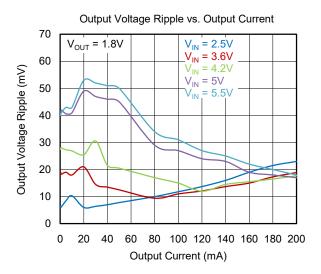






TYPICAL PERFORMANCE CHARACTERISTICS (continued)





FUNCTIONAL BLOCK DIAGRAM

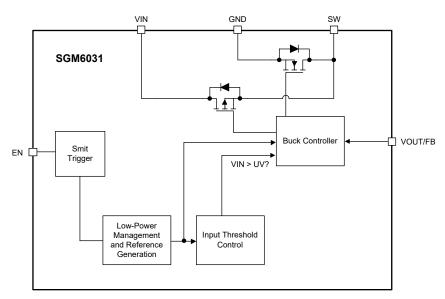


Figure 1. Block Diagram

TYPICAL APPLICATION CIRCUITS

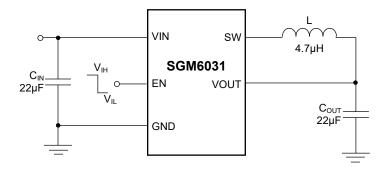


Figure 2. Fixed Voltage Typical Application Circuit

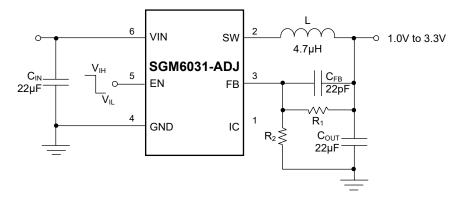


Figure 3. Adjustable Voltage Typical Application Circuit

DETAILED DESCRIPTION

The SGM6031 family is a synchronous Buck converter with ultra-low quiescent current of 400nA (TYP). The device is improved to provide high efficiency ranging from 10µA to 200mA.

The SGM6031 implements the hysteretic control architecture to regulate the output voltage. The 400nA (TYP) quiescent current extends the operation lifetime for battery operated applications.

Operation

The SGM6031 family is a Buck converter that is capable to regulate the voltage that is lower than the input voltage at the output. The device implements the pulse frequency modulation (PFM) control to regulate the voltage at light load. The SGM6031 delivers an average output current of 200mA with a peak inductor current of 430mA (TYP). The device implements an input under-voltage lockout (UVLO) function. When the input voltage drops below the UVLO threshold, the device stops operation. In addition to input UVLO function, the SGM6031 also implements the EN pin to allow external signal to control the turn-on and turn-off of the device. The device also implements output discharge function, when the EN pin is pulled to logic low, the low-side power FET remains on to discharge the output voltage. The SGM6031 also implements 100% duty cycle mode to bypass the input voltage to the

output when the input voltage is above the UVLO threshold.

Under-Voltage Lockout (UVLO)

The device monitors the input voltage and shuts down itself when the input voltage is lower than UVLO threshold voltage. When the input voltage is higher than the UVLO threshold plus hysteresis, the device will start again.

Thermal Shutdown (TSD)

A thermal shutdown function is implemented to prevent the damage caused by excessive heat and power dissipation. Once a temperature of +160°C (TYP) is exceeded, the device is shut down. The device is released from shutdown automatically when the junction temperature decreases by 20°C.

Nano-Power Management and Efficiency

The SGM6031 implements the nano-power circuitry and algorithm to achieve 400nA (TYP) quiescent current. This feature is accomplished through sampling and all references are saved, so as to lower the average quiescent current. During the sampling and saving, the internal circuits are only turned on for a short period of time and then turned off in the remaining time at the lowest feasible duty cycle.

APPLICATION INFORMATION

The SGM6031 family has ultra-low quiescent current and is capable of delivering up to 200mA load current, which is suitable for battery operated applications.

Setting the Output Voltage

The output voltage is set by a resistor divider between the output voltage and the FB pin. The voltage divider divides the output voltage down to the feedback voltage by the ratio:

$$V_{FB} = V_{OUT} \frac{R_2}{R_1 + R_2}$$
 (1)

where V_{FB} is the feedback voltage and V_{OUT} is the output voltage. Thus, the output voltage is:

$$V_{OUT} = 1.0 \times \frac{R_1 + R_2}{R_2}$$
 (2)

The value for R_2 can be as high as $1M\Omega$.

For example, for a 2V output voltage, R_2 is $1M\Omega$, and R_1 is $1M\Omega$.

Design Procedure

A 4.7µH inductor and a 22µF input ceramic capacitor are recommended for the SGM6031. Since the device only supports 200mA output load, a 22µF output ceramic capacitor is sufficient. For applications that are expected to have a large transient event, use I_{TRAN} = C_{OUT} \times $\Delta V_{OUT}/\Delta_{TIME}$ to size the output capacitor accordingly, where ΔV_{OUT} is the amount of output voltage drop during load step.

Inductor Selection

The SGM6031 is optimized to work with a $4.7\mu H$ inductor. The selected inductor's saturation current should be at least 25% higher than the maximum cycle-by-cycle current limit specified in the Electrical Characteristics table. The SGM6031 is naturally stable due to its hysteretic control architecture.

The recommended inductors for SGM6031 are shown in Table 1.

Table 1. Recommended Inductors

Inductance (µH)	Dimensions (mm³)	Part Number	Manufacturer	
4.7	2.0 × 2.5 × 1.2	DFE252012C-H-4R7M	Toko	
4.7	4.0 × 4.0 × 1.7	LPS4018-472ML	Coilcraft	

Output Capacitor Selection

A minimal of 22µF ceramic output capacitor is recommended for SGM6031. Larger size will result in higher effective capacitance under the same DC de-rating, which improves the transient response and output ripple.

Input Capacitor Selection

A 22 μ F ceramic capacitor and a 0.1 μ F ceramic bypass input capacitor are recommended to place between the VIN pin and GND as close as possible to minimize the parasitic inductance. For applications where the SGM6031 is located far away from the input source, a 22 μ F or higher capacitor is recommended to damp the inductance of the wiring harness.

400nA Ultra-Low Power, Buck Converter with 200mA Output Current

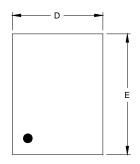
SGM6031

REVISION HISTORY

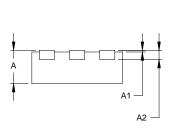
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

OCTOBER 2022 – REV.A.4 to REV.B	Page
Updated the Detailed Description and Application Information sections	11, 12
OCTOBER 2021 – REV.A.3 to REV.A.4	Page
Updated the Electrical Characteristics section	4
NOVEMBER 2017 – REV.A.2 to REV.A.3	Page
Updated Figure 3	9
OCTOBER 2017 – REV.A.1 to REV.A.2	Page
Changed the marking information for SGM6031-3.0YUDT6G	2
AUGUST 2017 – REV.A to REV.A.1	Page
Changed SGM6031-3.0 STATUS from PREVIEW to ACTIVE	2
Changes from Original (JUNE 2017) to REV.A	Page
Changed from product preview to production data	All

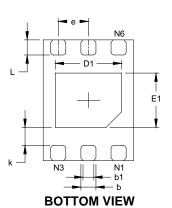
PACKAGE OUTLINE DIMENSIONS UTDFN-1.5×2-6L

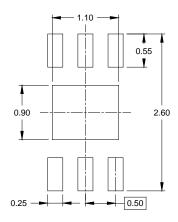


TOP VIEW



SIDE VIEW





RECOMMENDED LAND PATTERN (Unit: mm)

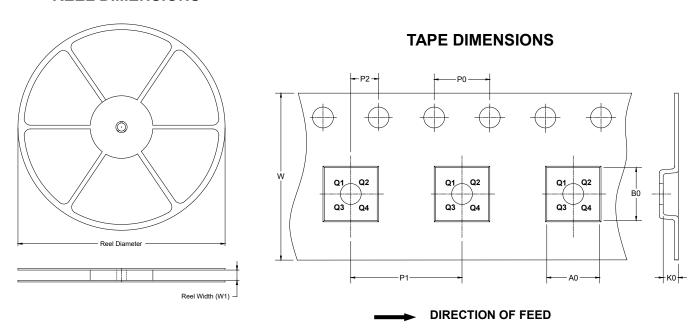
Symbol	_	nsions meters	_	nsions ches
	MIN	MAX	MIN	MAX
Α	0.500	0.600	0.020	0.024
A1	0.000	0.050	0.000	0.002
A2	0.152	REF	0.006	REF
D	1.400	1.600	0.055	0.063
D1	1.000	1.200	0.039	0.047
Е	1.900	2.100	0.075	0.083
E1	0.800	1.000	0.031	0.039
k	0.300) REF	0.012	REF
b	0.200	0.300	0.008	0.012
b1	0.180	REF	0.007 REF	
е	0.500	0 BSC 0.020 BS		BSC
L	0.200	0.300	0.008	0.012

NOTE: This drawing is subject to change without notice.



TAPE AND REEL INFORMATION

REEL DIMENSIONS

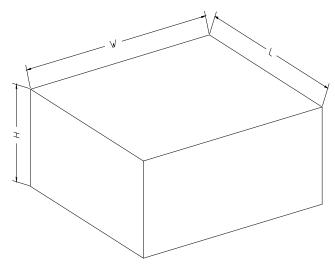


NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
UTDFN-1.5×2-6L	7"	9.5	1.70	2.30	0.75	4.0	4.0	2.0	8.0	Q2

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18