

GENERAL DESCRIPTION

The LM431VBS is a precision programmable shunt voltage reference that can be used in many applications. It is a three-terminal adjustable-output device that can keep stable under all capacitive loads. It requires two external resistors in application to set the output voltage from V_{REF} to 18V.

The LM431VBS features low dynamic impedance, low noise, and low temperature coefficient to ensure a stable output voltage over a wide range of operating temperatures and currents. These high performances make it very suitable for multiple applications, such as precision audio components and flyback switching power supplies.

The LM431VBS is available in a Green SOT-23 package.

FEATURES

- **Adjustable Output Voltage:** V_{REF} to 18V
- **Low Voltage Operation:** Down to 1.24V
- **Sink-Current Capability:** 0.1mA to 70mA
- **Reference Voltage Tolerance at +25°C:** 0.5%
- **Output Impedance:** 0.15Ω (TYP)
- **Temperature Drift:** 2mV (TYP)
- **High Stability under Capacitive Load**
- **Low Output Noise**
- **Operating Junction Temperature Range:** -40°C to +125°C
- **Available in a Green SOT-23 Package**

APPLICATIONS

- Portable, Battery-Powered Equipment
- Voltage Monitoring
- Flyback Switching Power Supplies
- Instrumentation
- Product Testing
- Precision Audio Components

TYPICAL APPLICATION

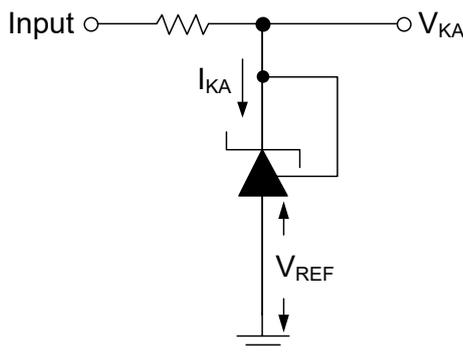


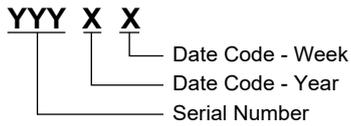
Figure 1. Typical Application Circuit

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
LM431VBS	SOT-23	-40°C to +125°C	LM431VBSXN3LG/TR	1ARXX	Tape and Reel, 3000

MARKING INFORMATION

NOTE: XX = Date Code.



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

- Cathode Voltage ⁽¹⁾, V_{KA}..... 20V
- Package Thermal Resistance
- SOT-23, θ_{JA}..... 193.9°C/W
- SOT-23, θ_{JB}..... 42.3°C/W
- SOT-23, θ_{JC}..... 83.3°C/W
- Junction Temperature.....+150°C
- Storage Temperature Range.....-65°C to +150°C
- Lead Temperature (Soldering, 10s).....+260°C
- ESD Susceptibility ^{(2) (3)}
- HBM.....±6000V
- CDM.....±1000V

NOTES:

1. All voltage values are with respect to ANODE, unless otherwise noted.
2. For human body model (HBM), all pins comply with ANSI/ESDA/JEDEC JS-001 specifications.
3. For charged device model (CDM), all pins comply with ANSI/ESDA/JEDEC JS-002 specifications.

RECOMMENDED OPERATING CONDITIONS

- Cathode Voltage, V_{KA}..... V_{REF} to 18V
- Cathode Current, I_{KA}.....0.1mA to 70mA
- Operating Junction Temperature Range.....-40°C to +125°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

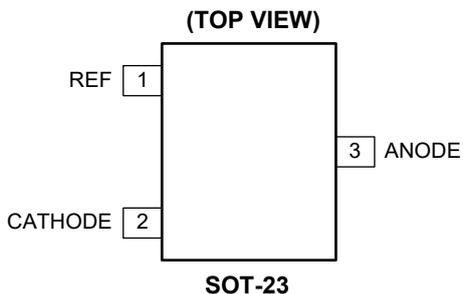
ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATION



PIN DESCRIPTION

PIN	NAME	TYPE	DESCRIPTION
1	REF	I	REF Input Pin. Threshold Related to the voltage of ANODE Pin.
2	CATHODE	I/O	Cathode Pin. Voltage input and shunts current.
3	ANODE	O	Anode Pin. Connect to ground directly.

NOTE: I = input, O = output, I/O = input/output.

FUNCTIONAL BLOCK DIAGRAM

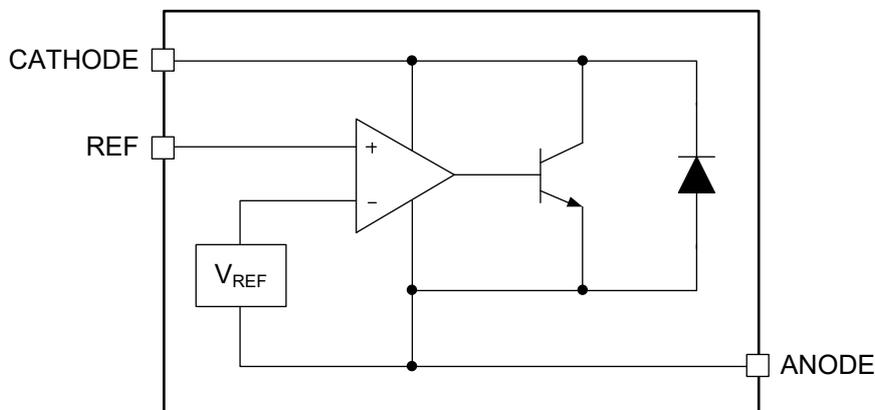


Figure 2. Equivalent Diagram

ELECTRICAL CHARACTERISTICS

(Over recommended operating conditions, $T_J = +25^\circ\text{C}$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Reference Voltage	V_{REF}	$V_{KA} = V_{REF}$, $I_{KA} = 10\text{mA}$	$T_J = +25^\circ\text{C}$ $T_J = -40^\circ\text{C to } +125^\circ\text{C}$	1.234 1.225	1.240	1.246 1.255	V
Deviation of Reference Input Voltage over Full Temperature Range ⁽¹⁾	$V_{I(DEV)}$	$V_{KA} = V_{REF}$, $I_{KA} = 10\text{mA}$		2	10	mV	
Ratio of Change in Reference Voltage to the Change in Cathode Voltage	$\Delta V_{REF}/\Delta V_{KA}$	$I_{KA} = 10\text{mA}$, $V_{KA} = V_{REF}$ to 18V	-0.2	-0.01	0.2	mV/V	
Reference Input Current	I_{REF}	$I_{KA} = 10\text{mA}$, $R_1 = 10\text{k}\Omega$, $R_2 = \text{open}$		0.09	0.15	μA	
Deviation of Reference Input Current over Full Temperature Range ⁽¹⁾	$I_{I(DEV)}$	$I_{KA} = 10\text{mA}$, $R_1 = 10\text{k}\Omega$, $R_2 = \text{open}$		0.02	0.05	μA	
Minimum Cathode Current for Regulation over Full Temperature Range	I_{MIN}	$V_{KA} = V_{REF}$		60	90	μA	
Off-State Cathode Current	$I_{K(OFF)}$	$V_{KA} = 18\text{V}$, $V_{REF} = 0\text{V}$		0.01	0.2	μA	
Dynamic Impedance ⁽²⁾	$ Z_{KA} $	$V_{KA} = V_{REF}$, $f \leq 1\text{kHz}$, $I_{KA} = 0.1\text{mA to } 70\text{mA}$		0.15	0.3	Ω	

NOTES:

1. $V_{I(DEV)}$ and $I_{I(DEV)}$ refer to the difference between the maximum value and the minimum value obtained within the rated temperature range. α_{VREF} is defined as:

$$|\alpha_{VREF}| \left(\frac{\text{ppm}}{^\circ\text{C}} \right) = \frac{\left(\frac{V_{I(DEV)}}{V_{REF} \text{ at } 25^\circ\text{C}} \right) \times 10^6}{\Delta T_J}$$

where,

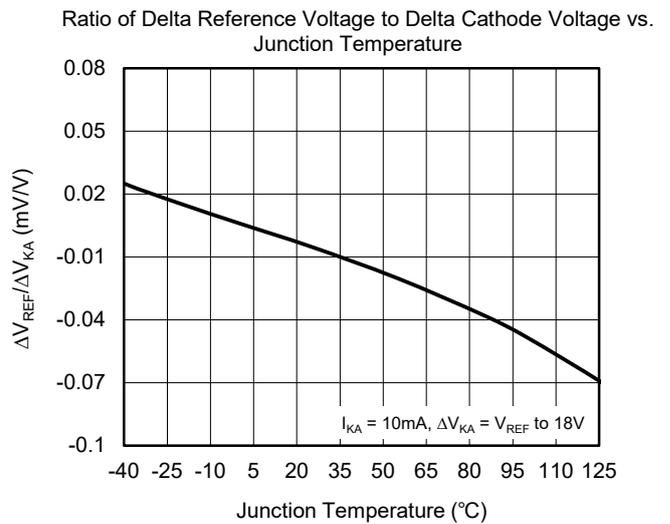
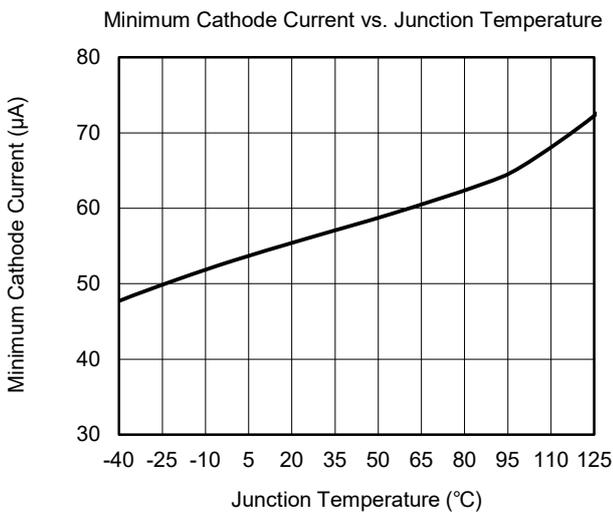
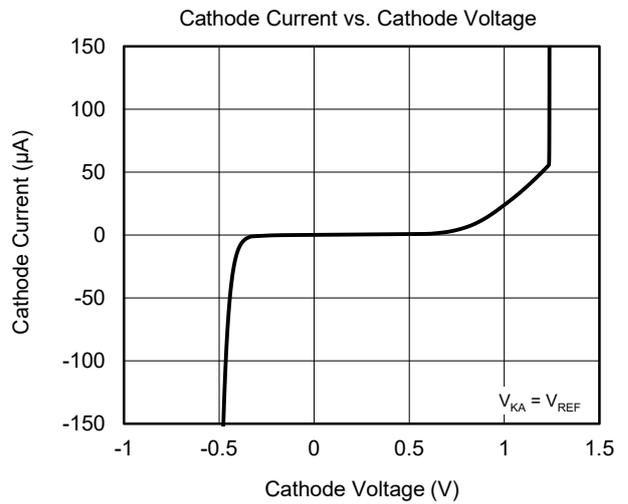
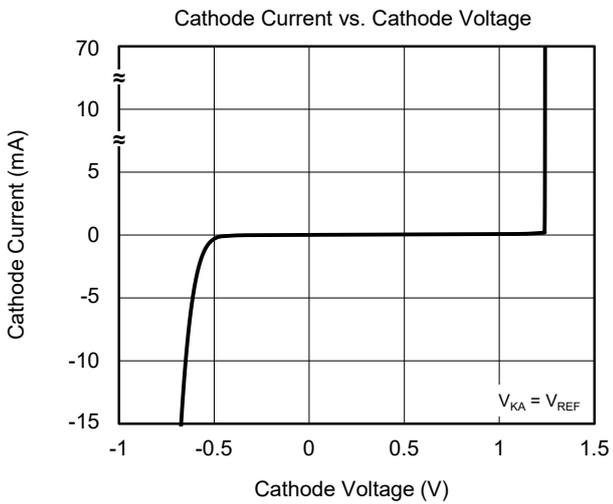
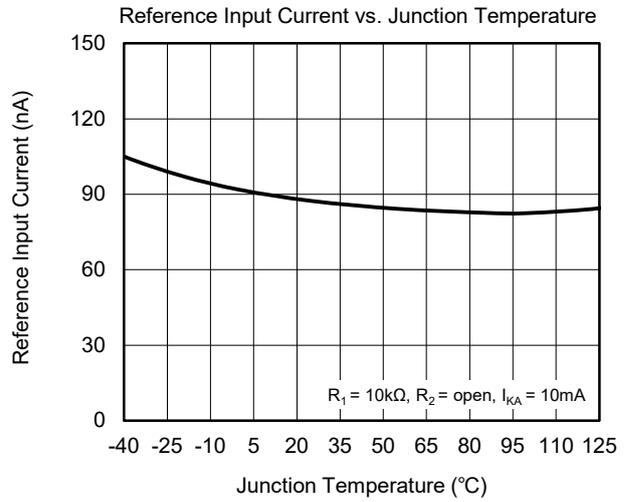
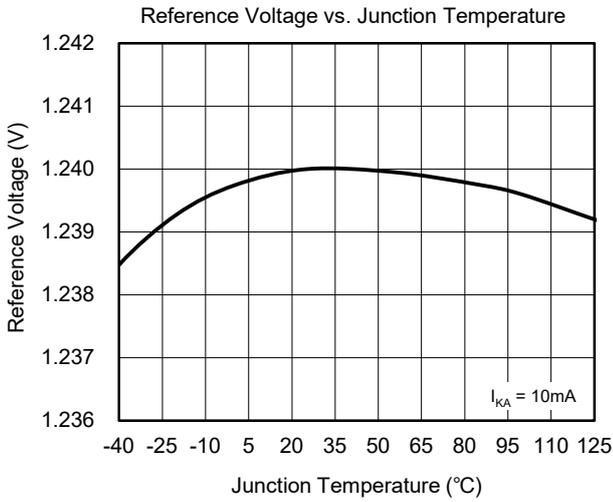
ΔT_J is the rated operating junction temperature range of the device.

α_{VREF} is the average full-range temperature coefficient of the reference input voltage.

2. $|Z_{KA}| = \Delta V_{KA} / \Delta I_{KA}$, when the device is operating with two external resistors, the total dynamic impedance of the circuit is equal to $|Z'| = \frac{\Delta V}{\Delta I}$, which is approximately equal to $|Z_{KA}| \left(1 + \frac{R_1}{R_2} \right)$.

TYPICAL PERFORMANCE CHARACTERISTICS

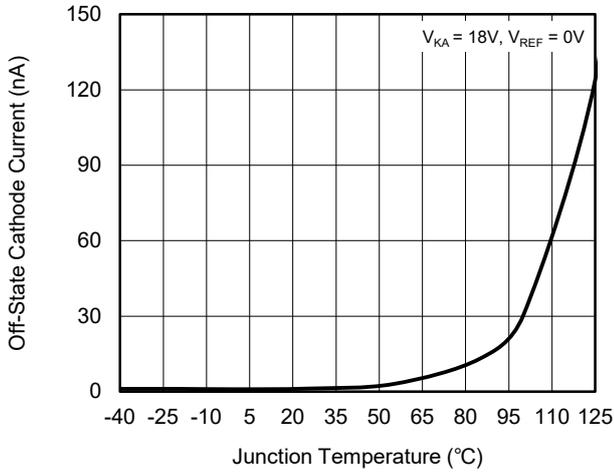
$T_J = +25^\circ\text{C}$, unless otherwise noted.



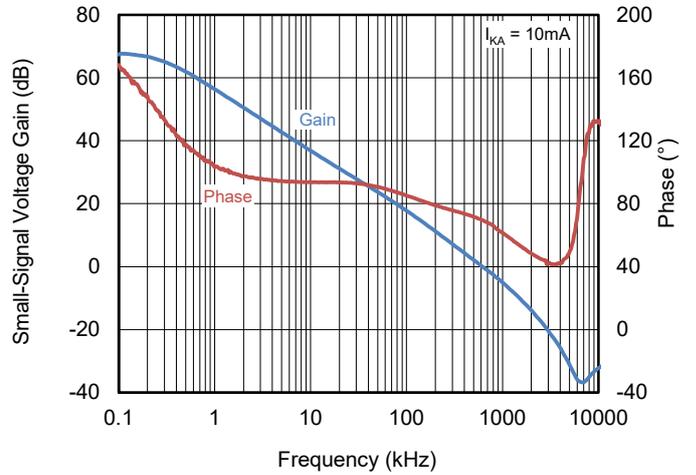
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

T_J = +25°C, unless otherwise noted.

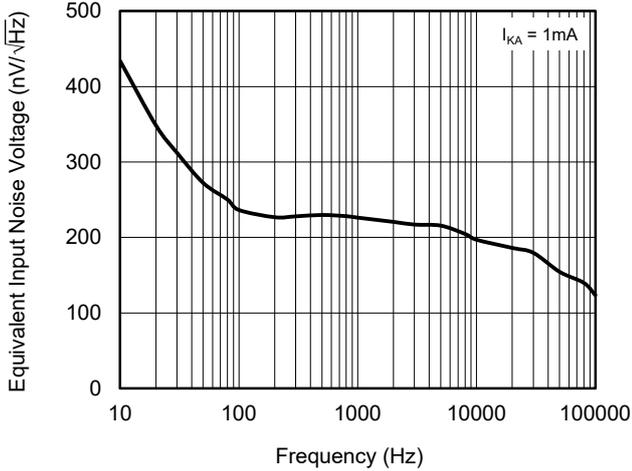
Off-State Cathode Current vs. Junction Temperature



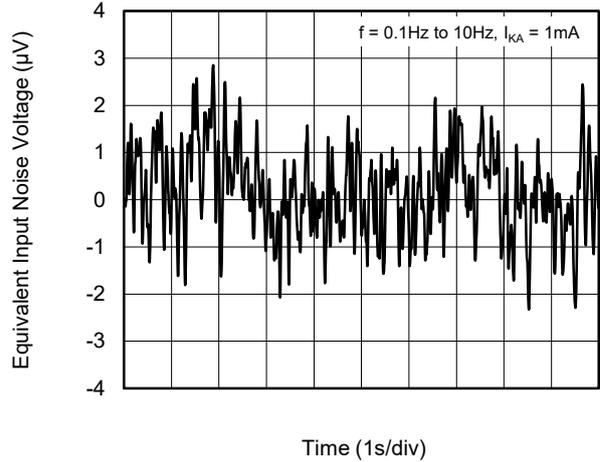
Small-Signal Voltage Gain/Phase vs. Frequency



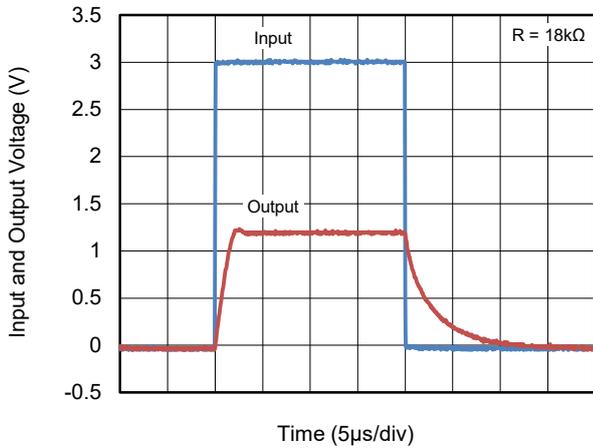
Equivalent Input Noise Voltage vs. Frequency



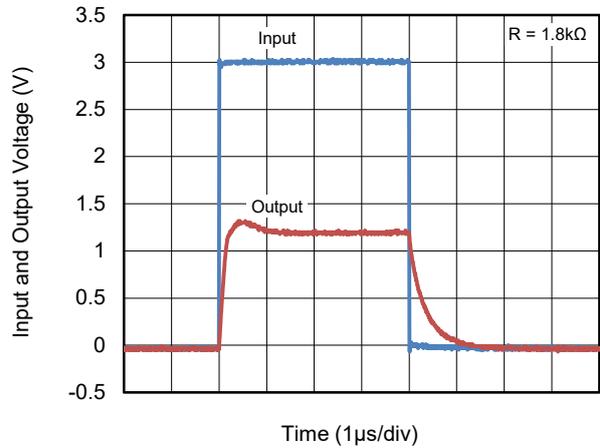
Equivalent Input Noise Voltage over a 10s Period



Pulse Response 1

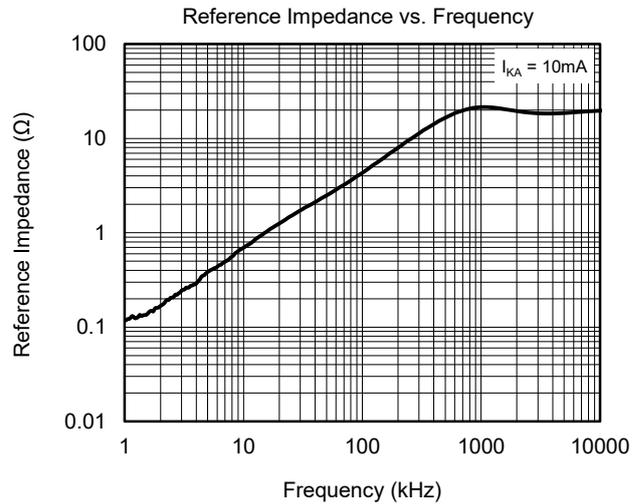
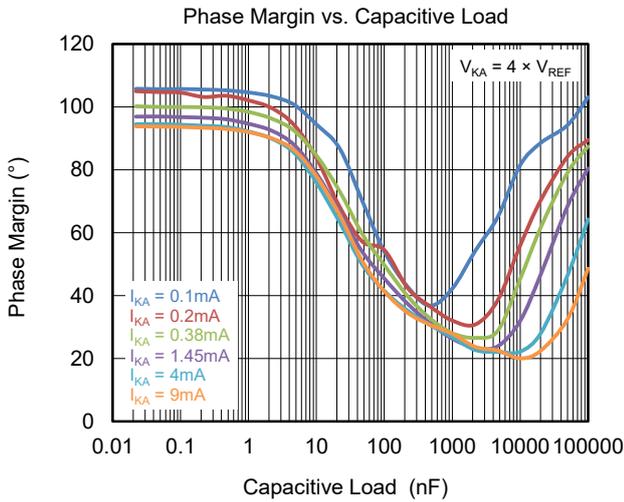
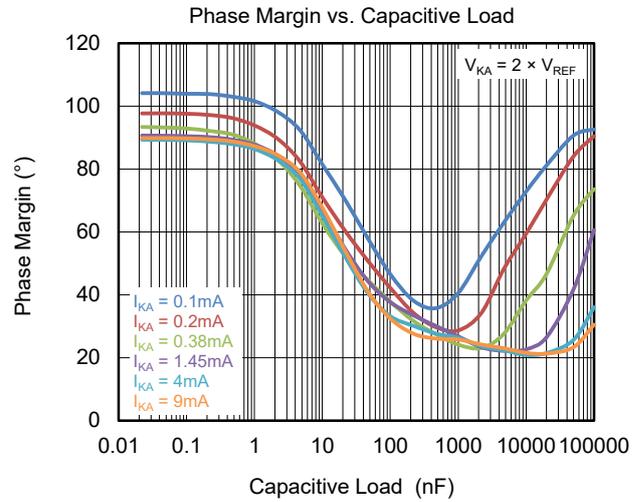
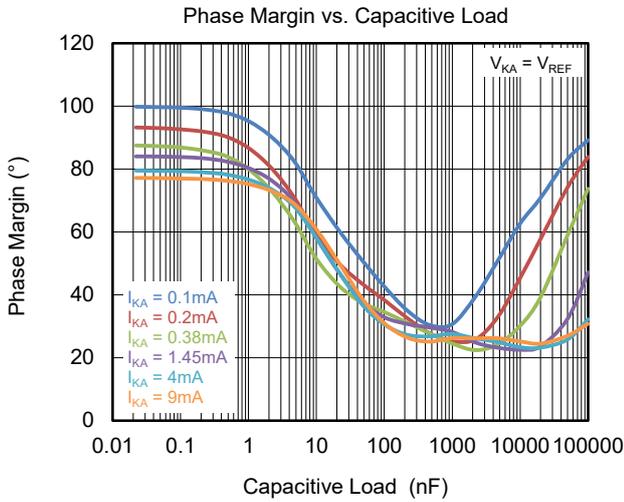


Pulse Response 2



TYPICAL PERFORMANCE CHARACTERISTICS (continued)

T_J = +25°C, unless otherwise noted.



APPLICATION INFORMATION

LM431VBS Used as Comparator with Integrated Reference

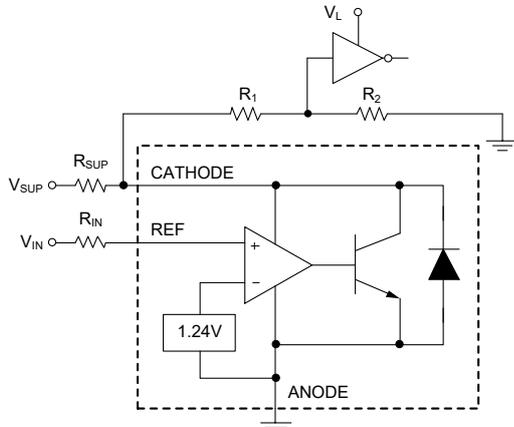


Figure 3. Comparator Application Schematic

Table 1. Design Parameters

Design Parameter	Example Value
Input Voltage Range	0V to 5V
Input Resistance	10kΩ
Supply Voltage	9V
Cathode Current (I_{KA})	500μA
Output Voltage Level	~1V to V_{SUP}
Logic Input Thresholds V_{IH}/V_{IL}	V_L

As shown in Figure 3, the LM431VBS is always used as a comparator with 1.24V integrated reference in application, for this design, the parameters are listed in Table 1. The REF pin voltage is compared with reference inside and is amplified. Also, the LM431VBS responds fast if the operation current I_{KA} is large

enough. The maximum cathode current of I_{MIN} is 90μA, any cathode current below this value will cause a low response (low open-loop gain) for the application of comparator.

The overdrive voltage is defined as the voltage over the internal reference voltage (1.24V). If the overdrive voltage is not large enough, the output response will be inaccurate and slow. On the contrary, the larger overdrive voltage provided at the REF pin, the faster the output response will be. Moreover, if the users desire fast response, the overdrive voltage should be larger than 10% of the internal voltage reference.

The input resistance should be less than 10kΩ in order to minimize the voltage drop between V_{IN} and V_{REF} . The readability of the logic output of the comparator should be guaranteed so that it can be received by the next stage. However, for the logic stage which is powered by 2.5V or 1.8V, the V_{IL} is hard to be figured out. To accommodate this, a voltage divider should be taken into account. Due to the Open-Collector (OC) output stage, the V_{IH} is equal to the voltage of its power supply. When the voltage divider is used to attenuate V_{IH} , please make sure that the sum of R_1 and R_2 is greater than the resistance R_{SUP} to the power supply.

However, for this application, an external resistor R_{IN} is required to provide the I_{REF} and guarantee the normal operation of the LM431VBS. Also, because of the voltage drop across R_{IN} , so that the gap between V_{IN} and V_{REF} is the voltage drop of R_{IN} . To decrease the error, the selected R_{IN} should be small enough as the maximum I_{REF} is 0.15μA.

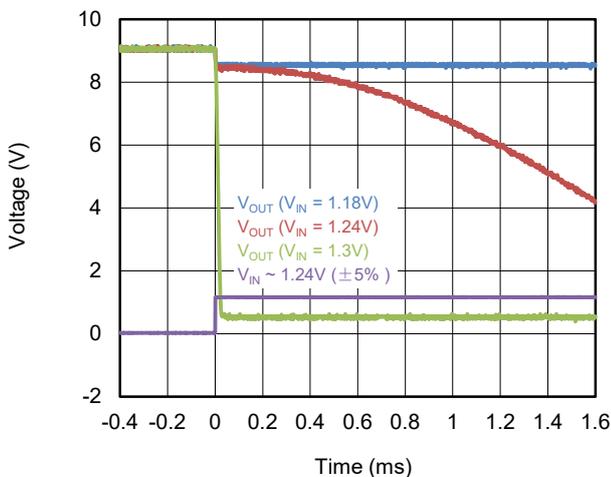


Figure 4. Output Response with Small Overdrive Voltages

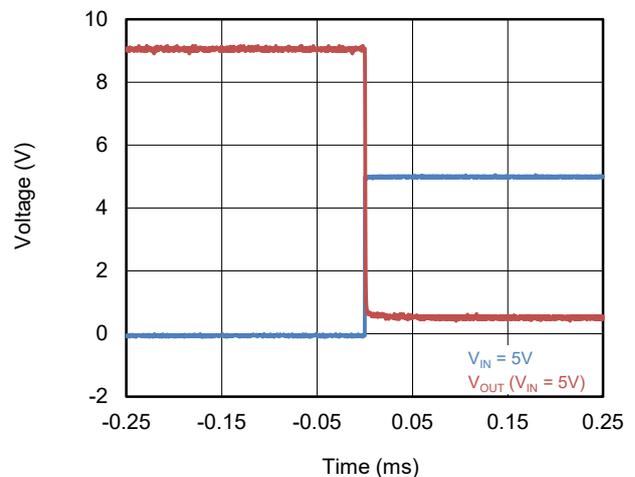


Figure 5. Output Response with Large Overdrive Voltage

APPLICATION INFORMATION (continued)

Shunt Regulator/Reference

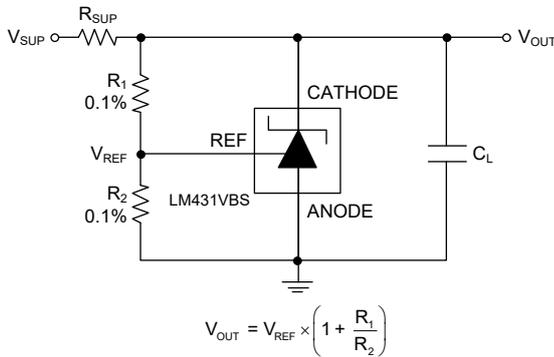


Figure 6. Shunt Regulator Schematic

As shown in Figure 6, for this design, the input parameters are listed in Table 2.

Table 2. Design Parameters

Design Parameter	Example Value
Reference Initial Accuracy	1.0%
Supply Voltage	6V
Cathode Current (I _{KA})	500µA
Output Voltage Level	1.24V to 6V
Load Capacitance	4.7µF
Feedback Resistor Values and Accuracy (R ₁ and R ₂)	10kΩ

Programming Output Voltage

The output/cathode voltage of the LM431VBS is programmed by resistor R₁ and R₂. The following equation shows the relationship between V_{REF} and V_{OUT}. In accurate applications, the error caused by I_{REF} should also be taken into account:

$$V_{OUT} = V_{REF} \times \left(1 + \frac{R_1}{R_2} \right) + I_{REF} \times R_1$$

And the users need to ensure that the LM431VBS is fully biased, so that this error can be decreased significantly by the enough open-loop gain. This can be qualified by satisfying the minimum cathode current I_{MIN} in Electrical Characteristics.

Total Accuracy

When the output voltage is set higher than unity gain (1.24V), the behavior of the LM431VBS will be influenced by the following possible errors:

- ◆ The accuracy of both R₁ and R₂.
- ◆ The internal reference voltage will be changed by the temperature.
- ◆ ΔV_{REF}/ΔV_{KA}: The change of internal reference voltage will affect the output voltage.
- ◆ |Z_{KA}|: This impedance will be affected by the cathode current.

The above possible errors are the worst cases for the operation of the LM431VBS.

Stability

For traditional 431 devices, the reference output stability is affected by output decoupling capacitor which must be selected in a limited range in order to keep 431 devices stable in applications. However, the LM431VBS is different from the others. The LM431VBS uses a special design to greatly improve output stability when it drives capacitive load. From the curve of the Cathode Current vs. Load Capacitance in Figure 7, the output of the LM431VBS is stable in a wider range of capacitive load. If the LM431VBS is used as a linear regulator, a bigger decoupling capacitor can be used to improve regulator's load transient or reduce the noise of power supply. If the LM431VBS is used to replace the others, the LM431VBS will be stable and there is no need to change the output decoupling capacitor in the original design.

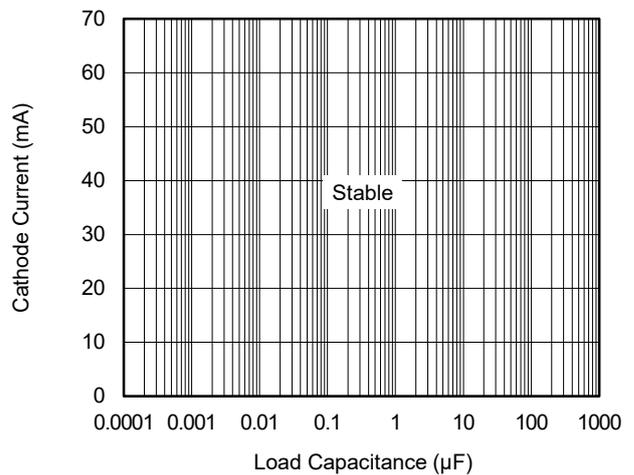


Figure 7. Cathode Current vs. Load Capacitance

APPLICATION INFORMATION (continued)

Start-Up Time

As shown in Figure 8, the LM431VBS is suitable for many clamp applications with the fast response.

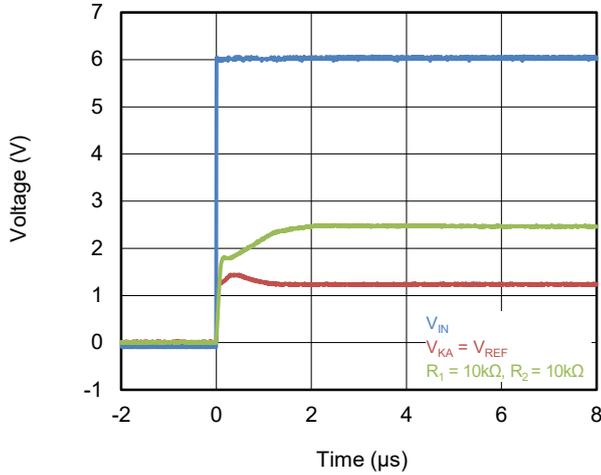


Figure 8. LM431VBS Start-Up Response

Power Supply Recommendations

When the LM431VBS is used as a shunt regulator, it is necessary to use a bypass capacitor on the CATHODE pin.

Limiting the current of the power supply is necessary for the LM431VBS to operate within the maximum cathode current. Also, the current of the REF pin should be limited as well.

When the device shunts high current, adjusting the width and length of traces on the CATHODE pin and ANODE pin is necessary in the PCB board to have a proper density of current.

Layout

Please make sure that the bypass capacitor is close enough to the part. The width of trace on the PCB should be fit for its amount of current flowing. For the LM431VBS, these currents will not be large.

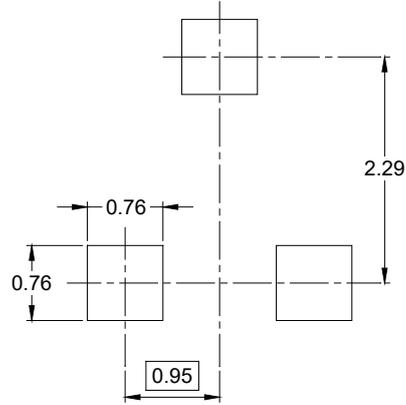
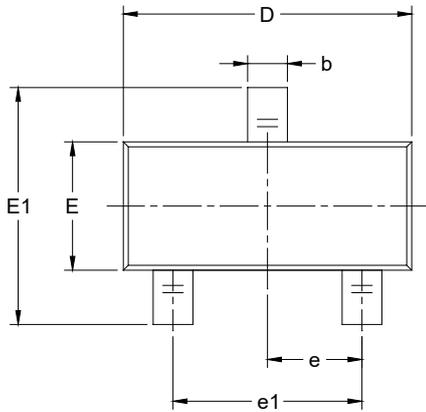
REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

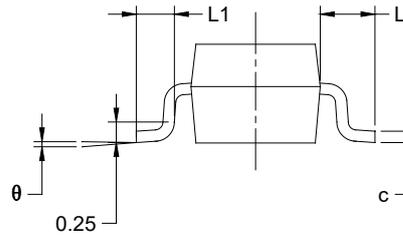
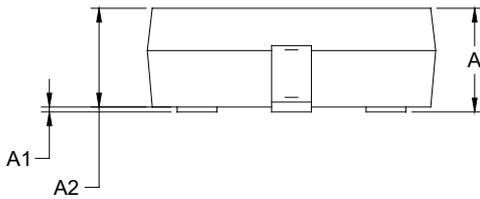
Changes from Original to REV.A (JUNE 2025)	Page
Changed from product preview to production data.....	All

PACKAGE OUTLINE DIMENSIONS

SOT-23



RECOMMENDED LAND PATTERN (Unit: mm)



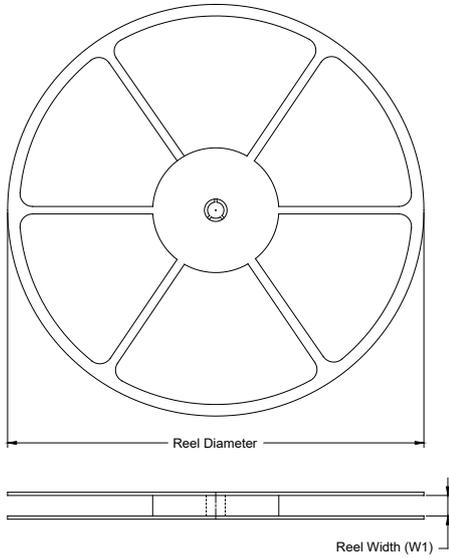
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.89	1.12	0.035	0.044
A1	0.01	0.10	0.000	0.004
A2	0.88	1.02	0.035	0.040
b	0.30	0.50	0.012	0.020
c	0.08	0.20	0.003	0.008
D	2.80	3.04	0.110	0.120
E	1.20	1.40	0.047	0.055
E1	2.10	2.64	0.083	0.104
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.54 REF		0.021 REF	
L1	0.40	0.60	0.016	0.024
θ	0°	8°	0°	8°

NOTES:

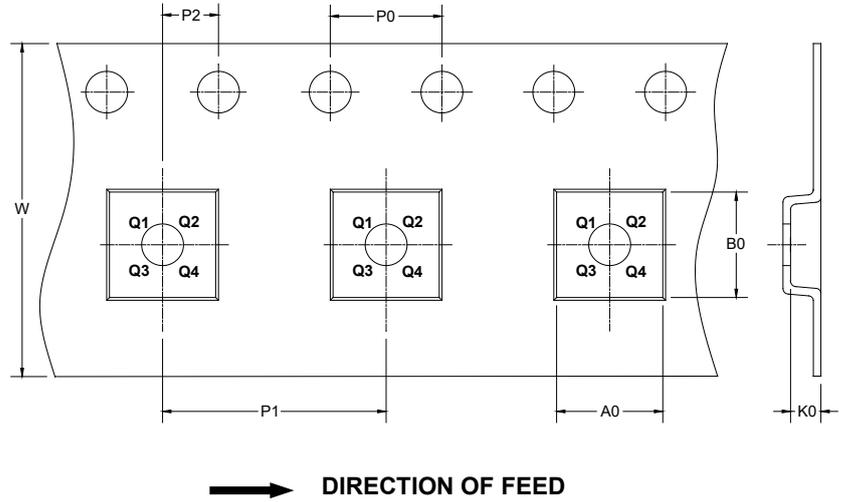
1. Body dimensions do not include mode flash or protrusion.
2. This drawing is subject to change without notice.

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

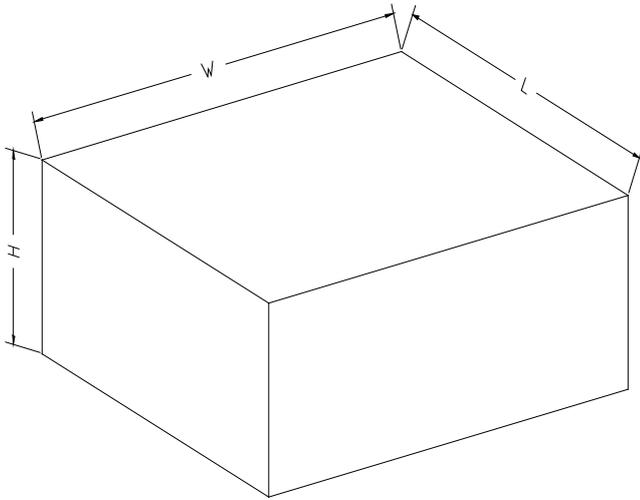
KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT-23	7"	9.5	3.15	2.77	1.22	4.0	4.0	2.0	8.0	Q3

DD0001

PACKAGE INFORMATION

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18

D00002