

SGM4807 Shunt Mode Audio Click-Pop Eliminator

GENERAL DESCRIPTION

The SGM4807 is an audio click-pop eliminator which can operate from a 1.7V to 5V single power supply. It is designed for portable devices.

The SGM4807 provides a low-impedance grounding path during startup and shutdown by connecting to the output of the system amplifier. It allows - V_{CC} to + V_{CC} wide range audio signals passing with low distortion. Shunt mode can eliminate the click-pop noise.

The SGM4807 has two low-impedance analog switches. The opening and closing of the switches are controlled by mute events that include external power supply and under-voltage of V_{CC} , power-up, power-down and high to low logical level change of $\overline{\text{MUTE}}$ pin. Opening switches during normal operation will not affect the output signal.

The SGM4807 can be activated during startup and shutdown of the amplifier to short-circuit the output to the ground and reduce clicks-pops.

The SGM4807 is available in Green TDFN-2×2-8L, MSOP-8 and WLCSP-1.57×0.80-8B packages. It operates over an ambient temperature range of -40°C to +85°C.

FEATURES

• Supply Voltage Range: 1.7V to 5V

• Pop/Click Reduction Circuitry

• Supply Current: 1.5µA (TYP)

• 600kΩ Pull-Low Resistor at MUTE Pin

• -40°C to +85°C Operating Temperature Range

 Available in Green TDFN-2×2-8L, MSOP-8 and WLCSP-1.57×0.80-8B Packages

APPLICATIONS

Notebook Computers
Phones
Portable Equipment



PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION	
	TDFN-2×2-8L	-40°C to +85°C	SGM4807YTDE8G/TR	4807 XXXX	Tape and Reel, 3000	
SGM4807	MSOP-8	-40°C to +85°C	SGM4807YMS8G/TR	SGM4807 YMS8 XXXXX	Tape and Reel, 4000	
	WLCSP-1.57×0.80-8B	-40°C to +85°C	SGM4807YG/TR	8EXX	Tape and Reel, 4000	

NOTE: XX = Date Code, XXXX = Date Code, XXXXX = Date Code and Vendor Code.

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

$V_{CC},\ \overline{MUTE}$, SET, CPB, UVP to GND0.3V to +6V
INL, INR to GND $-V_{CC}$ -0.3V to +V _{CC} + 0.3V
Continuous Current In/Out of V _{CC} 30mA
Continuous Current In/Out of MUTE
Continuous Current In/Out of SET, CPB, UVP 30mA
Continuous Current In/Out of INL, INR and GND 390mA
Junction Temperature+150°C
Storage Temperature Range65°C to +150°C
Lead Temperature (Soldering, 10s)+260°C
ESD Susceptibility
HBM8000V
MM400V
CDM

RECOMMENDED OPERATING CONDITIONS

Operating Voltage Range	1.7V to 5V
Operating Temperature Range.	40°C to +85°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

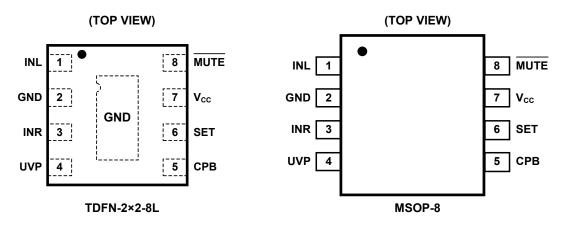
ESD SENSITIVITY CAUTION

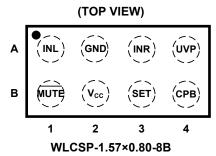
This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATIONS





PIN DESCRIPTION

Pl	N		
TDFN-2×2-8L/ MSOP-8	WLCSP- 1.57×0.80-8B	NAME	FUNCTION
1	A1	INL	Left-Channel Input. Connect the INL between the headphone jack and the output coupling capacitor.
2	A2	GND	Ground.
3	A3	INR	Right-Channel Input. Connect the INR between the headphone jack and the output coupling capacitor.
4	A4	UVP	Under-Voltage Protection Input. When UVP event happens, chip will be in mute status.
5	B4	СРВ	Power-On Blanking Time Adjusting. Connect a capacitor from CPB pin to GND to program the power-on blanking time. Chip is in mute status during power-on blanking time.
6	В3	SET	Turn-Off Time Set. Connect an external capacitor between SET and GND to set the switch open delay.
7	B2	V_{CC}	Power Supply.
8	B1	MUTE	Active Low Enable. When $\overline{\text{MUTE}}$ = "Low", chip enters into mute status; when $\overline{\text{MUTE}}$ = "High", chip works normally. There is one $600 \text{k}\Omega$ pull-low resistor at $\overline{\text{MUTE}}$ pin.

ELECTRICAL CHARACTERISTICS

 $(V_{CC} = 5.0V, V_{GND} = 0V, Full = -40^{\circ}C$ to +85°C, typical values are at $T_A = +25^{\circ}C$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Supply Voltage Range	V _{cc}	Guaranteed by R _{ON} test		1.7		5.5	V
Recommended Supply Voltage Range (1)	V _{cc}	I _{IN} = -3.5mA		1.7		5.0	V
Input Voltage Range	V _{IN}	Guaranteed by input leakage current test		-V _{CC}		+V _{CC}	V
Supply Current (2)		V _{CC} = 5.0V			1.5	2	
Supply Current **	I _{cc}	V _{CC} = 5.0V	Full			2.5	μA
		V = 2.2V	+25°C		110	150	
Switch On Registeres	В	$V_{CC} = 3.3V$	Full			175	m0
Switch On-Resistance	R _{on}	V - 5 0V	+25°C		90	120	mΩ
		$V_{CC} = 5.0V$	Full			145	
		V 0.0V	+25°C		82	110	
0 11 L 0 D 1 L 0 AM 00D)		$V_{CC} = 3.3V$	Full			135	
Switch On-Resistance (WLCSP)	R _{ON(WLCSP)}		+25°C		65	88	mΩ
		$V_{CC} = 5.0V$	Full			105	
Click-Pop Reduction		$R_{SERIES} = 30\Omega$, $R_{LOAD} = 16\Omega$	+25°C		45		dB
Click-Pop Reduction (WLCSP)		$R_{SERIES} = 30\Omega$, $R_{LOAD} = 16\Omega$	+25°C		48		dB
		$V_{IN} = -5.0V, V_{CC} = 5.0V$ +25°C Full +25°C	+25°C		±0.2		μΑ
lowest Leading Comment			Full			±1	
Input Leakage Current	I _{IN}		+25°C		±0.1		
	$V_{IN} = V_{CC}, V_{CC} = 5.0V$		Full			±1	
THE Lookage Current		V - 5 0V V 0V	+25°C		±0.1		
MUTE Leakage Current	I _{MUTE}	$V_{CC} = 5.0V, V_{\overline{MUTE}} = 0V$	Full			±1	μΑ
MUTE Input-Voltage High	V _{IH}		+25°C	1.5			V
MUTE Input-Voltage Low	V _{IL}		+25°C			0.4	V
Under-Voltage Detection Threshold	V_{UVPTH}	V _{CC} = 5.0V, MUTE = "High"	+25°C	1.14	1.2	1.26	V
Pull-Low Resistor at MUTE Pin	R _{Pull-Low}	V _{CC} = 5.0V	+25°C		600		kΩ
Power-On Blanking Time of Mute Status	t _{PB}	C _{SET1} = 500pF, Figure 1	+25°C		2.6		ms
Turn-On Time	t _{on}	Measured from $\overline{\text{MUTE}}$ = GND and input voltage settled to 90% of its final value, Figure 2	+25°C		110		ns
		C _{SET} = 500pF, Figure 2	+25°C	5	13	17	
Turn-Off Time	t _{OFF}	C _{SET} = 50pF, Figure 2 +25°C		1.5		ms	
		C _{SET} = 50nF, Figure 2	+25°C		1300		

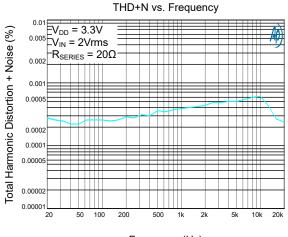
NOTES:

- 1. Working within the recommended supply voltage range ensures that negative audio signals are not restricted by the device. Supply voltages above the recommended voltage range may limit the maximum output voltage of the headphone amplifier.
- 2. Measure the supply current when switches are off.

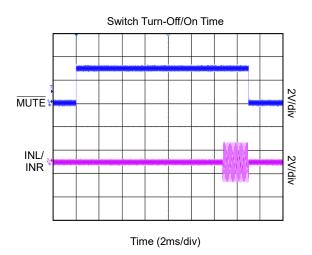


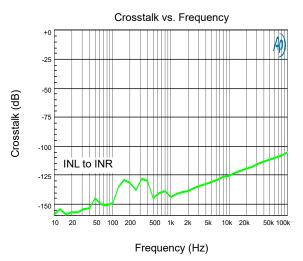
TYPICAL PERFORMANCE CHARACTERISTICS

 V_{CC} = 5.0V, V_{GND} = 0V, typical values are at T_A = +25°C, unless otherwise noted.





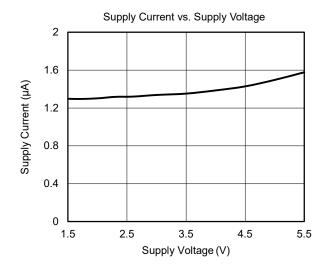


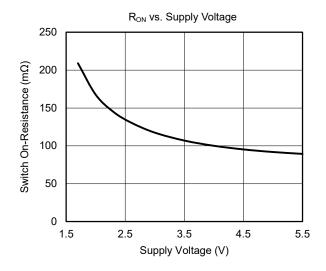


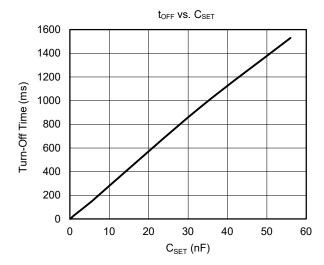
Frequency (Hz)

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

 V_{CC} = 5.0V, V_{GND} = 0V, typical values are at T_A = +25°C, unless otherwise noted.

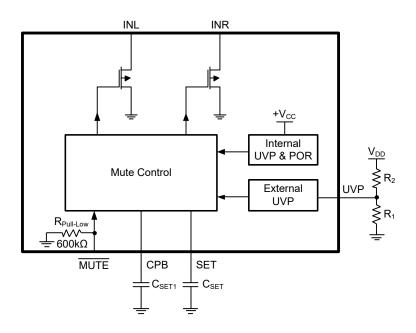








SYSTEM BLOCK DIAGRAM



TIMING DIAGRAM

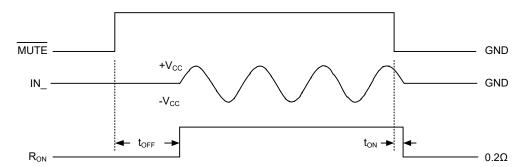


Figure 1. Turn-On/Off Time

SGM4807

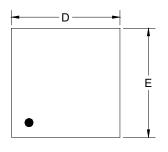
Shunt Mode Audio Click-Pop Eliminator

REVISION HISTORY

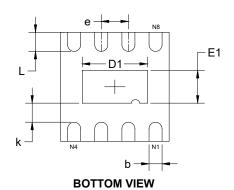
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

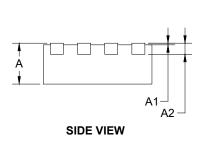
Changes from Original (DECEMBER 2016) to REV.A

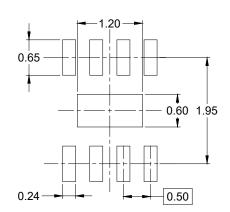
PACKAGE OUTLINE DIMENSIONS TDFN-2×2-8L



TOP VIEW







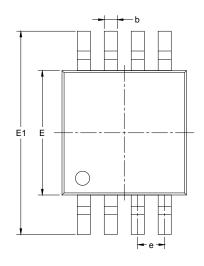
RECOMMENDED LAND PATTERN (Unit: mm)

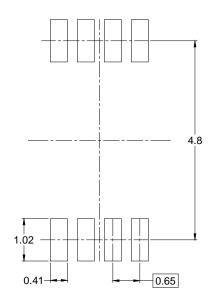
Symbol		nsions meters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
А	0.700	0.800	0.028	0.031	
A1	0.000	0.050	0.000	0.002	
A2	0.203	REF	0.008	REF	
D	1.900	2.100	0.075	0.083	
D1	1.100	1.300	0.043	0.051	
E	1.900	2.100	0.075	0.083	
E1	0.500	0.700	0.020	0.028	
k	0.200	0.008 MIN		3 MIN	
b	0.180	0.300	0.007	0.012	
е	0.500) TYP	TYP 0.020 TYP		
L	0.250	0.450	0.010	0.018	

NOTE: This drawing is subject to change without notice.

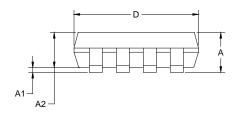


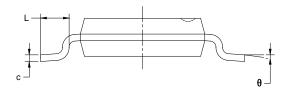
PACKAGE OUTLINE DIMENSIONS MSOP-8





RECOMMENDED LAND PATTERN (Unit: mm)



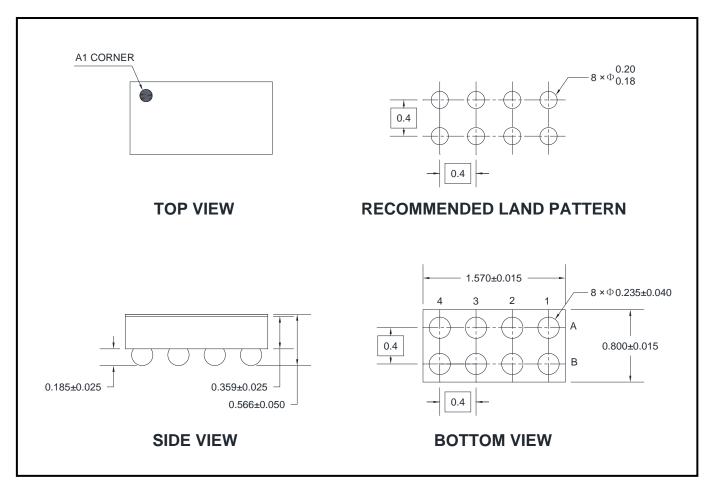


Symbol		nsions meters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
Α	0.820	1.100	0.032	0.043	
A1	0.020	0.150	0.001	0.006	
A2	0.750	0.950	0.030	0.037	
b	0.250	0.380	0.010	0.015	
С	0.090	0.230	0.004	0.009	
D	2.900	3.100	0.114	0.122	
E	2.900	3.100	0.114	0.122	
E1	4.750	5.050	0.187	0.199	
е	0.650 BSC		0.026	BSC	
L	0.400	0.800	0.016	0.031	
θ	0°	6°	0°	6°	

- Body dimensions do not include mode flash or protrusion.
 This drawing is subject to change without notice.

PACKAGE OUTLINE DIMENSIONS

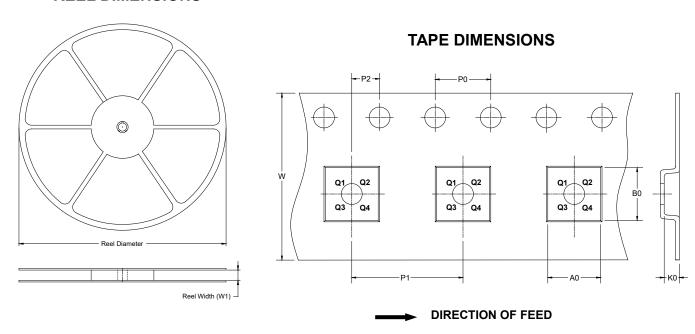
WLCSP-1.57×0.80-8B



- All linear dimensions are in millimeters.
 This drawing is subject to change without notice.

TAPE AND REEL INFORMATION

REEL DIMENSIONS

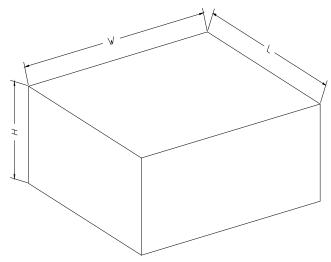


NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
TDFN-2×2-8L	7"	9.5	2.30	2.30	1.10	4.0	4.0	2.0	8.0	Q1
MSOP-8	13"	12.4	5.20	3.30	1.50	4.0	8.0	2.0	12.0	Q1
WLCSP-1.57×0.80-8B	7"	9.5	1.00	1.80	0.70	4.0	4.0	2.0	8.0	Q2

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18
13"	386	280	370	5