

FEATURES

- Low On-State Resistance
- Low Total Gate Charge and Capacitance Losses
- Small Footprint ($5 \times 6 \text{mm}^2$) for Compact Design
- Halogen-Free/RoHS Compliant

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNITS
Drain-to-Source Voltage	V_{DS}	40	V
Gate-to-Source Voltage	V_{GS}	± 20	V
Drain Current ⁽¹⁾	I_D	$T_A = +25^\circ\text{C}$	44
		$T_A = +70^\circ\text{C}$	35
		$T_C = +25^\circ\text{C}$	320
		$T_C = +100^\circ\text{C}$	202
Drain Current (Pulse) ⁽²⁾	I_{DM}	900	A
Total Dissipation	P_D	$T_A = +25^\circ\text{C}$	2.5
		$T_A = +70^\circ\text{C}$	1.6
		$T_C = +25^\circ\text{C}$	131
		$T_C = +100^\circ\text{C}$	52
Avalanche Current ⁽³⁾	I_{AS}	97	A
Avalanche Energy ⁽³⁾	E_{AS}	470.45	mJ
Junction Temperature	T_J	+150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 to +150	$^\circ\text{C}$
Lead Temperature (Soldering, 10s)		+260	$^\circ\text{C}$

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

NOTES:

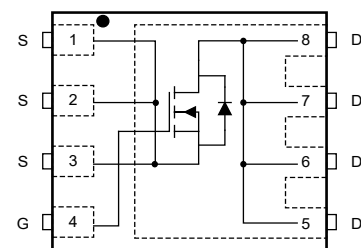
1. The current will be limited by PCB, thermal design and operating temperature.
2. $t_{PLUSE} < 10\mu\text{s}$
3. Parts are 100% tested at $V_{GS} = 10\text{V}$, $I_L = 69\text{A}$, $E_{AS} = 238.05\text{mJ}$

PRODUCT SUMMARY

$R_{DS(on)}$ (TYP) $V_{GS} = 10\text{V}$	$R_{DS(on)}$ (MAX) $V_{GS} = 10\text{V}$	I_D (MAX) $T_C = +25^\circ\text{C}$
0.72m Ω	0.94m Ω	320A

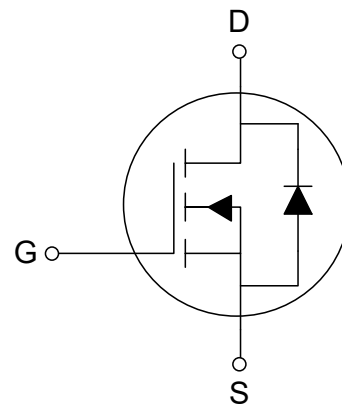
PIN CONFIGURATION

(TOP VIEW)



PDFN-5x6-8CL

EQUIVALENT CIRCUIT



APPLICATIONS

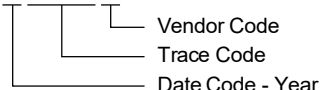
- Power Tool
- Brushless DC Motor Control
- Hotswap/In-Rush Current Management
- DC/DC Converters
- Power Load Switch and eFuse

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGMNQ90540	PDFN-5×6-8CL	-55°C to +150°C	SGMNQ90540TPDA8G/TR	SGM0T4 TPDA8 XXXXXX	Tape and Reel, 4000

MARKING INFORMATION

NOTE: XXXXX = Date Code, Trace Code and Vendor Code.

XXXXX

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

THERMAL RESISTANCE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNITS
Junction-to-Ambient Thermal Resistance ⁽¹⁾	$R_{\theta JA}$	49	°C/W
Junction-to-Case Thermal Resistance	$R_{\theta JC}$	0.95	°C/W

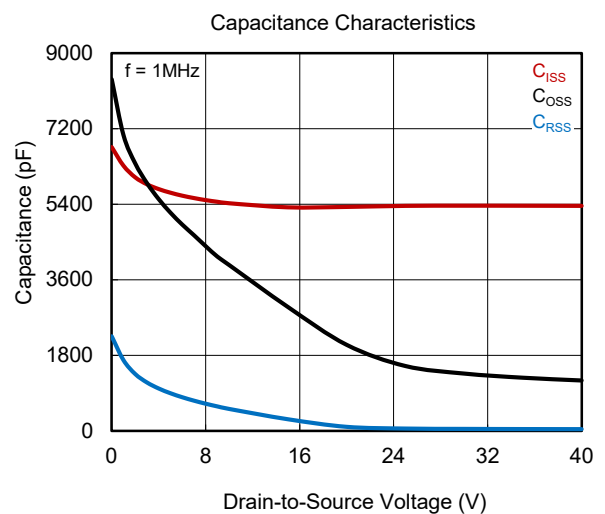
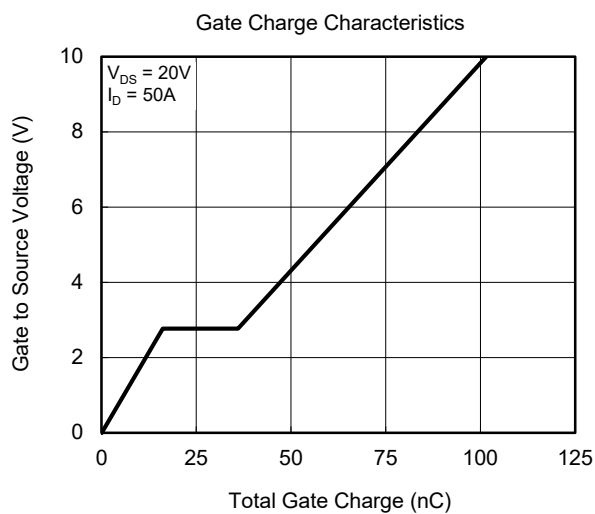
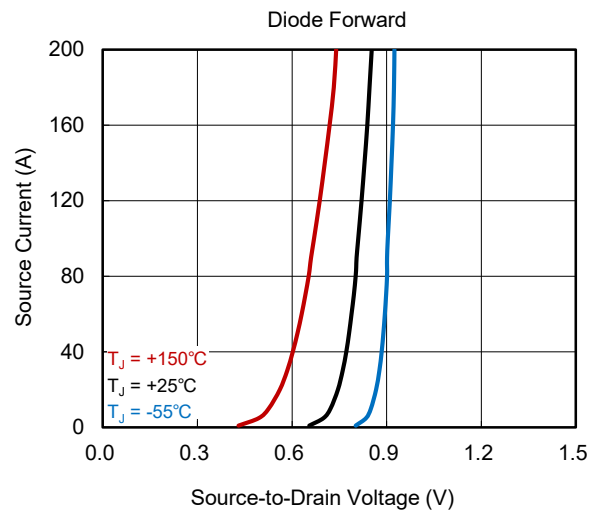
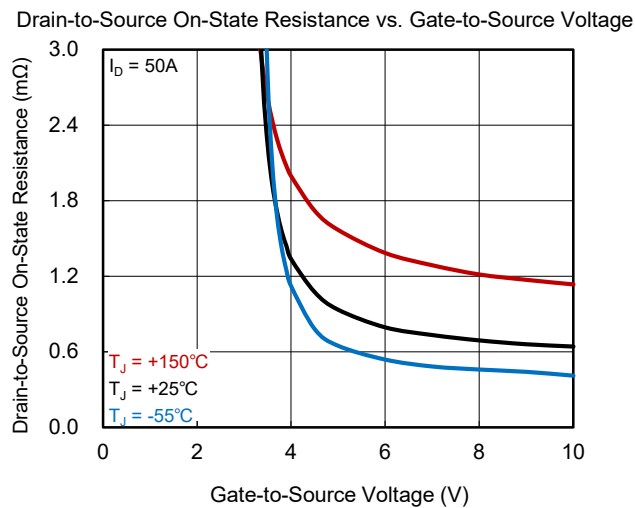
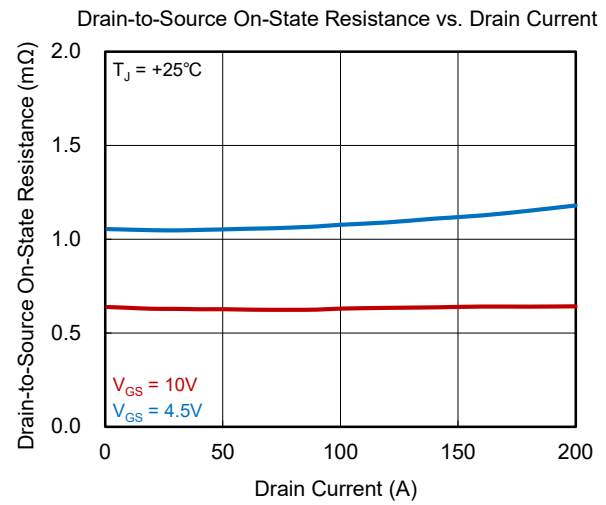
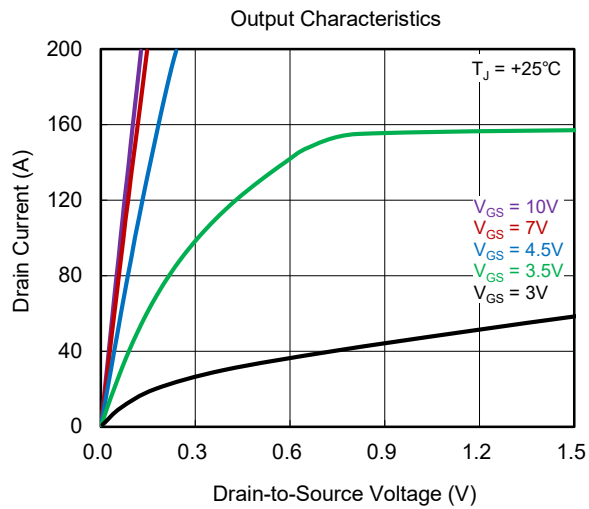
NOTE: 1. $R_{\theta JA}$ is determined with the device mounted on one square inch of copper pad, 2oz copper on FR4 board.

ELECTRICAL CHARACTERISTICS

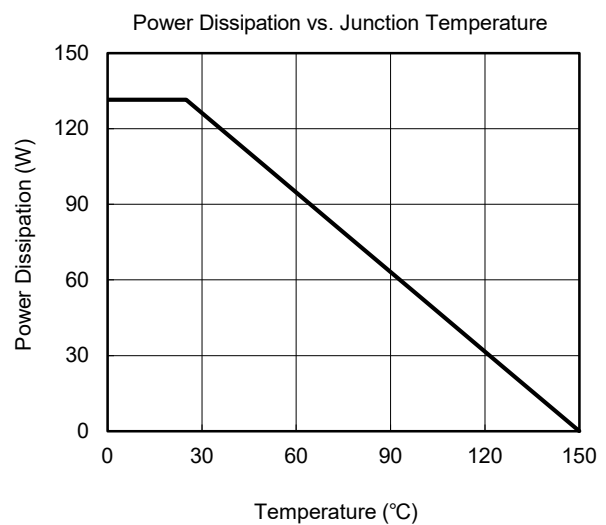
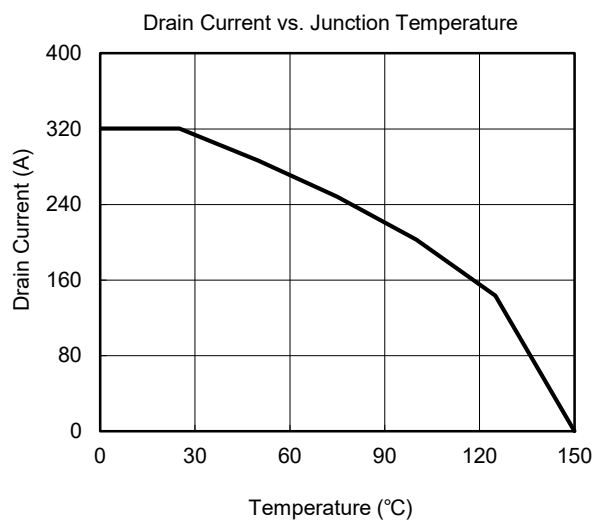
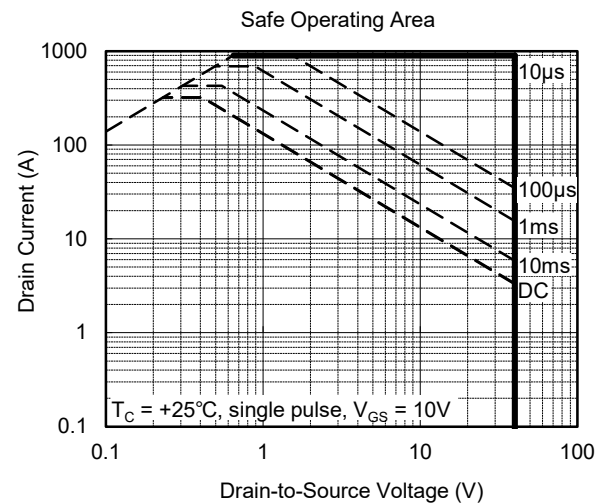
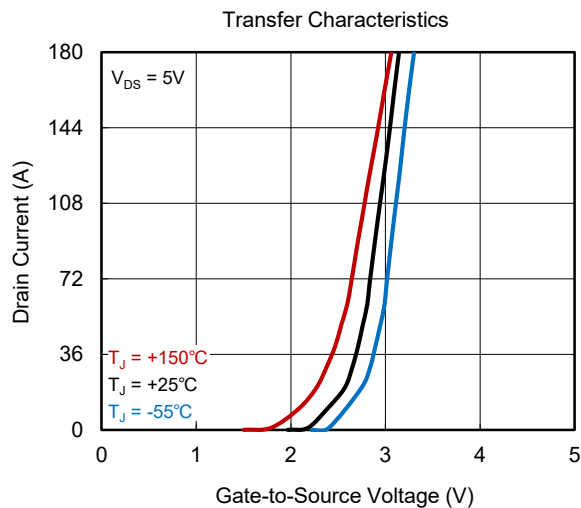
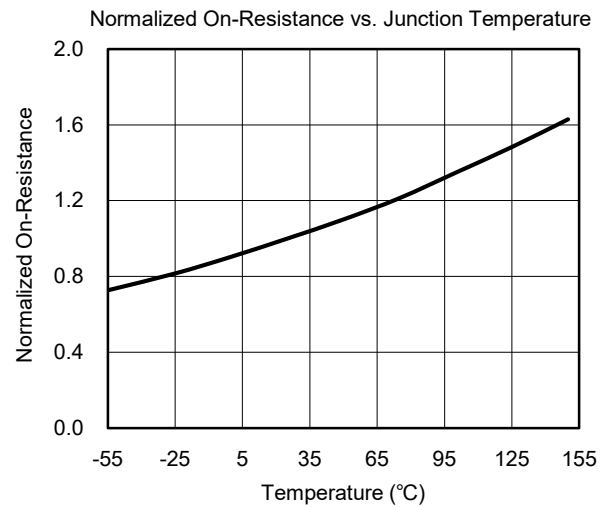
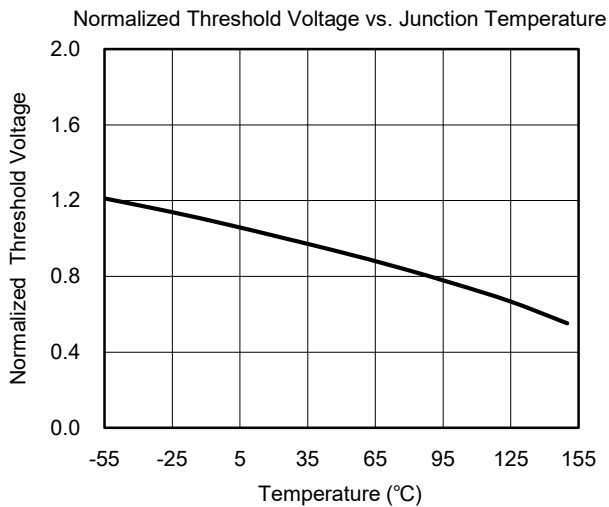
(T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Static OFF Characteristics							
Drain-to-Source Breakdown Voltage	V _{BR_DSS}	V _{GS} = 0V, I _D = 250μA		40			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0V, V _{DS} = 32V				10	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V				±100	nA
Static ON Characteristics							
Gate-to-Source Threshold Voltage	V _{GS_TH}	V _{GS} = V _{DS} , I _D = 250μA		1.2	1.6	2.2	V
Drain-to-Source On-State Resistance	R _{DSON}	V _{GS} = 10V, I _D = 50A			0.72	0.94	mΩ
		V _{GS} = 4.5V, I _D = 50A			1.05	1.47	
Forward Transconductance	g _{fs}	V _{DS} = 5V, I _D = 50A			103		S
Gate Resistance	R _G	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz			1.6		Ω
Diode Characteristics							
Diode Forward Voltage	V _{F_SD}	V _{GS} = 0V, I _S = 50A			0.7		V
Reverse Recovery Time	t _{RR}	V _{GS} = 0V, I _S = 50A, di/dt = 100A/μs			73		ns
Reverse Recovery Charge	Q _{RR}				125		nC
Dynamic Characteristics							
Input Capacitance	C _{ISS}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz			5385		pF
Output Capacitance	C _{OSS}				1562		
Reverse Transfer Capacitance	C _{RSS}				49		
Total Gate Charge	Q _g	V _{DS} = 20V, I _D = 50A	V _{GS} = 10V		102		nC
			V _{GS} = 4.5V		52		
Gate-to-Source Charge	Q _{GS}	V _{GS} = 4.5V, V _{DS} = 20V, I _D = 50A			16		nC
Gate-to-Drain Charge	Q _{GD}				20		
Switch Characteristics							
Turn-On Delay Time	t _{D_ON}	V _{GS} = 10V, V _{DS} = 20V, I _D = 50A, R _G = 3Ω			13		ns
Rise Time	t _r				52		
Turn-Off Delay Time	t _{D_OFF}				63		
Fall Time	t _f				78		

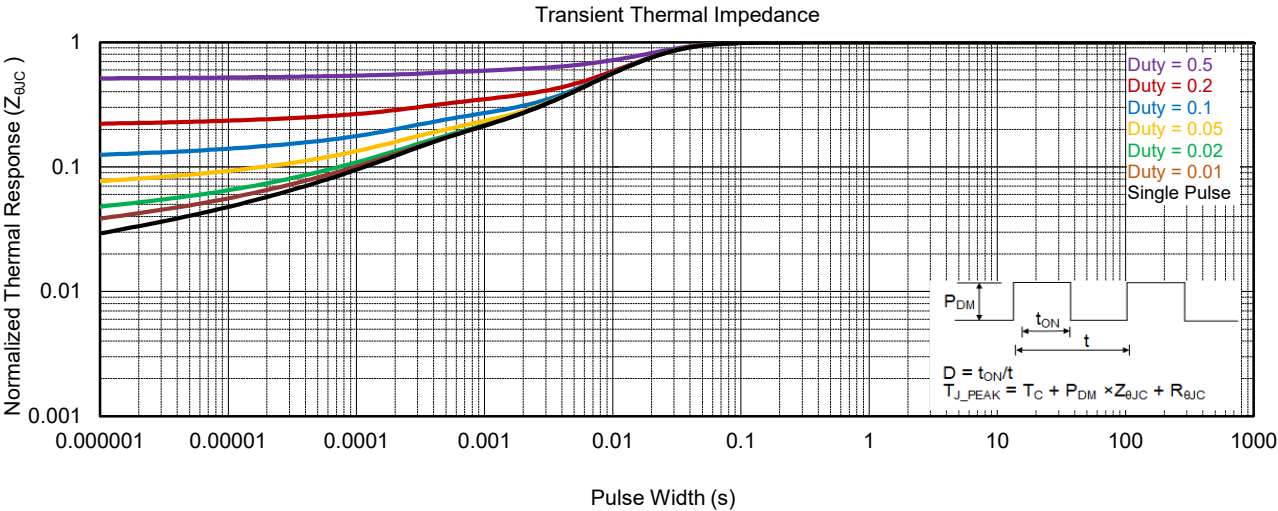
TYPICAL PERFORMANCE CHARACTERISTICS



TYPICAL PERFORMANCE CHARACTERISTICS (continued)



TYPICAL PERFORMANCE CHARACTERISTICS (continued)



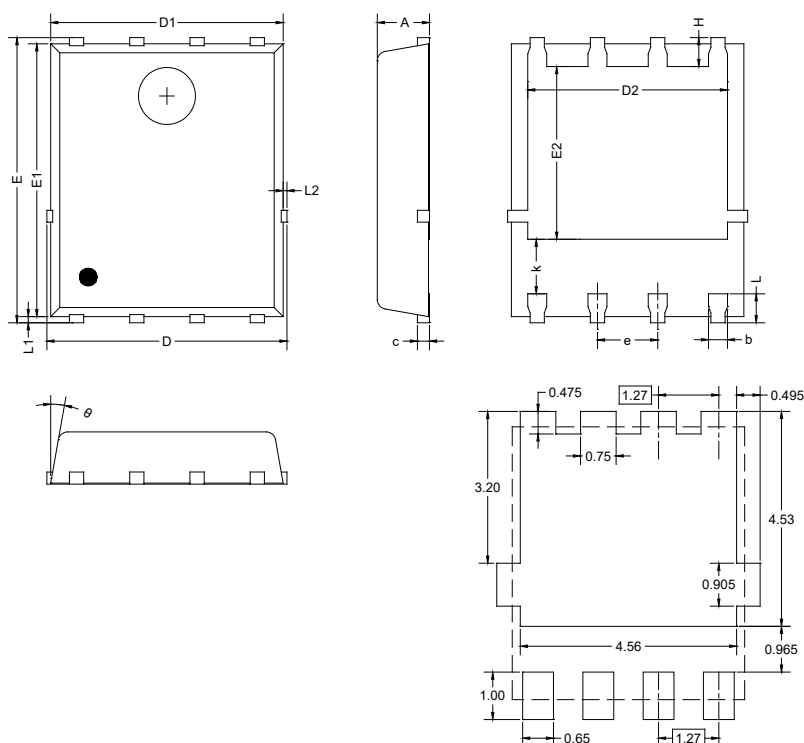
REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

JULY 2025 – REV.A to REV.A.1		Page
Updated package outline dimensions information		7
Changes from Original (NOVEMBER 2024) to REV.A		Page
Changed from product preview to production data		All

PACKAGE OUTLINE DIMENSIONS

PDFN-5×6-8CL



RECOMMENDED LAND PATTERN (Unit: mm)

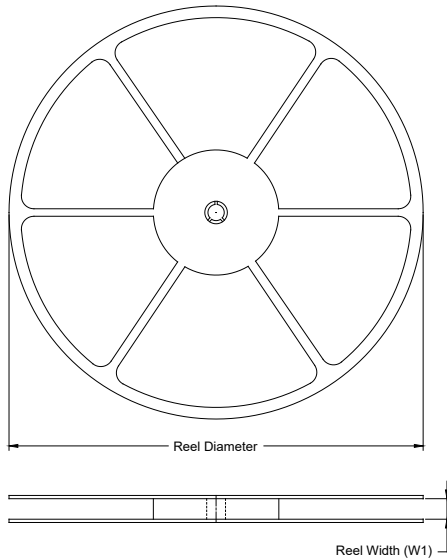
Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	1.000	1.100	1.200
b	0.300	0.400	0.500
c	0.150	0.250	0.350
D	4.800	-	5.400
D1	4.800	4.900	5.000
D2	4.000	4.210	4.400
E	5.900	6.000	6.250
E1	5.650	5.750	5.850
E2	3.500	3.640	3.900
e	1.270 BSC		
H	0.350	0.610	0.710
k	1.100	-	-
L	0.400	0.610	0.710
L1	0.050	0.130	0.250
L2	-	-	0.220
θ	8°	10°	12°

NOTE: This drawing is subject to change without notice.

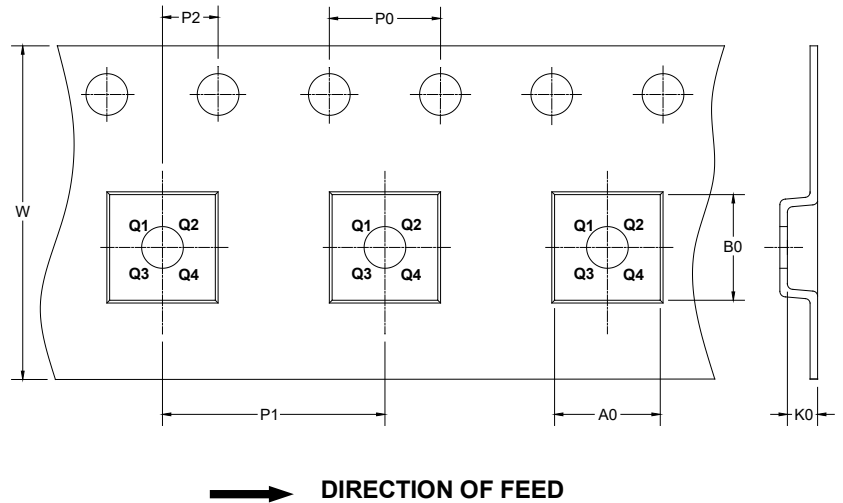
PACKAGE INFORMATION

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

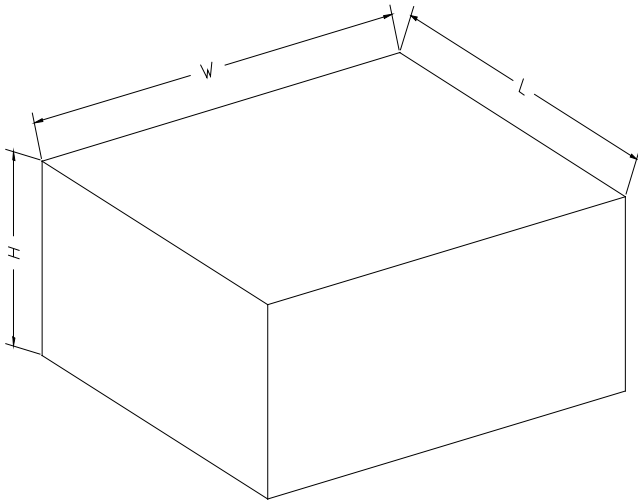
KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
PDFN-5×6-8CL	13"	12.4	6.45	5.30	1.40	4.0	8.0	2.0	12.0	Q1

DD0001

PACKAGE INFORMATION

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13"	386	280	370	5

DP0002