

SGM8772 High Voltage, High Precision, Dual Differential Comparator with V_{REF}

GENERAL DESCRIPTION

The SGM8772 is a dual, high precision differential voltage comparator with a voltage reference and a NAND gate. The device optimized for high voltage operation from 2.8V to 36V single supply or ± 1.4 V to ± 18 V dual power supplies. It consumes low supply current without being affected by the supply voltage. Input common mode voltage is 1.5V lower than ± 1.4 V lower than ± 1.4 V as a push-pull output structure without external pull-up resistors. This feature makes it a good choice for applications whose PCB sizes are limited. The SGM8772 also provides one window comparator output.

The SGM8772 is available in a Green MSOP-10 package. It is operated over the -40°C to +125°C temperature range.

FEATURES

Wide Supply Ranges
 Single Supply: 2.8V to 36V

Dual Supplies: ±1.4V to ±18V

• Low Supply Current: 420µA (TYP)

• Low Input Offset Voltage: 4mV (MAX)

• Low Input Bias Current: ±20pA (TYP)

• Minimum Input Common Mode Voltage: -V_S

Maximum Differential Input Voltage: +36V/-36V

• Push-Pull Output and Window Comparator Output

• Low Output Saturation Voltage

• Supports CMOS or TTL Logic

• -40°C to +125°C Operating Temperature Range

• Available in a Green MSOP-10 Package

APPLICATIONS

Power System Monitor
Medical Equipment
Industrial Application
Battery Management System

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM8772	MSOP-10	-40°C to +125°C	SGM8772XMS10G/TR	SGM8772 XMS10 XXXXX	Tape and Reel, 4000

MARKING INFORMATION

NOTE: XXXXX = Date Code, Trace Code and Vendor Code.



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, V _S 40V
Differential Input Voltage, V _{ID} 40V
Input Voltage Range (-Vs) - 0.3V to (+Vs) + 0.3V
Output Voltage Range, V_{OUT} (- V_S) - 0.3V to (+ V_S) + 0.3V
Output Voltage Range, V_{REF} (-V_S) - 0.3V to (-V_S) + 5.5V
Junction Temperature+150°C
Storage Temperature Range65°C to +150°C
Lead Temperature (Soldering, 10s)+260°C
ESD Susceptibility
HBM2500V
CDM1000V

RECOMMENDED OPERATING CONDITIONS

Operating Temperature Range-40°C to +125°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

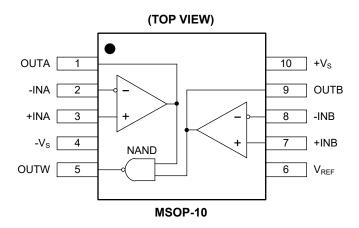
ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATION



PIN DESCRIPTION

PIN	NAME	FUNCTION				
1	OUTA	Output of Comparator A.				
2	-INA	Inverting Input of Comparator A.				
3	+INA	Non-Inverting Input of Comparator A.				
4	-Vs	Negative Power Supply.				
5	OUTW	utput of Window Comparator.				
6	V_{REF}	eference Output.				
7	+INB	Non-Inverting Input of Comparator B.				
8	-INB	Inverting Input of Comparator B.				
9	OUTB	Output of Comparator B.				
10	+V _S	Positive Power Supply.				

ELECTRICAL CHARACTERISTICS

 $(V_S = \pm 1.4V \text{ to } \pm 18V, \text{ Full} = -40^{\circ}\text{C} \text{ to } + 125^{\circ}\text{C}, \text{ typical values are at } T_A = +25^{\circ}\text{C}, \text{ unless otherwise noted.})$

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Innuit Official Voltage	V	V - 0V	+25°C		0.6	4	- mV
Input Offset Voltage	V _{os}	$V_{CM} = 0V$	Full			4.5	
Input Bias Current	I _B	V _{CM} = 0V	+25°C		±20	±200	pА
Input Offset Current	I _{os}	V _{CM} = 0V	+25°C		±20	±200	pА
Maximum lanut Difference Dies Comment		V = 140V V = 140V	+25°C		2.2	4	μА
Maximum Input Difference Bias Current	I _{ID}	$V_S = \pm 18V, V_{ID} = \pm 18V$	Full			5	
Input Common Mode Voltage Range (1)	V_{CM}		Full	-V _S		(+V _S) - 1.5V	V
Occurred Marks Balantina Batis	CMRR	V .40V/V (V/) +- (·)// A 5V/	+25°C	90	116		- dB
Common Mode Rejection Ratio		$V_S = \pm 18V$, $V_{CM} = (-V_S)$ to $(+V_S) - 1.5V$	Full	87			
	PSRR		+25°C	96	116		- dB
Power Supply Rejection Ratio		V _S = 2.8V to 36V	Full	93			
		V _{OH} I _{SOURCE} = 8mA, V _{ID} = 0.2V	+25°C		370	500	
	V _{OH}		Full			780	
Output Voltage Swing from Rail	.,		+25°C		210	280	mV
	V_{OL}	$I_{SINK} = 8mA$, $V_{ID} = -0.2V$	Full			400	
	I _{SOURCE}	$V_{OH} = (+V_S) - 1.5V, V_{ID} = 0.2V$	+25°C	17	24		
Output Short-Circuit Current	I _{SINK}	$V_{OL} = (-V_S) + 1.5V, V_{ID} = -0.2V$	+25°C	25	36		mA
T. 10 10 1			+25°C		420	500	μА
Total Supply Current	I _S	I _{OUT} = 0mA	Full			600	
Voltage Reference	V_{REF}	I _{REF} = 0 to 5mA	+25°C	(-V _s) + 1.205	(-V _s) + 1.225	(-V _S) + 1.245	V

SWITCHING CHARACTERISTICS

(At $T_A = +25$ °C, $V_S = \pm 2.5$ V, $C_L = 15$ pF ⁽²⁾, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS	
Decreasion Delevi (High to Leve)	4	Overdrive = 10mV	+25°C		85			
Propagation Delay (High to Low)	t _{PHL}	Overdrive = 100mV +25°C	50		ns			
Decreasion Delevitante Hink	4	Overdrive = 10mV	+25°C		95			
Propagation Delay (Low to High)	t _{PLH}	Overdrive = 100mV	+25°C		60		ns	
Fall Time		Overdrive = 10mV	+25°C		12		ns	
Fall Time	t _{FALL}	Overdrive = 100mV	+25°C		12			
Die a Time	t _{RISE}	Overdrive = 10mV	+25°C		12			
Rise Time		Overdrive = 100mV	+25°C		12		ns	

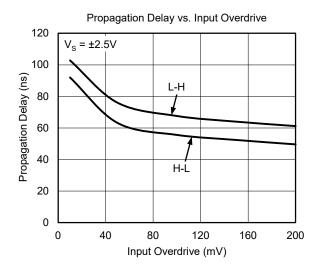
NOTES:

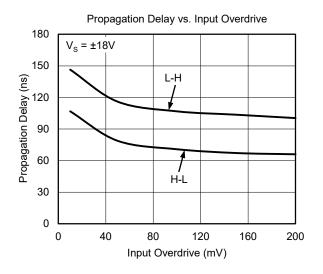
- 1. Any input voltage should not be lower than $(-V_S)$ 0.3V. The maximum input common mode voltage is $(+V_S)$ 1.5V, but it will not be damaged when the upper limit of the input voltage reaches 36V.
- 2. C_L: Load capacitance (jig and probe included).

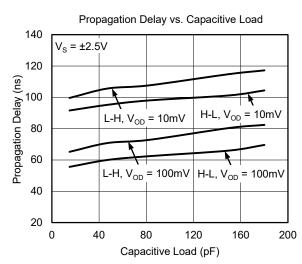


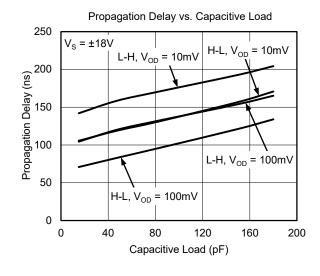
TYPICAL PERFORMANCE CHARACTERISTICS

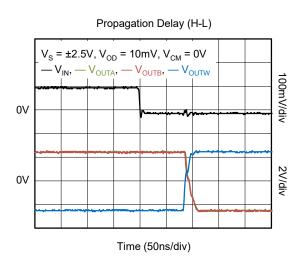
At $T_A = +25$ °C, $V_S = \pm 18$ V and $C_L = 15$ pF, unless otherwise noted.

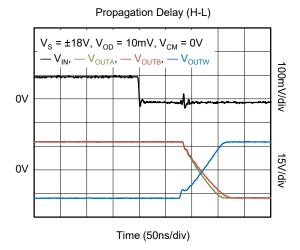






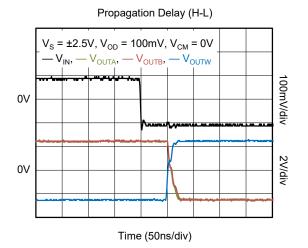


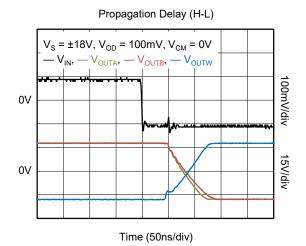


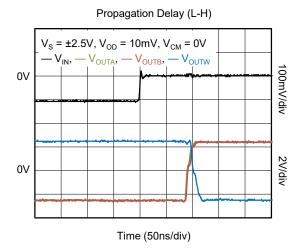


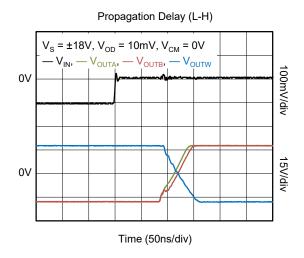
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

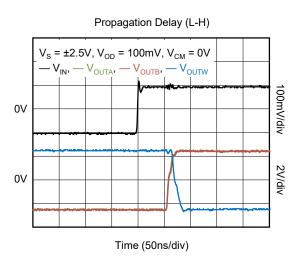
At T_A = +25°C, V_S = ±18V and C_L = 15pF, unless otherwise noted.

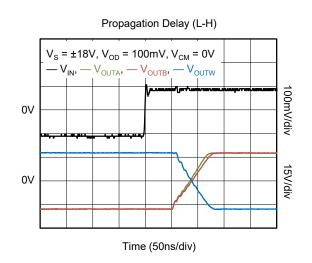






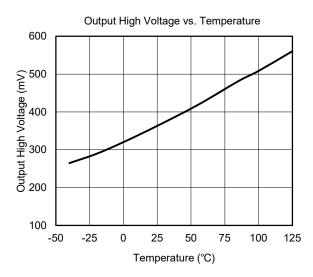


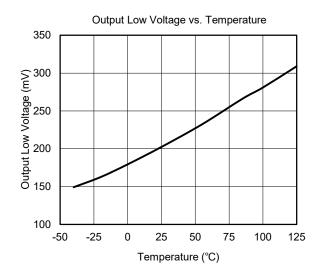


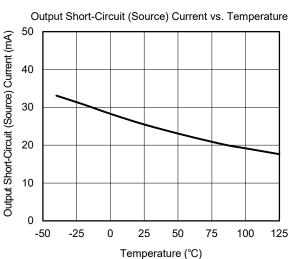


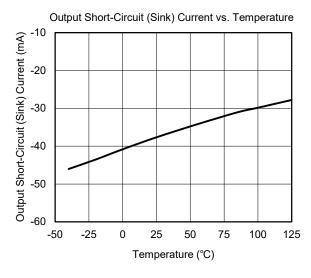
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

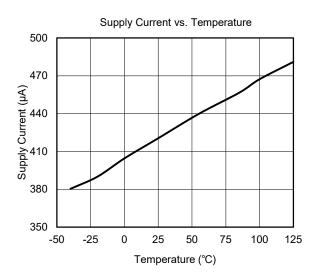
At T_A = +25°C, V_S = ±18V and C_L = 15pF, unless otherwise noted.

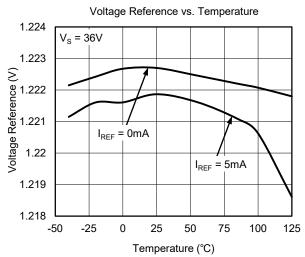






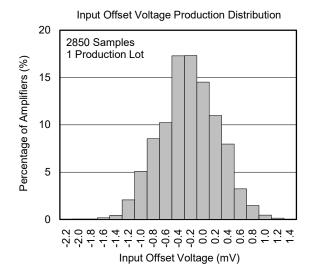






TYPICAL PERFORMANCE CHARACTERISTICS (continued)

At $T_A = +25$ °C, $V_S = \pm 18$ V and $C_L = 15$ pF, unless otherwise noted.





DETAILED DESCRIPTION

The SGM8772 is dual, high precision, low power comparator. The wide input voltage range and power supply range make the device a good choice for industrial equipment. Push-pull structure saves external pull-up resistors. The SGM8772 can be compatible with CMOS and TTL logics.

Output Structure

In Figure 1, the SGM8772 has a push-pull output stage. When output is changed from logic high/low to low/high, the changed sink/source current pulls/pushes output pin to logic low/high. Beginning this transition, larger sink/source current is used to create a high slew rate transit from high/low to low/high. Once the output voltage reaches $V_{\rm OL}/V_{\rm OH}$, it will reduce the sink/source current to a just right value to maintain the $V_{\rm OL}/V_{\rm OH}$ static condition. This current-driven push-pull output stage will significantly reduce the power consumption in application system. OUTA, OUTB and NAND gate form the third window comparator output.

If low slew rate transition is needed in system design, adjusting the load capacitance will change the slew rate. The heavier capacitive load will slow down the output voltage transition. This feature will be used to reduce the interference generated by fast edge of transition between 1 and 0 in noise-sensitive system.

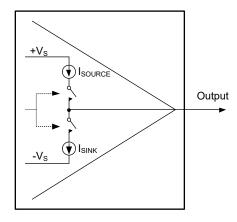


Figure 1. Push-Pull Output Structure

APPLICATION INFORMATION

Window Comparator

It's easy to design a window comparator by using SGM8772, and this design is shown in Figure 2.

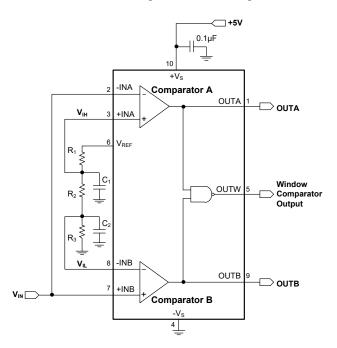


Figure 2. Window Comparator

Layout and Bypassing

Good power supply decoupling, layout and grounding are very important for SGM8772 to realize the full high-speed capabilities in system, following skills will be used:

- \bullet A 0.1µF to 4.7µF range ceramic capacitor is used to provide good power supply decoupling. This ceramic capacitor must be placed as close to $+V_S$ pin as possible.
- For grounding, unbroken and low-inductance ground plane is a good choice.
- For Layout, use short PCB trace to avoid unwanted parasitic feedback around the comparator. SGM8772 must be soldered directly to the PCB and the socket is not recommended.

SGM8772

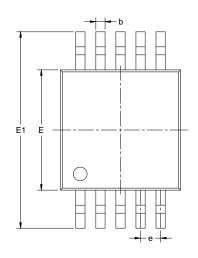
REVISION HISTORY

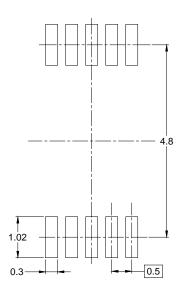
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

OCTOBER 2023 – REV.A to REV.A.1	Page
Updated Absolute Maximum Ratings section	2
Updated Recommended Operating Conditions section	2
Updated Pin Configuration section	3
Updated Pin Description section	3
Updated Electrical Characteristics section	4
Changes from Original (DECEMBER 2019) to REV.A	Page
Changed from product preview to production data	All

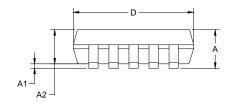


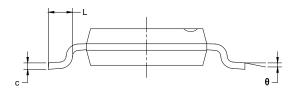
PACKAGE OUTLINE DIMENSIONS MSOP-10





RECOMMENDED LAND PATTERN (Unit: mm)





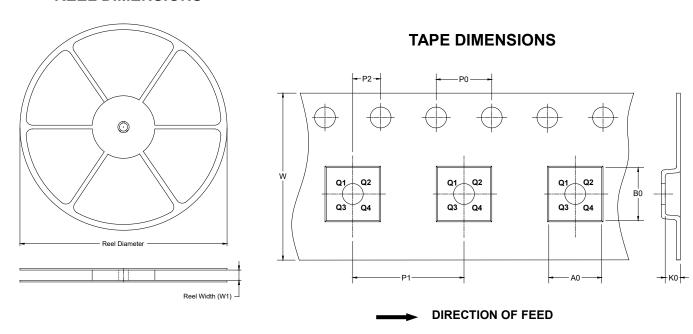
Symbol		nsions meters	Dimensions In Inches		
	MIN MAX		MIN	MAX	
А	0.820	1.100	0.032	0.043	
A1	0.020	0.150	0.001	0.006	
A2	0.750	0.950	0.030	0.037	
b	0.180	0.280	0.007	0.011	
С	0.090	0.230	0.004	0.009	
D	2.900	3.100	0.114	0.122	
Е	2.900	3.100	0.114	0.122	
E1	4.750 5.050		0.187	0.199	
е	0.500 BSC		0.020 BSC		
L	0.400	0.800	0.016	0.031	
θ	0°	6°	0°	6°	

NOTES:

- Body dimensions do not include mode flash or protrusion.
 This drawing is subject to change without notice.

TAPE AND REEL INFORMATION

REEL DIMENSIONS

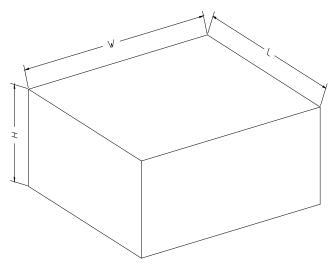


NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
MSOP-10	13"	12.4	5.20	3.30	1.50	4.0	8.0	2.0	12.0	Q1

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13"	386	280	370	5