

High Voltage, 2.4µA Ultra-Low Current, High Precision, Dual Differential Comparator

GENERAL DESCRIPTION

The SGM8779-2 series is a dual, rail-to-rail input and output, micro-power and high precision voltage comparator optimized for high voltage operation. The device can operate from 1.65V to 40V single supply or from $\pm 0.85V$ to $\pm 20V$ dual power supplies. The SGM8779-2 series supports rail-to-rail input while input signal range is from (-V_S) - 0.3V to (+V_S) + 0.3V and input common mode voltage range is from (-V_S) - 0.1V to (+V_S) + 0.1V. The SGM8779-2 series consumes only 2.4µA low supply current.

The SGM8779-2 series features low input offset voltage of ±2.2mV (MAX). It is suitable for applications requiring precision.

The SGM8779-2 series supports open-drain or push-pull output. The SGM8779A-2 has an open-drain output structure that needs external pull-up resistor. The SGM8779B-2 has a push-pull output structure, which is capable of sinking and sourcing milliamps of current when driving loads.

The SGM8779-2 series is available in Green SOIC-8 and MSOP-8 packages. It is rated over the -40°C to +125°C operating temperature range.

FEATURES

Wide Supply Ranges
 Single Supply: 1.65V to 40V

Dual Supplies: ±0.85V to ±20V

Ultra-Low Supply Current: 2.4μA (TYP)

Low Input Offset Voltage: ±2.2mV (MAX) at +25°C

Low Input Bias Current: ±200pA (MAX)

• Rail-to-Rail Input and Output

• Internal Hysteresis: ±2.5mV (TYP)

Common Mode Rejection Ratio: 105dB (TYP)
 Power Supply Rejection Ratio: 105dB (TYP)

Open-Drain Output: SGM8779A-2Push-Pull Output: SGM8779B-2

• Support CMOS or TTL Logic

• -40°C to +125°C Operating Temperature Range

• Available in Green SOIC-8 and MSOP-8 Packages

APPLICATIONS

Power System Monitor
Medical Equipment
Industrial Application
Battery Management System



PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM8779A-2	SOIC-8	-40°C to +125°C	SGM8779A-2XS8G/TR	SGM 8779A2XS8 XXXXX	Tape and Reel, 4000
(Open-Drain)	MSOP-8	-40°C to +125°C	SGM8779A-2XMS8G/TR	SGM10U XMS8 XXXXX	Tape and Reel, 4000
SGM8779B-2	SOIC-8	-40°C to +125°C	SGM8779B-2XS8G/TR	SGM 8779B2XS8 XXXXX	Tape and Reel, 4000
(Push-Pull)	MSOP-8	-40°C to +125°C	SGM8779B-2XMS8G/TR	SGM10V XMS8 XXXXX	Tape and Reel, 4000

MARKING INFORMATION

NOTE: XXXXX = Date Code, Trace Code and Vendor Code.

2	<u>(X)</u>	$\mathbf{X} \mathbf{X} \mathbf{X}$		
			. '	Vendor Code
			. '	Trace Code
				Date Code - Year

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, +V _S to -V _S	42V
Differential Input Voltage, V _{ID}	42V
Input/Output Voltage Range (-V _S) - 0.3V to (+V _S) + 0.3V
Junction Temperature	.+150°C
Storage Temperature Range65°C to	+150°C
Lead Temperature (Soldering, 10s)	.+260°C
ESD Susceptibility (1) (2)	
HBM (SGM8779A -2)	±2000V
HBM (SGM8779B-2)	V 0008±
CDM	±1000V

NOTES:

- 1. For human body model (HBM), all pins comply with ANSI/ESDA/JEDEC JS-001 specifications.
- 2. For charged device model (CDM), all pins comply with ANSI/ESDA/JEDEC JS-002 specifications.

RECOMMENDED OPERATING CONDITIONS

Power Supply Range	1.65V to 40V
Operating Temperature Range	40°C to +125°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

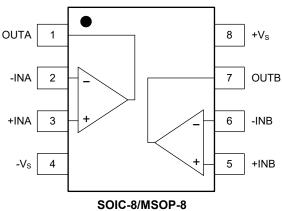
DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.



PIN CONFIGURATIONS

SGM8779A/B-2 (TOP VIEW)



ELECTRICAL CHARACTERISTICS (SGM8779A-2)

 $(V_S = 1.65V \text{ to } 40V, \text{ Full} = -40^{\circ}\text{C} \text{ to } +125^{\circ}\text{C}, \text{ typical values are at } T_A = +25^{\circ}\text{C}, \text{ unless otherwise noted.})$

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Input Offact Valtage		V = 0V	+25°C		±0.8	±2.2	mV
Input Offset Voltage	V _{os}	$V_{CM} = 0V$	Full			±2.5	IIIV
Input Bias Current		V _{CM} = 0V	+25°C		±10	±200	nΛ
Input Bias Current	l _B	VCM - UV	Full			6000	рA
Input Offset Current	Ios	V _{CM} = 0V	+25°C		±10	±200	рA
input Onset Current	ios	VCM - UV	Full			2000	pΑ
Input Common Mode Voltage Range (1)	V _{CM}		Full	(-V _S) - 0.1		(+V _S) + 0.1	٧
Common Mode Rejection Ratio	CMRR	$V_S = \pm 20V$, $V_{CM} = (-V_S)$ to $(+V_S)$	+25°C	93	105		dB
Common Mode Rejection Ratio	CIVIKK	VS - ±20V, VCM - (-VS) to (+VS)	Full	90			uБ
Power Supply Rejection Ratio	PSRR		+25°C	93	105		- dB
Power Supply Rejection Ratio			Full	90			
	V _{OL}	$V_S = 1.65V$, $I_{OL} = -8mA$, $V_{ID} = -0.2V$	+25°C		260	370	- mV
Low Lovel Output Voltage		V _S = 1.05V, I _{OL} = -8ITIA, V _{ID} = -0.2V	Full			520	
Low-Level Output Voltage		$V_S = 5V$ to 40V, $I_{OL} = -8mA$, $V_{ID} = -0.2V$	+25°C		140	180	
		V _S - 5V to 40V, I _{OL} 6ITIA, V _{ID} 0.2V	Full			300	
(2)	_	$V_S = 1.65V$, $V_{OL} = (-V_S) + 1.5V$, $V_{ID} = -0.2V$	+25°C	9	13		
Output Short-Circuit Current (2)	I _{SINK}	$V_S = 5V \text{ to } 40V, V_{OL} = (-V_S) + 1.5V, V_{ID} = -0.2V$	+25°C	51	62		mA
		V_{OH} - (- V_{S}) = 2.8 V , V_{ID} = 0.2 V	+25°C		40	75	
High-Level Output Current		VOH - (-VS) - 2.6V, VID - 0.2V	Full			100] _
Inigri-Level Output Current	I _{OH}	V_{OH} - (- V_{S}) = 36V, V_{ID} = 0.2V	+25°C		40	85	nA
		VoH - (-Vs) - 30V, VID - U.2V	Full			120]
Supply Current		-00	+25°C		2.4	2.6	
Supply Current	l _s	I _{OUT} = 0A	Full			4	μA
Input I hydaracia Valtaga			+25°C	0.8	2.5	3.8	mV
Input Hysteresis Voltage	V _{HYS}		Full			4.9	IIIV

NOTES:

- 1. Any input voltage should not be lower than $(-V_S)$ 0.3V. The maximum input common mode voltage is $(+V_S)$ + 0.1V.
- 2. For the MSOP-8 package, when the output is short-circuited to (+V_S), continuous shorting at high supply voltages may cause significant overheating. This can elevate the junction temperature beyond its maximum limit, potentially resulting in irreversible damage to the device.

ELECTRICAL CHARACTERISTICS (SGM8779B-2)

 $(V_S = 1.65V \text{ to } 40V, \text{ Full} = -40^{\circ}\text{C} \text{ to } +125^{\circ}\text{C}, \text{ typical values are at } T_A = +25^{\circ}\text{C}, \text{ unless otherwise noted.})$

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Innut Offset Valtage		\\ - 0\\	+25°C		±0.8	±2.2	mV
Input Offset Voltage	Vos	$V_{CM} = 0V$	Full			±2.5] ""
Input Bias Current	I _B	V _{CM} = 0V	+25°C		±10	±200	pA
input bias Current	IB	VCM - UV	Full			6000	PΑ
Input Offset Current	Ios	V _{CM} = 0V	+25°C		±10	±200	- pA
input onset ourient	ios	V CM — V V	Full			2000	PA
Input Common Mode Voltage Range (1)	V _{CM}		Full	(-V _s) - 0.1		(+V _S) + 0.1	V
Common Mada Rejection Retic	CMDD	\(- 120\(\) \(- \) \(\)	+25°C	93	105		- dB
Common Mode Rejection Ratio	CMRR	$V_S = \pm 20V$, $V_{CM} = (-V_S)$ to $(+V_S)$	Full	90			ab ab
Power Supply Rejection Ratio	PSRR		+25°C	93	105		dB
Power Supply Rejection Ratio	PSKK		Full	90			ub
	V _{OL}	V _S = 1.65V, I _{OL} = -8mA, V _{ID} = -0.2V	+25°C		260	370	mV
Low-Level Output Voltage		Vs - 1.03V, IOL0111A, VID0.2V	Full			520	
Low-Level Output Voltage		$V_S = 5V \text{ to } 40V, I_{OL} = -8\text{mA}, V_{ID} = -0.2V$	+25°C		140	180	
		V _S = 5V to 40V, 1 _{OL} = -0111A, V _{ID} = -0.2V	Full			300	
		$V_S = 1.65V$, $I_{OH} = 8mA$, $V_{ID} = 0.2V$	+25°C		560	850	mV
High-Level Output Voltage	V _{OH}	VS = 1.00 V, IOH = 0111A, VID = 0.2 V	Full			1200	
Tright Level Gulput Voltage	V OH	$V_S = 5V \text{ to } 40V, I_{OH} = 8\text{mA}, V_{ID} = 0.2V$	+25°C		420	600	
		VS OV 10 40 V, IOH OITM, VID 0.2V	Full			900	
		$V_S = 1.65V, V_{OL} = (-V_S) + 1.5V, V_{ID} = -0.2V$	+25°C	9	13		^
Output Short-Circuit Current (2)	I _{SINK}	$V_S = 5V \text{ to } 40V, V_{OL} = (-V_S) + 1.5V, V_{ID} = -0.2V$	+25°C	51	62		mA
Output Short-Circuit Current	_	$V_S = 1.65V$, $V_{OL} = (+V_S) - 1.5V$, $V_{ID} = 0.2V$	+25°C	9	16		
	I _{SOURCE}	$V_S = 5V \text{ to } 40V, V_{OL} = (+V_S) - 1.5V, V_{ID} = 0.2V$	+25°C	16	24		mA
			+25°C		2.4	2.6	
Supply Current	Is	I _{OUT} = 0A	Full			4	μA
Input I hystorogic Voltage	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		+25°C	0.8	2.5	3.8	m)/
Input Hysteresis Voltage	V _{HYS}		Full			4.9	mV

NOTES:

- 1. Any input voltage should not be lower than $(-V_S)$ 0.3V. The maximum input common mode voltage is $(+V_S)$ + 0.1V.
- 2. When the output is short-circuited to either $(-V_S)$ or $(+V_S)$, continuous shorting at high supply voltages may cause significant overheating. This can elevate the junction temperature beyond its maximum limit, potentially resulting in irreversible damage to the device.

SWITCHING CHARACTERISTICS (SGM8779A-2)

 $(V_S = 5V, V_{CM} = 0V \text{ or } (+V_S), C_L = 15pF, Full = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ typical values are at } T_A = +25^{\circ}C, \text{ unless otherwise noted.})$

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX (1)	UNITS
		$V_{OD} = 20$ mV, $R_{PUU} = 4.7$ k Ω	+25°C		3.5	5.5	
	t _{PLH}	V _{OD} – 20111V, N _{PULL} – 4.7K2	Full			6.5	
	L PLH	$V_{OD} = 100 \text{mV}, R_{PULL} = 4.7 \text{k}\Omega$	+25°C		2.5	4	
Propagation Delay		VOD - TOUTTV, RPULL - 4.7KS2	Full			5	
Propagation Delay	t _{PHL}	V _{OD} = 20mV	+25°C		3	4.5	μs
		V _{OD} – ZUIIV	Full			5	
		V = 100mV	+25°C		2	3	
		$V_{OD} = 100 \text{mV}$	Full			3.5	
Propagation Delay Mismatch	t _{SKEW}	V_{OD} = 20mV or 100mV, R_{PULL} = 4.7k Ω	Full		650	1500	ns
Maximum Toggle Frequency	f _{MAX}	$V_{CM} = 0V$, $R_{PULL} = 4.7k\Omega$	+25°C		250		kHz

NOTE:

1. Specified by design and characterization, not production tested.

SWITCHING CHARACTERISTICS (SGM8779B-2)

 $(V_S = 5V, V_{CM} = 0V \text{ or } (+V_S), C_L = 15pF, Full = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ typical values are at } T_A = +25^{\circ}C, \text{ unless otherwise noted.})$

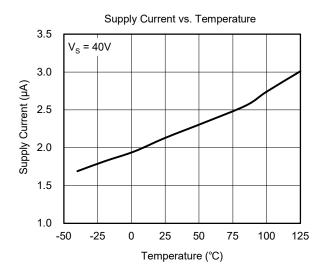
PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX (1)	UNITS
		V _{OD} = 20mV	+25°C		3.5	5.5	
		V _{OD} – ZOITIV	Full			7	
	t _{PLH}	V _{OD} = 100mV	+25°C		2.5	4	
		V _{OD} = 100mV	Full			5.5	
Propagation Delay	t _{PHL}	\\ = 20m\\	+25°C		3	4.5	μs
		$V_{OD} = 20 \text{mV}$	Full			5.5	
		V 400V	+25°C		2	3	
		V _{OD} = 100mV	Full			4.5	
Propagation Delay Mismatch	t _{SKEW}	V _{OD} = 20mV or 100mV	Full		500	1150	ns
Maximum Toggle Frequency	f _{MAX}	V _{CM} = 0V	+25°C		250		kHz

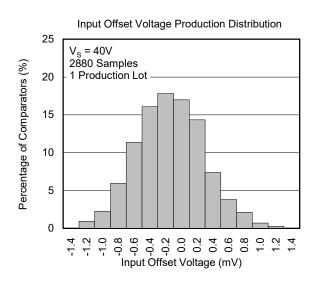
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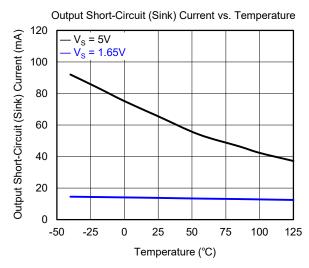
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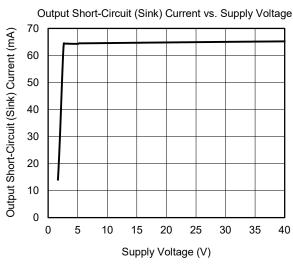
TYPICAL PERFORMANCE CHARACTERISTICS

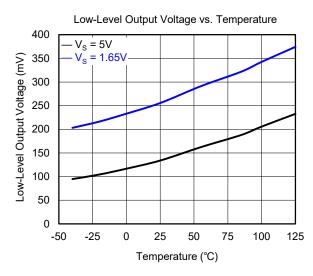
Performance measured with the SGM8779A/B-2 at T_A = +25°C, unless otherwise noted.

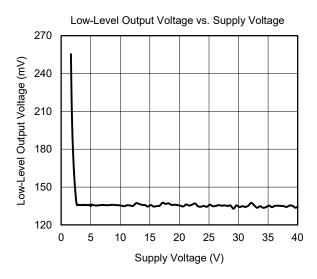




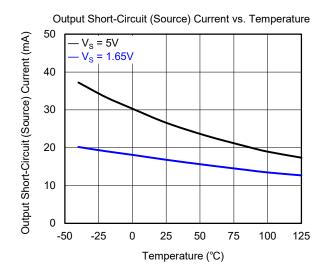


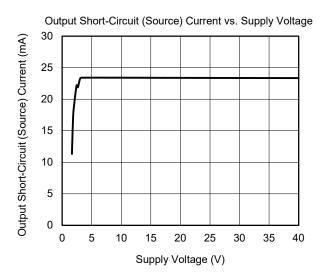


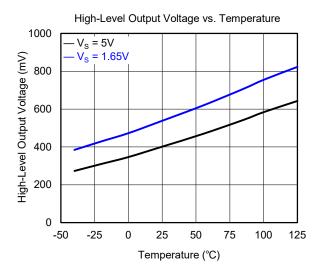


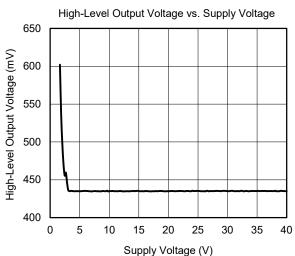


Performance measured with the SGM8779B-2 at $T_A = +25^{\circ}C$, unless otherwise noted.

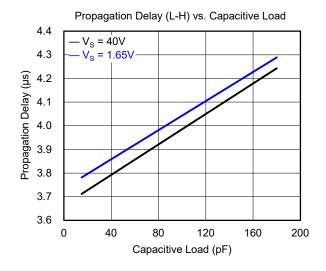


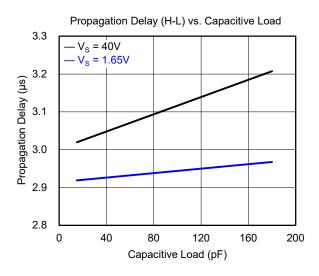


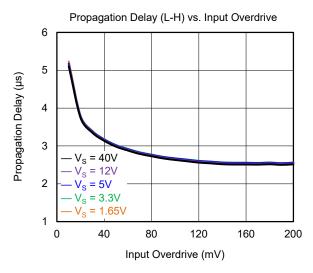


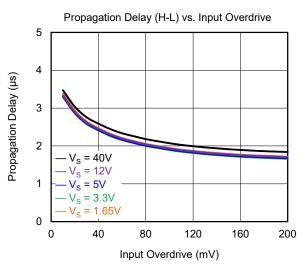


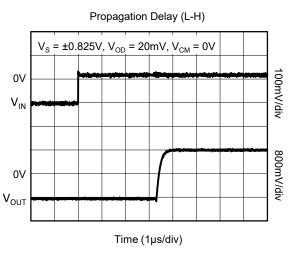
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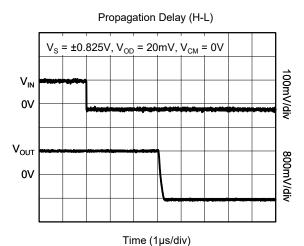




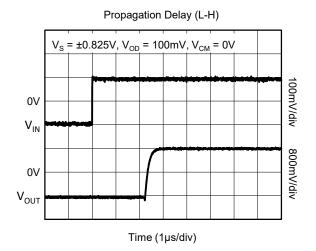


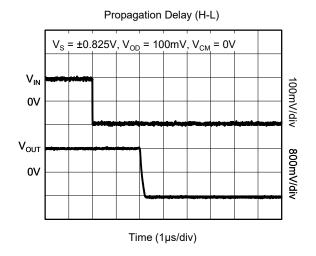


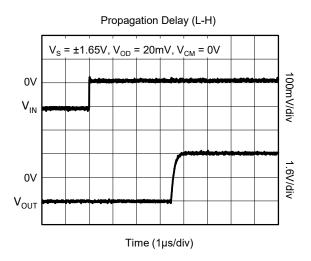


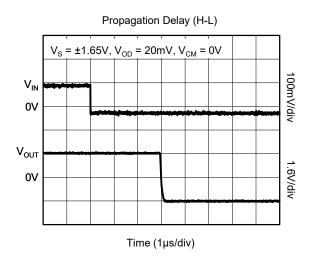


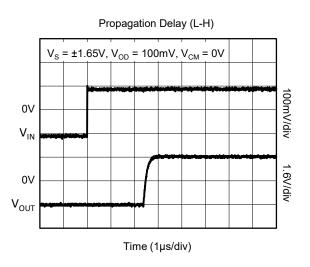
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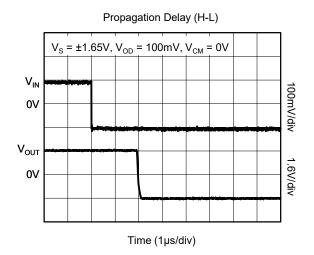




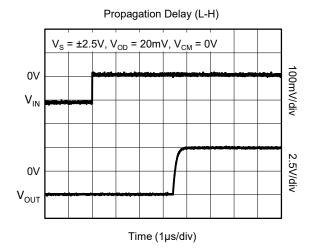


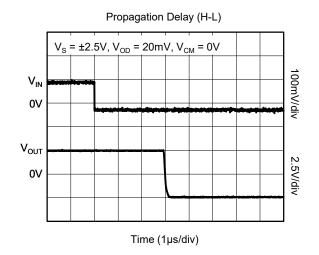


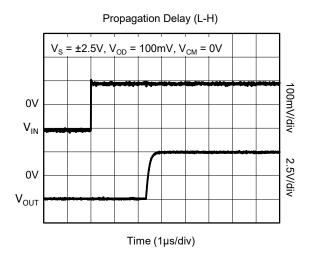


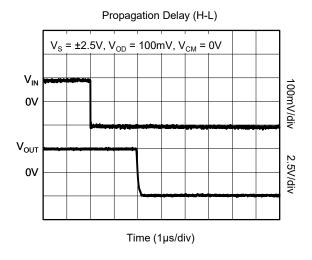


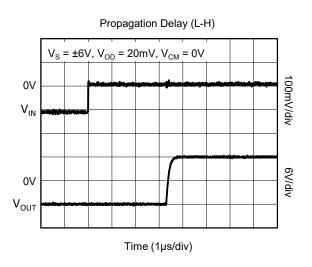
Performance measured with the SGM8779A-2 at $T_A = +25^{\circ}C$, unless otherwise noted.

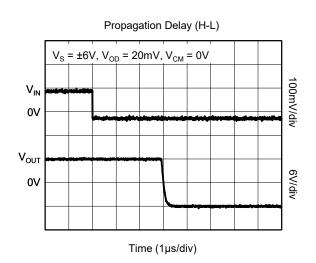




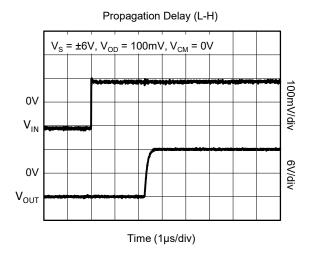


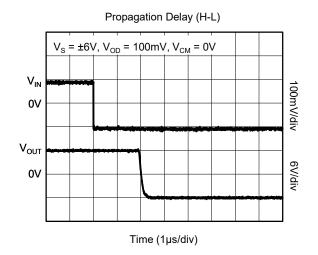


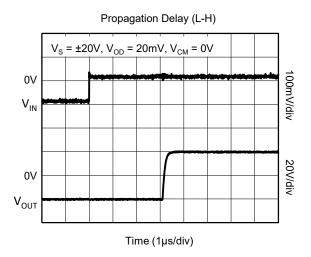


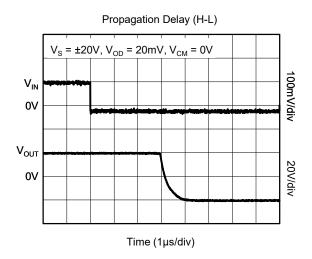


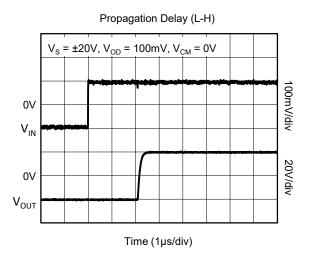
Performance measured with the SGM8779A-2 at $T_A = +25^{\circ}C$, unless otherwise noted.

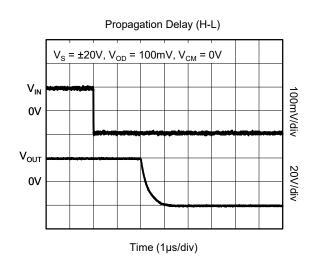




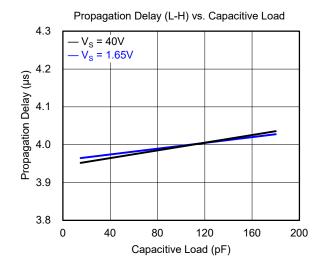


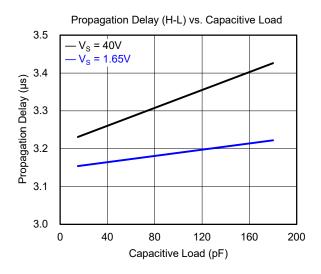


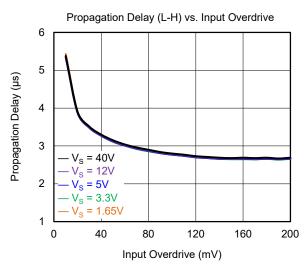


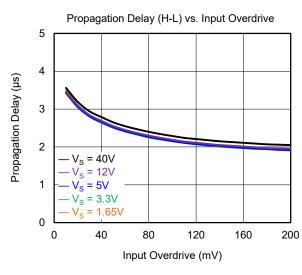


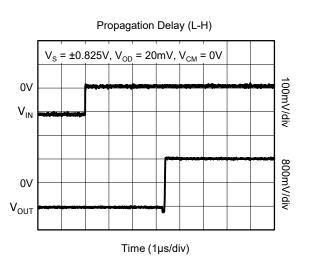
Performance measured with the SGM8779B-2 at $T_A = +25^{\circ}C$, unless otherwise noted.

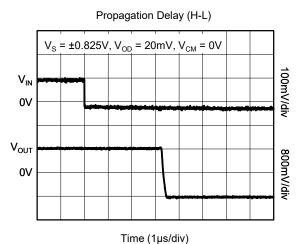




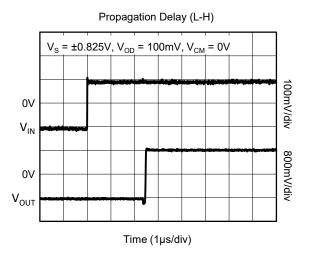


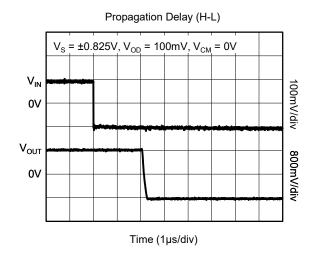


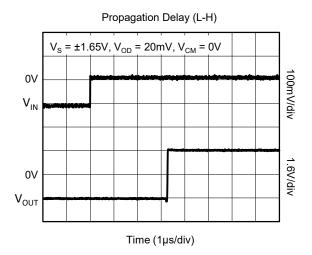


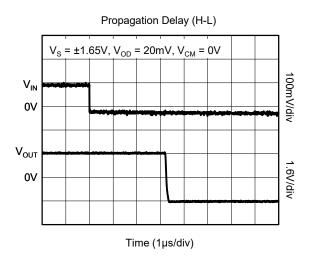


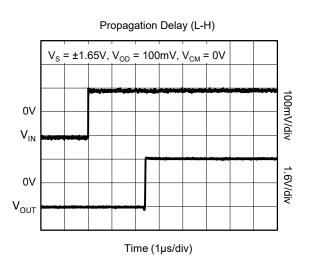
Performance measured with the SGM8779B-2 at T_A = +25°C, unless otherwise noted.

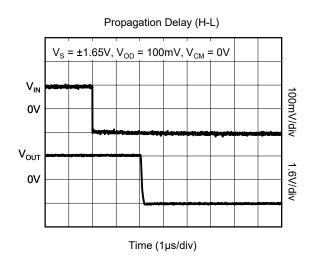




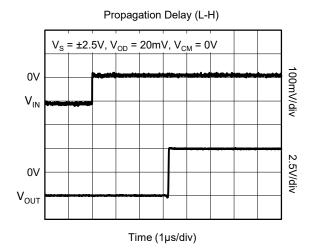


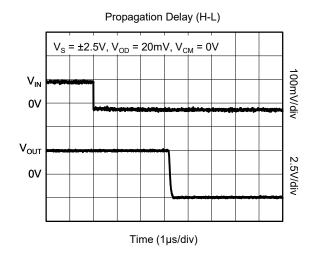


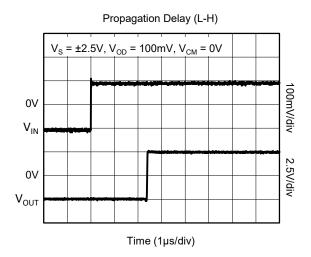


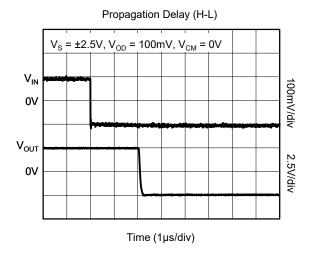


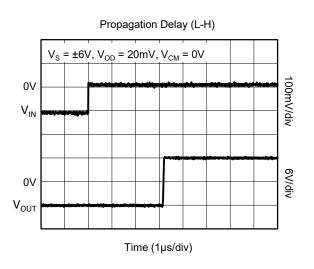
Performance measured with the SGM8779B-2 at T_A = +25°C, unless otherwise noted.

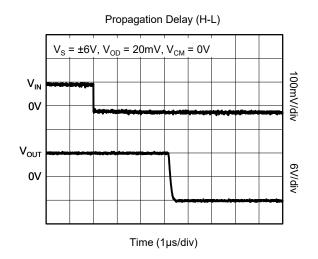




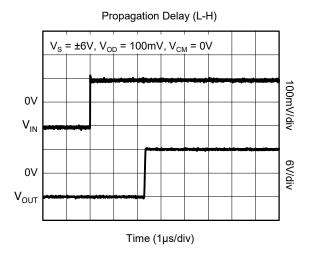


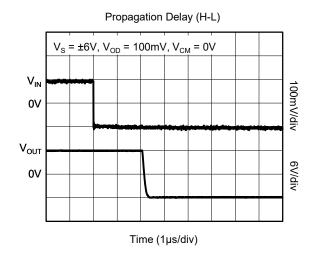


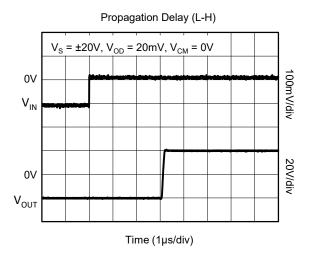


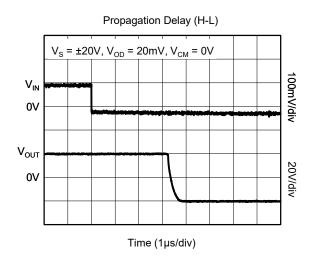


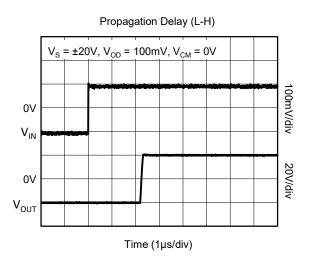
Performance measured with the SGM8779B-2 at $T_A = +25^{\circ}C$, unless otherwise noted.

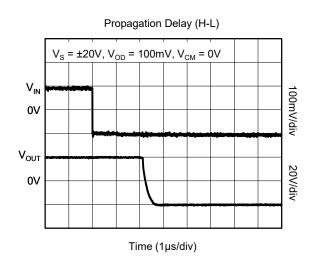












DETAILED DESCRIPTION

The SGM8779-2 series is a dual, rail-to-rail input, high precision, low power comparator. The wide input voltage range and power supply range make the device a good choice for industrial equipment. The SGM8779A-2 has an open-drain output structure that needs external pull-up resistor. The SGM8779B-2 has a push-pull output structure without external circuits. The SGM8779-2 can be compatible with CMOS and TTL logics.

Output Structure

In Figure 1, the SGM8779A-2 has a current-driven open-drain output stage. When output is changed from logic high to low, the changed sink current pulls output pin to logic low. Beginning this transition, larger sink current is used to create a high slew rate transit from high to low. Once the output voltage reaches V_{OL} , it will reduce the sink current to a just right value to maintain the V_{OL} static condition. In Figure 2, the SGM8779B-2 has a current-driven push-pull output stage. In addition to the above capability of sinking current, it is also capable of sourcing current when driving loads. This current-driven output stage will significantly reduce the power consumption in application system.

If low slew rate transition is needed in system design, adjusting the load capacitance will change the slew rate. The heavier capacitive load will slow down the output voltage transition. This feature will be used to reduce the interference generated by fast edge of transition between 1 and 0 in noise-sensitive system.

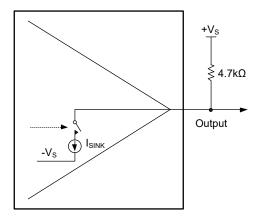


Figure 1. SGM8779A-2 Open-Drain Output Structure

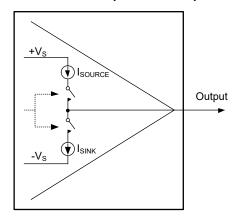


Figure 2. SGM8779B-2 Push-Pull Output Structure

APPLICATION INFORMATION

Layout and Bypassing

Good power supply decoupling, layout and grounding are very important for SGM8779-2 to realize the full high-speed capabilities in system, following skills will be used:

- A $0.1\mu F$ to $4.7\mu F$ range ceramic capacitor is used to provide good power supply decoupling. This ceramic capacitor must be placed as close to +V_S pin as possible.
- For grounding, unbroken and low-inductance ground plane is a good choice.
- For Layout, use short PCB trace to avoid unwanted parasitic feedback around the comparator. SGM8779-2 must be soldered directly to the PCB and the socket is not recommended.

High Voltage, 2.4µA Ultra-Low Current, High Precision, Dual Differential Comparator

SGM8779-2

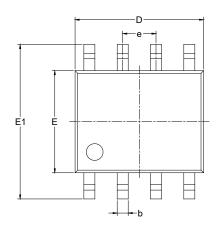
REVISION HISTORY

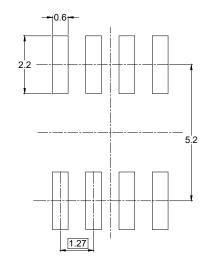
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

SEPTEMBER 2024 – REV.A to REV.A.1	Page
Added MSOP-8 package	All
Updated Absolute Maximum Ratings section	2
Updated Electrical Characteristics section	4
Changes from Original (JUNE 2024) to REV.A	Page
Changed from product preview to production data	All

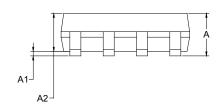


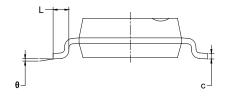
PACKAGE OUTLINE DIMENSIONS SOIC-8





RECOMMENDED LAND PATTERN (Unit: mm)





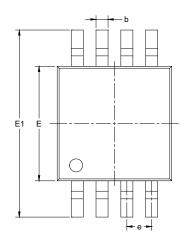
Symbol	-	nsions meters	Dimensions In Inches			
	MIN	MAX	MIN	MAX		
Α	1.350	1.750	0.053	0.069		
A1	0.100	0.250	0.004	0.010		
A2	1.350	1.550	0.053	0.061		
b	0.330	0.510	0.013	0.020		
С	0.170	0.250	0.006	0.010		
D	4.700	5.100	0.185	0.200		
E	3.800	4.000	0.150	0.157		
E1	5.800	6.200	0.228	0.244		
е	1.27	BSC	0.050	BSC		
L	0.400	1.270	0.016	0.050		
θ	0°	8°	0°	8°		

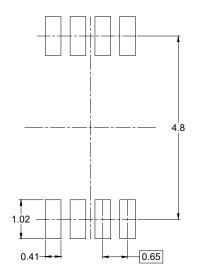
- NOTES:

 1. Body dimensions do not include mode flash or protrusion.

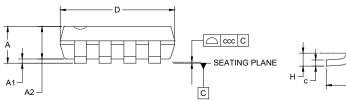
 2. This drawing is subject to change without notice.

PACKAGE OUTLINE DIMENSIONS MSOP-8





RECOMMENDED LAND PATTERN (Unit: mm)



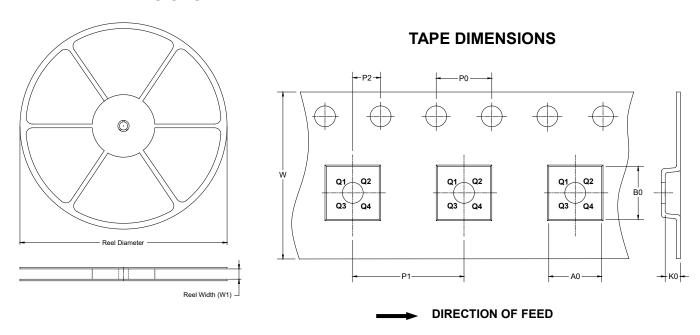


Cymphal	Dir	nensions In Millimet	ers			
Symbol	MIN	NOM	MAX			
Α	-	-	1.100			
A1	0.000	-	0.150			
A2	0.750	-	0.950			
b	0.220	0.220 - 0.3				
С	0.080	-	0.230			
D	2.800	-	3.200			
Е	2.800	-	3.200			
E1	4.650	-	5.150			
е		0.650 BSC				
L	0.400 - 0.800					
Н	0.250 TYP					
θ	0°	-	8°			
ccc	0.100					

- This drawing is subject to change without notice.
 The dimensions do not include mold flashes, protrusions or gate burrs.
- 3. Reference JEDEC MO-187.

TAPE AND REEL INFORMATION

REEL DIMENSIONS

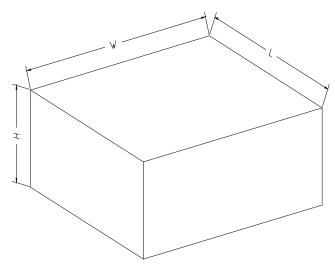


NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOIC-8	13″	12.4	6.40	5.40	2.10	4.0	8.0	2.0	12.0	Q1
MSOP-8	13"	12.4	5.20	3.30	1.50	4.0	8.0	2.0	12.0	Q1

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13"	386	280	370	5