

GENERAL DESCRIPTION

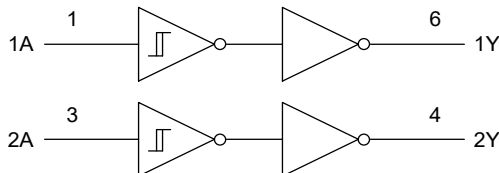
The 74LVC2G17 is a dual Schmitt-Trigger buffer which can accept supply voltage range from 1.65V to 5.5V.

The device features two independent buffers, but it may have different input threshold levels for positive-going (V_{T+}) and negative-going (V_{T-}) signals due to Schmitt-Trigger action. The device implements the Boolean function $Y = A$.

This device is highly suitable for partial power-down applications by using power-off leakage current (I_{OFF}) circuit. When the device is powered down, the outputs are disabled, and the current backflow can be prevented from passing through the device.

The 74LVC2G17 is available in Green SC70-6, SOT-23-6, XTDFN-1×1-6L and UTDFN-1.45×1-6AL packages. It operates over a temperature range of -40°C to $+125^{\circ}\text{C}$.

LOGIC DIAGRAM



FEATURES

- **Wide Operating Voltage Range: 1.65V to 5.5V**
- **Inputs Accept Voltages up to 5.5V**
- **+24mA/-24mA Output Current at $V_{CC} = 3.0\text{V}$**
- **Low Power Dissipation: $I_{CC} = 2\mu\text{A}$ (MAX)**
- **Propagation Delay: $t_{PD} = 7.5\text{ns}$ (TYP) at $V_{CC} = 3.3\text{V}$**
- **Support Partial Power-Down Mode**
- **-40°C to $+125^{\circ}\text{C}$ Operating Temperature Range**
- **Available in Green SC70-6, SOT-23-6, XTDFN-1×1-6L and UTDFN-1.45×1-6AL Packages**

APPLICATIONS

Audio Equipment
 LCD TV
 Panel
 Computing: Server, PC, Pad, Notebook, SSD
 Smartphone
 Telecom Equipment

FUNCTION TABLE

INPUT	OUTPUT
nA	nY
H	H
L	L

Y = A

H = High Voltage Level

L = Low Voltage Level

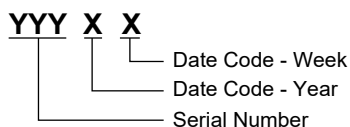
PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
74LVC2G17	SC70-6	-40°C to +125°C	74LVC2G17XC6G/TR	0A9XX	Tape and Reel, 3000
	SOT-23-6	-40°C to +125°C	74LVC2G17XN6G/TR	0ACXX	Tape and Reel, 3000
	XTDFN-1×1-6L	-40°C to +125°C	74LVC2G17XXDM6G/TR	5X	Tape and Reel, 10000
		-40°C to +125°C	74LVC2G17XXDM6SG/TR	5X	Tape and Reel, 5000
	UTDFN-1.45×1-6AL	-40°C to +125°C	74LVC2G17XUDL6G/TR	0CX	Tape and Reel, 5000

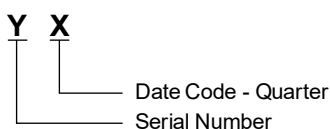
MARKING INFORMATION

NOTE: XX = Date Code. X = Date Code.

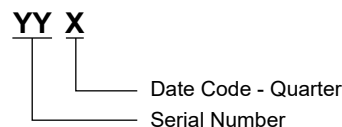
SC70-6/SOT-23-6



XTDFN-1×1-6L



UTDFN-1.45×1-6AL



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS ⁽¹⁾

Supply Voltage Range, V_{CC}	-0.5V to 6.5V
Input Voltage Range, V_I ⁽²⁾	-0.5V to 6.5V
Output Voltage Range, V_O ⁽²⁾	
Active Mode	-0.5V to MIN(6.5V, $V_{CC} + 0.5V$)
Power-Down Mode ($V_{CC} = 0V$)	-0.5V to 6.5V
Input Clamp Current, I_{IK} ($V_I < 0V$)	-50mA
Output Clamp Current, I_{OK} ($V_O < 0V$)	-50mA
Continuous Output Current, I_O	$\pm 50mA$
Continuous Current through V_{CC} or GND	$\pm 100mA$
Junction Temperature ⁽³⁾	+150°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10s)	+260°C
ESD Susceptibility	
HBM	5000V
CDM	1000V

RECOMMENDED OPERATING CONDITIONS

Supply Voltage Range, V_{CC}	1.65V to 5.5V
Input Voltage Range, V_I	0V to 5.5V
Output Voltage Range, V_O	
Active Mode	0V to V_{CC}
Power-Down Mode ($V_{CC} = 0V$)	0V to 5.5V
Operating Temperature Range	-40°C to +125°C

OVERSTRESS CAUTION

- Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.
- The input and output voltage ratings may be exceeded if the input and output clamp current ratings are observed.
- The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability.

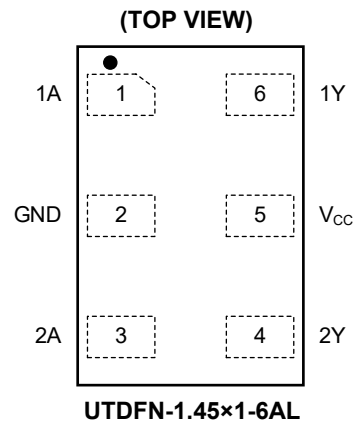
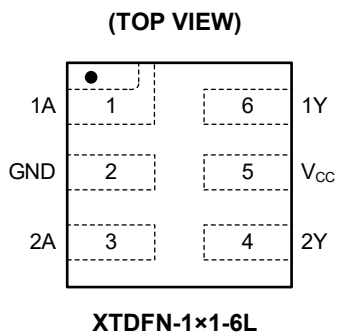
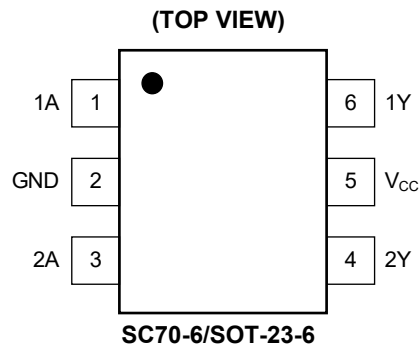
ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATIONS



PIN DESCRIPTION

PIN	NAME	FUNCTION
1, 3	1A, 2A	Data Inputs.
2	GND	Ground.
4, 6	2Y, 1Y	Data Outputs.
5	V _{CC}	Supply Voltage.

ELECTRICAL CHARACTERISTICS(Full = -40°C to +125°C, all typical values are measured at $T_A = +25^\circ\text{C}$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Positive-Going Input Threshold Voltage	V_{T+}	$V_{CC} = 1.65\text{V}$	Full	0.7	1.03	1.4	V
		$V_{CC} = 2.3\text{V}$	Full	1.0	1.46	1.7	
		$V_{CC} = 3.0\text{V}$	Full	1.3	1.88	2.1	
		$V_{CC} = 4.5\text{V}$	Full	1.9	2.65	3.1	
		$V_{CC} = 5.5\text{V}$	Full	2.2	3.12	3.33	
Negative-Going Input Threshold Voltage	V_{T-}	$V_{CC} = 1.65\text{V}$	Full	0.3	0.55	0.7	V
		$V_{CC} = 2.3\text{V}$	Full	0.4	0.81	1.0	
		$V_{CC} = 3.0\text{V}$	Full	0.8	1.08	1.3	
		$V_{CC} = 4.5\text{V}$	Full	1.1	1.61	2.0	
		$V_{CC} = 5.5\text{V}$	Full	1.4	1.96	2.5	
Hysteresis ($V_{T+} - V_{T-}$)	ΔV_T	$V_{CC} = 1.65\text{V}$	Full	0.35	0.49	0.61	V
		$V_{CC} = 2.3\text{V}$	Full	0.53	0.65	0.74	
		$V_{CC} = 3.0\text{V}$	Full	0.68	0.80	0.93	
		$V_{CC} = 4.5\text{V}$	Full	0.89	1.04	1.19	
		$V_{CC} = 5.5\text{V}$	Full	1.01	1.17	1.36	
High-Level Output Voltage	V_{OH}	$V_{CC} = 1.65\text{V to } 5.5\text{V}, I_{OH} = -100\mu\text{A}$	Full	$V_{CC} - 0.1$	$V_{CC} - 0.01$		V
		$V_{CC} = 1.65\text{V}, I_{OH} = -4\text{mA}$	Full	1.2	1.55		
		$V_{CC} = 2.3\text{V}, I_{OH} = -8\text{mA}$	Full	1.9	2.15		
		$V_{CC} = 2.7\text{V}, I_{OH} = -12\text{mA}$	Full	2.3	2.55		
		$V_{CC} = 3.0\text{V}, I_{OH} = -16\text{mA}$	Full	2.4	2.80		
		$V_{CC} = 3.0\text{V}, I_{OH} = -24\text{mA}$	Full	2.3	2.75		
		$V_{CC} = 4.5\text{V}, I_{OH} = -32\text{mA}$	Full	3.8	4.25		
Low-Level Output Voltage	V_{OL}	$V_{CC} = 1.65\text{V to } 5.5\text{V}, I_{OL} = 100\mu\text{A}$	Full		0.01	0.10	V
		$V_{CC} = 1.65\text{V}, I_{OL} = 4\text{mA}$	Full		0.10	0.45	
		$V_{CC} = 2.3\text{V}, I_{OL} = 8\text{mA}$	Full		0.15	0.30	
		$V_{CC} = 2.7\text{V}, I_{OL} = 12\text{mA}$	Full		0.20	0.30	
		$V_{CC} = 3.0\text{V}, I_{OL} = 16\text{mA}$	Full		0.25	0.40	
		$V_{CC} = 3.0\text{V}, I_{OL} = 24\text{mA}$	Full		0.30	0.55	
		$V_{CC} = 4.5\text{V}, I_{OL} = 32\text{mA}$	Full		0.35	0.55	
Input Leakage Current	I_I	$V_{CC} = 0\text{V to } 5.5\text{V}, V_I = 5.5\text{V or GND}$	Full		± 0.1	± 2	μA
Power-Off Leakage Current	I_{OFF}	$V_{CC} = 0\text{V}, V_I = 5.5\text{V or } V_O = 5.5\text{V}$	Full		± 0.1	± 2	μA
Supply Current	I_{CC}	$V_{CC} = 1.65\text{V to } 5.5\text{V}, V_I = 5.5\text{V or GND}, I_O = 0\text{A}$	Full		0.1	2	μA
Additional Supply Current	ΔI_{CC}	One input at $V_{CC} - 0.6\text{V}$, other inputs at V_{CC} or GND, $V_{CC} = 3.0\text{V to } 5.5\text{V}$	Full		0.1	5	μA
Input Capacitance	C_I	$V_{CC} = 3.3\text{V}, V_I = V_{CC}$ or GND	+25°C		4		pF

DYNAMIC CHARACTERISTICS

(See Figure 1 for test circuit. Full = -40°C to +125°C, all typical values are measured at T_A = +25°C and V_{CC} = 1.8V, 2.5V, 3.3V and 5.0V respectively, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN ⁽¹⁾	TYP	MAX ⁽¹⁾	UNITS	
Propagation Delay ⁽²⁾	t _{PD}	nA to nY, see Figure 2	V _{CC} = 1.8V ± 0.15V	Full	1.0	10.5	18.0	ns
			V _{CC} = 2.5V ± 0.2V	Full	0.5	8.0	12.0	
			V _{CC} = 3.3V ± 0.3V	Full	0.5	7.5	10.5	
			V _{CC} = 5.0V ± 0.5V	Full	0.5	7.0	10.0	
Power Dissipation Capacitance ⁽³⁾	C _{PD}	f = 10MHz	V _{CC} = 1.8V	+25°C		44		pF
			V _{CC} = 2.5V	+25°C		37		
			V _{CC} = 3.3V	+25°C		34		
			V _{CC} = 5.0V	+25°C		35		

NOTES:

- Specified by design and characterization, not production tested.
- t_{PD} is the same as t_{PLH} and t_{PHL}.
- C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$$

where:

f_i = Input frequency in MHz.

f_o = Output frequency in MHz.

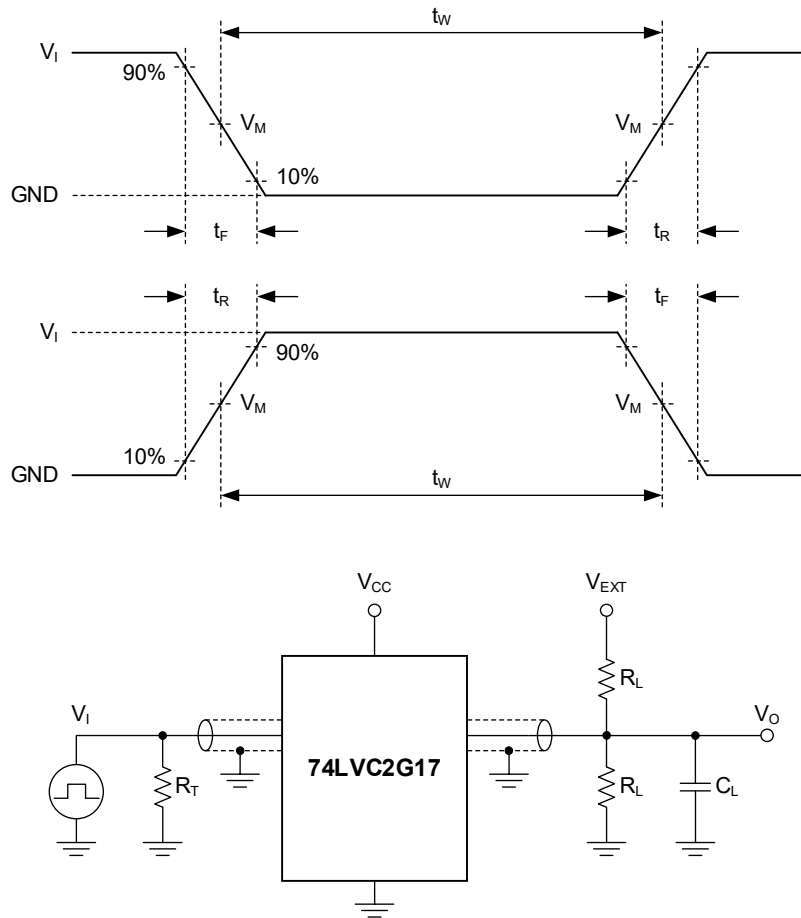
C_L = Output load capacitance in pF.

V_{CC} = Supply voltage in Volts.

N = Number of inputs switching.

Σ(C_L × V_{CC}² × f_o) = Sum of outputs.

TEST CIRCUIT



Test conditions are given in Table 1.

Definitions for test circuit:

R_L : Load resistance.

C_L : Load capacitance (includes jig and probe).

R_T : Termination resistance (equals to output impedance Z_O of the pulse generator).

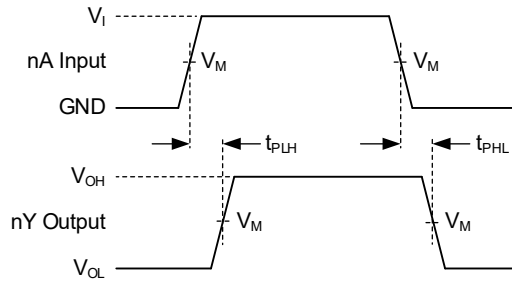
V_{EXT} : External voltage is used to measure switching time.

Figure 1. Test Circuit for Measuring Switching Times

Table 1. Test Conditions

SUPPLY VOLTAGE	INPUT		LOAD		V_{EXT}
V_{CC}	V_I	t_R, t_F	C_L	R_L	t_{PLH}, t_{PHL}
$1.8V \pm 0.15V$	V_{CC}	$\leq 2.0ns$	30pF	1k Ω	Open
$2.5V \pm 0.2V$	V_{CC}	$\leq 2.0ns$	30pF	500 Ω	Open
$3.3V \pm 0.3V$	3V	$\leq 2.5ns$	50pF	500 Ω	Open
$5.0V \pm 0.5V$	V_{CC}	$\leq 2.5ns$	50pF	500 Ω	Open

WAVEFORMS



Test conditions are given in Table 1.

Measurement points are given in Table 2.

Logic levels: VOL and VOH are typical output voltage levels that occur with the output load.

Figure 2. Input nA to Output nY Propagation Delay Times

Table 2. Measurement Points

SUPPLY VOLTAGE	INPUT		OUTPUT
V _{CC}	V _I	V _M ⁽¹⁾	V _M
1.8V ± 0.15V	V _{CC}	0.5 × V _{CC}	0.5 × V _{CC}
2.5V ± 0.2V	V _{CC}	0.5 × V _{CC}	0.5 × V _{CC}
3.3V ± 0.3V	3V	1.5V	1.5V
5.0V ± 0.5V	V _{CC}	0.5 × V _{CC}	0.5 × V _{CC}

NOTE:

1. The measurement points should be VIH or VIL when the input rising or falling time exceeds 2.5ns.

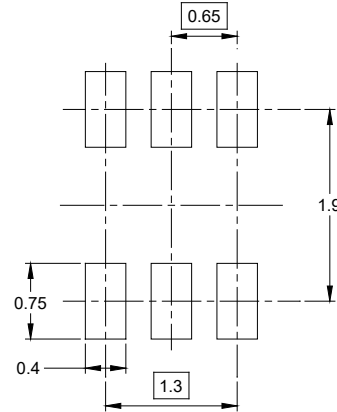
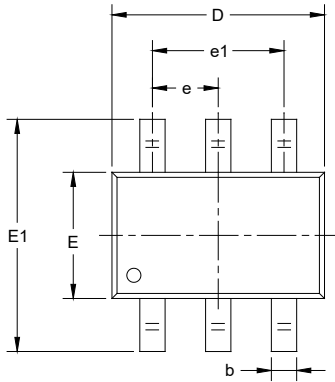
REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

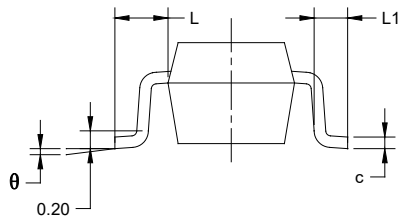
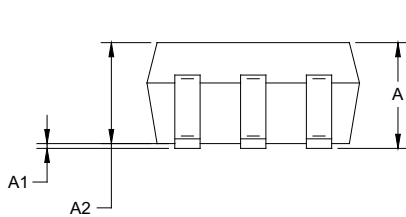
DECEMBER 2025 – REV.A.1 to REV.A.2	Page
Updated Package/Ordering Information of XTDFN-1×1-6L	2
Updated Tape and Reel Information of XTDFN-1×1-6L	12
MARCH 2024 – REV.A to REV.A.1	Page
Updated HBM value in Absolute Maximum Ratings section	2
Added UTDFN-1.45×1-6AL package	All
Changes from Original (JANUARY 2024) to REV.A	Page
Changed from product preview to production data	All

PACKAGE OUTLINE DIMENSIONS

SC70-6



RECOMMENDED LAND PATTERN (Unit: mm)

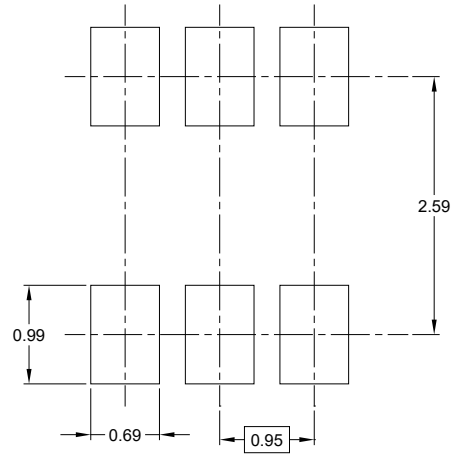
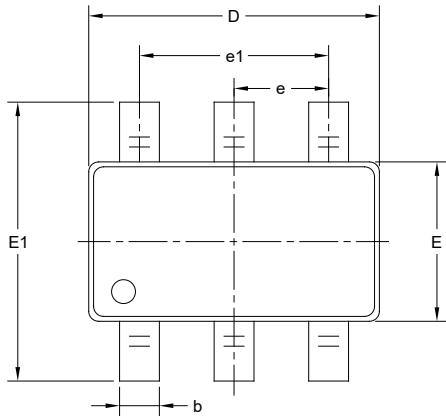


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.800	1.100	0.031	0.043
A1	0.000	0.100	0.000	0.004
A2	0.800	1.000	0.031	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.220	0.003	0.009
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.65 TYP		0.026 TYP	
e1	1.300 BSC		0.051 BSC	
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

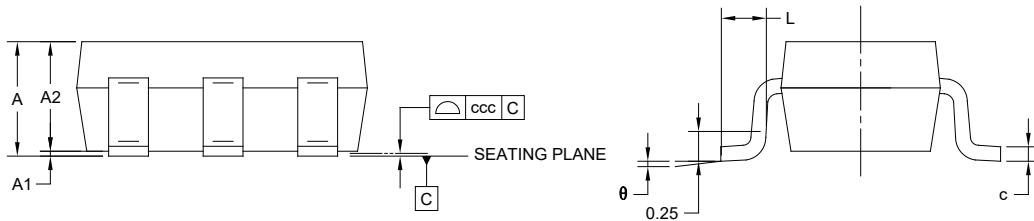
- NOTES:
 1. Body dimensions do not include mode flash or protrusion.
 2. This drawing is subject to change without notice.

PACKAGE OUTLINE DIMENSIONS

SOT-23-6



RECOMMENDED LAND PATTERN (Unit: mm)



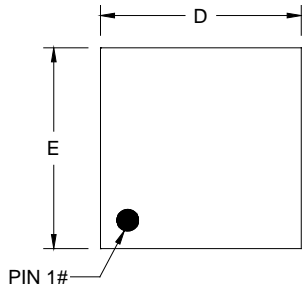
Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	-	-	1.450
A1	0.000	-	0.150
A2	0.900	-	1.300
b	0.300	-	0.500
c	0.080	-	0.220
D	2.750	-	3.050
E	1.450	-	1.750
E1	2.600	-	3.000
e	0.950 BSC		
e1	1.900 BSC		
L	0.300	-	0.600
θ	0°	-	8°
ccc	0.100		

NOTES:

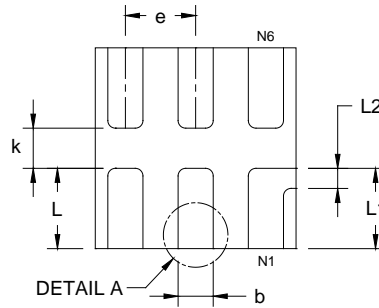
1. This drawing is subject to change without notice.
2. The dimensions do not include mold flashes, protrusions or gate burrs.
3. Reference JEDEC MO-178.

PACKAGE OUTLINE DIMENSIONS

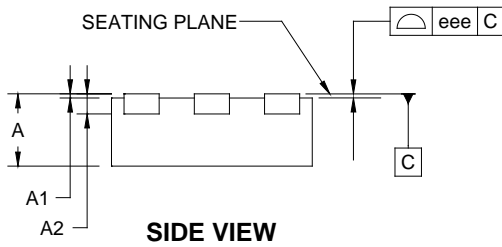
XTDFN-1x1-6L



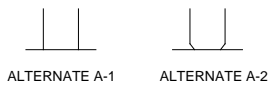
TOP VIEW



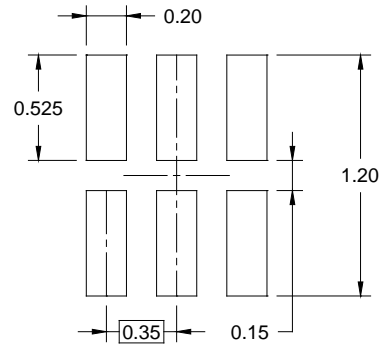
BOTTOM VIEW



SIDE VIEW



DETAIL A
ALTERNATE TERMINAL
CONSTRUCTION



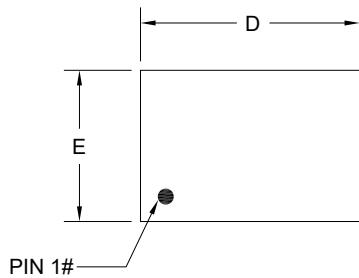
RECOMMENDED LAND PATTERN (Unit: mm)

Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	0.320	-	0.400
A1	0.000	-	0.050
A2	0.100 REF		
b	0.120	-	0.230
D	0.950	-	1.050
E	0.950	-	1.050
e	0.350 BSC		
k	0.150 MIN		
L	0.350	-	0.450
L1	0.350	-	0.450
L2	0.100 REF		
eee	0.050		

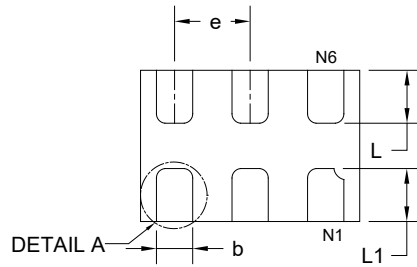
NOTE: This drawing is subject to change without notice.

PACKAGE OUTLINE DIMENSIONS

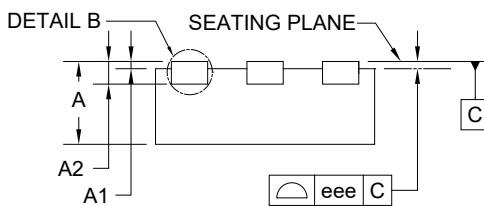
UTDFN-1.45×1-6AL



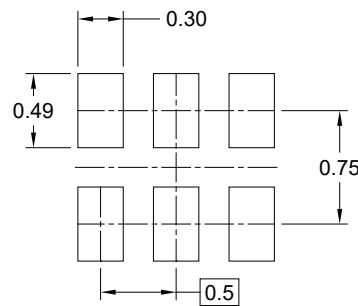
TOP VIEW



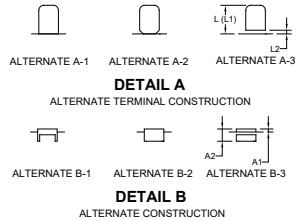
BOTTOM VIEW



SIDE VIEW



RECOMMENDED LAND PATTERN (Unit: mm)



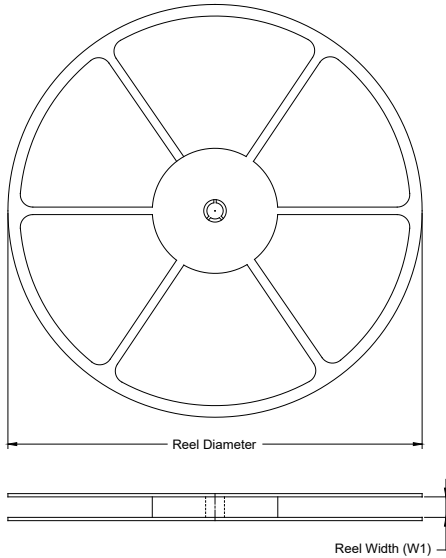
Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	0.450	-	0.600
A1	-0.004	-	0.050
A2	0.150 REF		
b	0.150	-	0.300
D	1.374	-	1.526
E	0.924	-	1.076
e	0.500 BSC		
L	0.250	-	0.450
L1	0.250	-	0.500
L2	0.000	-	0.100
eee	0.050		

NOTE: This drawing is subject to change without notice.

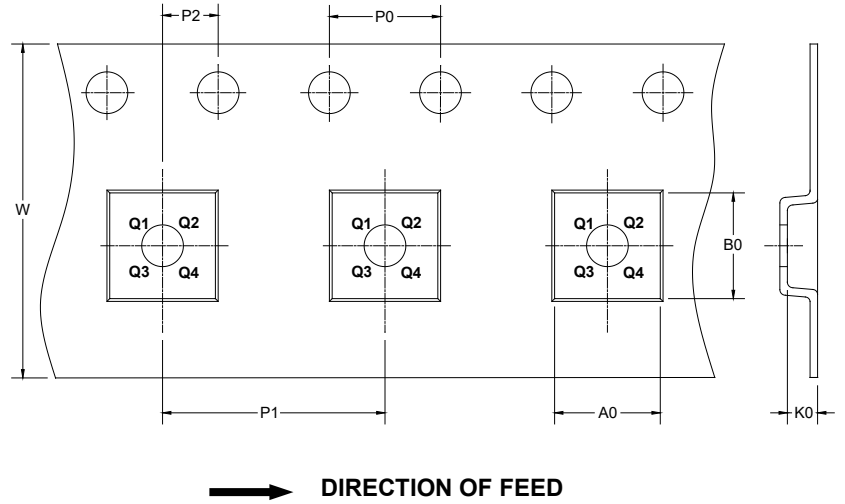
PACKAGE INFORMATION

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

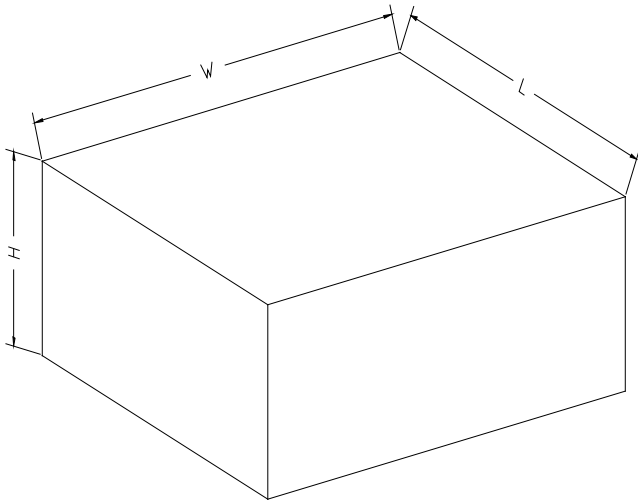
KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SC70-6	7"	9.5	2.40	2.50	1.20	4.0	4.0	2.0	8.0	Q3
SOT-23-6	7"	9.5	3.23	3.17	1.37	4.0	4.0	2.0	8.0	Q3
XTDFN-1×1-6L (74LVC2G17XXDM6G/TR)	7"	9.5	1.16	1.16	0.50	4.0	2.0	2.0	8.0	Q3
XTDFN-1×1-6L (74LVC2G17XXDM6SG/TR)	7"	9.5	1.15	1.17	0.56	4.0	4.0	2.0	8.0	Q3
UTDFN-1.45×1-6AL	7"	9.5	1.15	1.60	0.75	4.0	4.0	2.0	8.0	Q1

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PACKAGE INFORMATION

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18

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